

RoGUE: RDMA over Generic Unconverged Ethernet

Yanfang Le

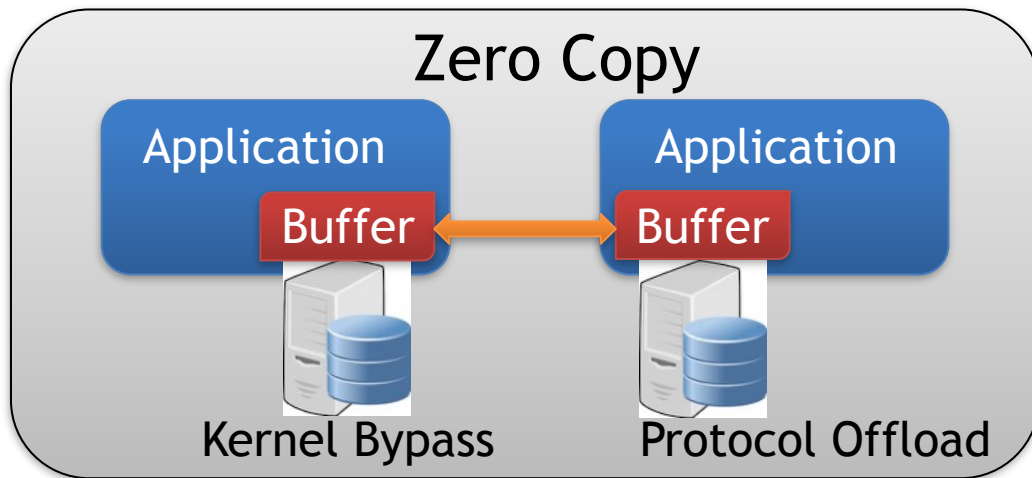
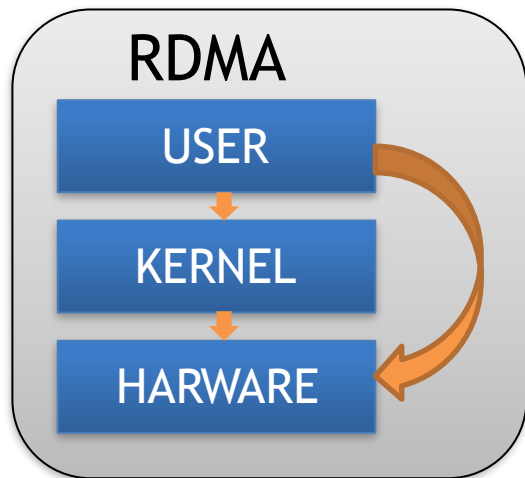
with Brent Stephens, Arjun Singhvi, Aditya Akella, Mike
Swift



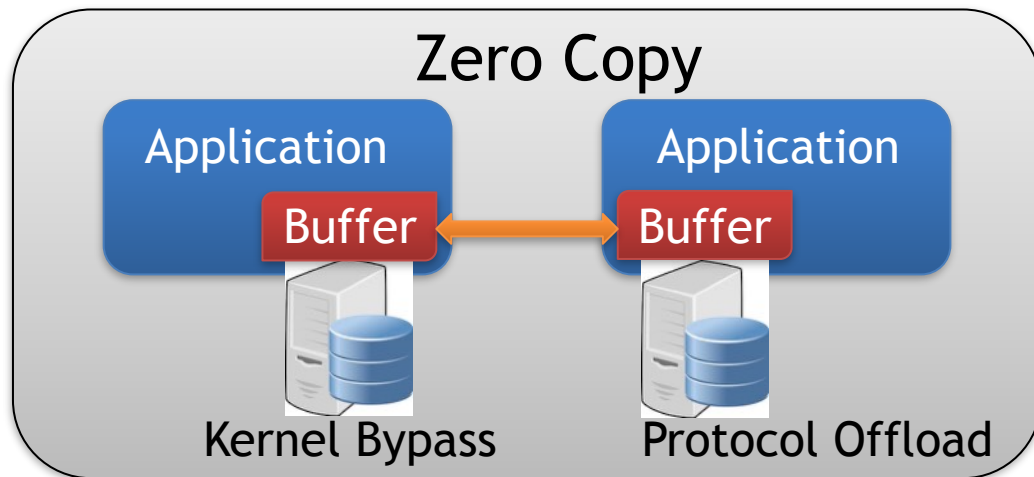
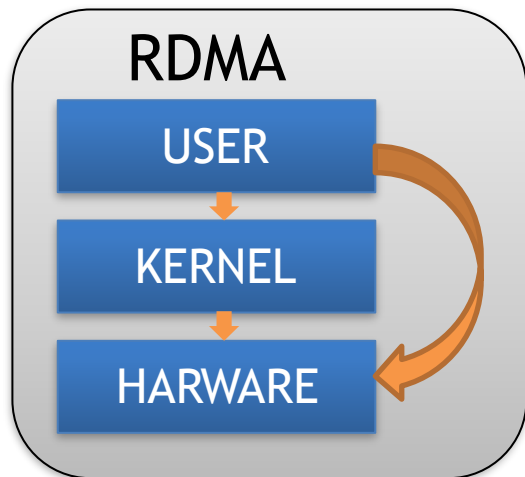
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RDMA Overview

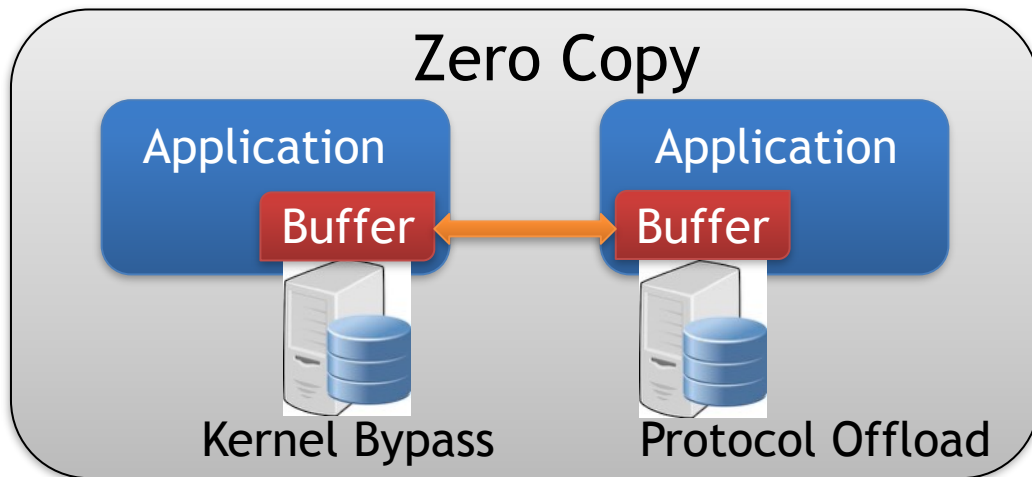
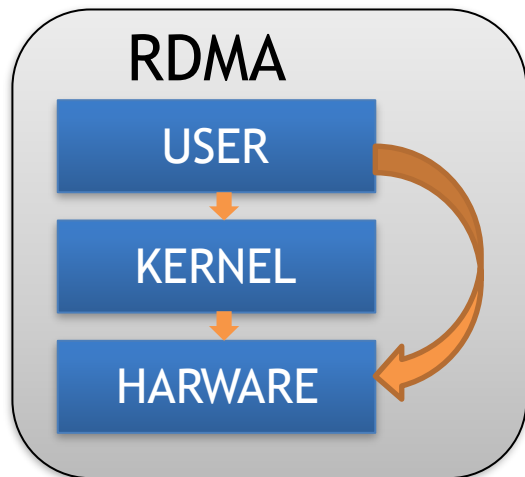


RDMA Overview



Low Latency, High throughput, Low CPU utilization

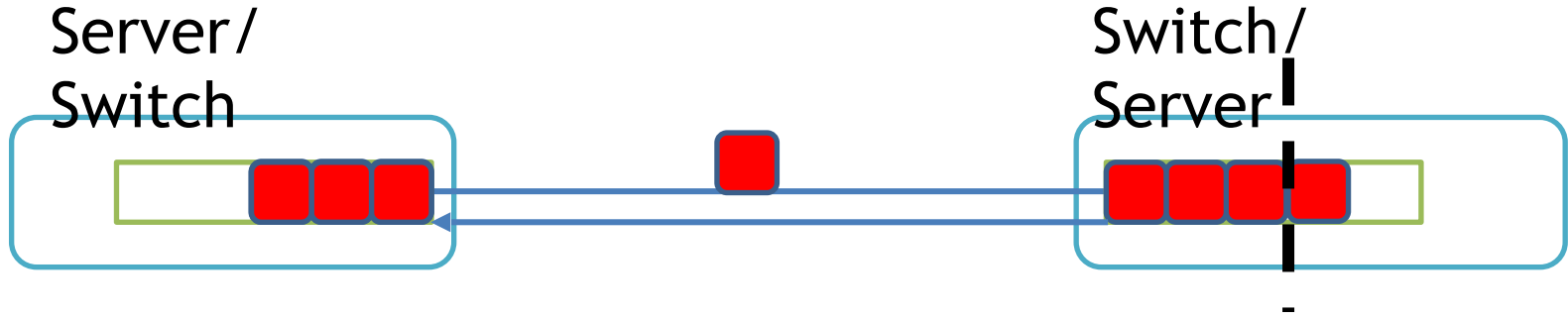
RDMA Overview



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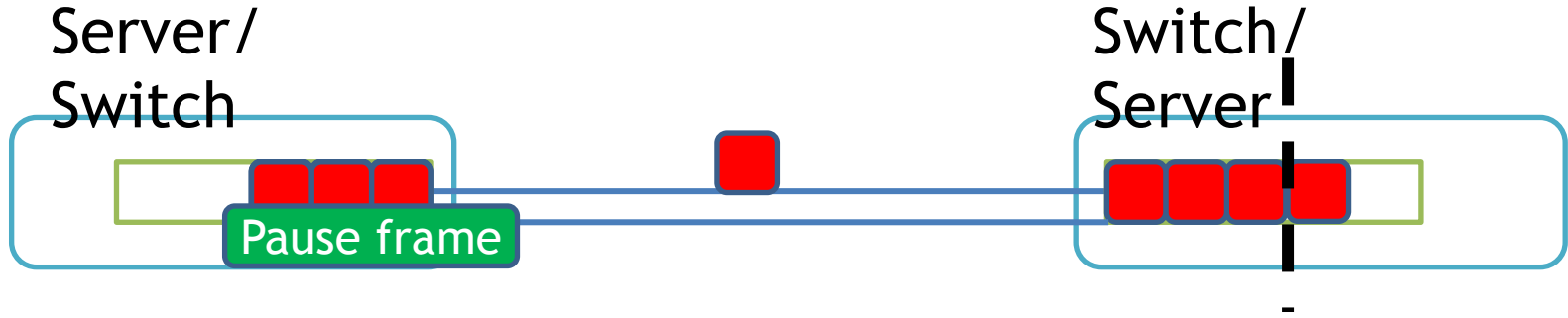
- RoCE: a protocol that provides RDMA over a **lossless** Ethernet network

Priority Flow Control



RoCE assumes Ethernet network to be lossless - achieved by enabling Priority Flow Control (PFC).

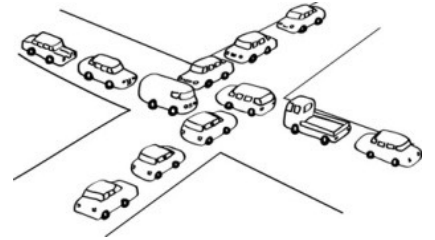
Priority Flow Control



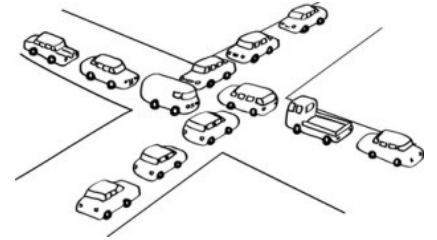
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Motivation

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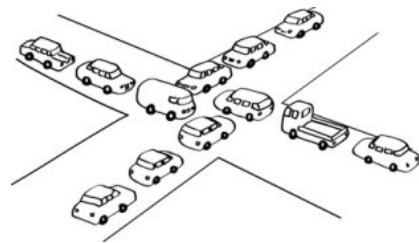


Motivation



HOL Blocking

Motivation

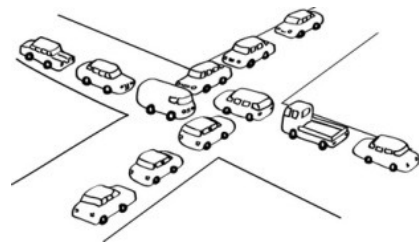


HOL Blocking

Unfairness

Motivation

- Data center providers are reluctant to enable PFC
 - Instead, isolate RDMA traffic and TCP traffic

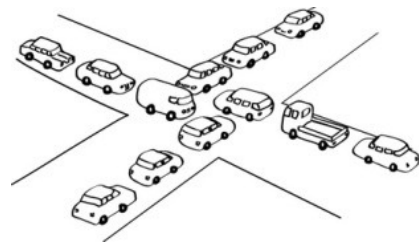


HOL Blocking

Unfairness

Motivation

- Data center providers are reluctant to enable PFC
 - Instead, isolate RDMA traffic and TCP traffic
- RDMA has not seen the uptake it deserves



HOL Blocking

Unfairness

Can we run RDMA over generic Ethernet network without any reliance on PFC ?

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RoCE + PFC

Congestion Control
No packet drop

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RoGUE

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RoGUE

Congestion Control
Retransmission

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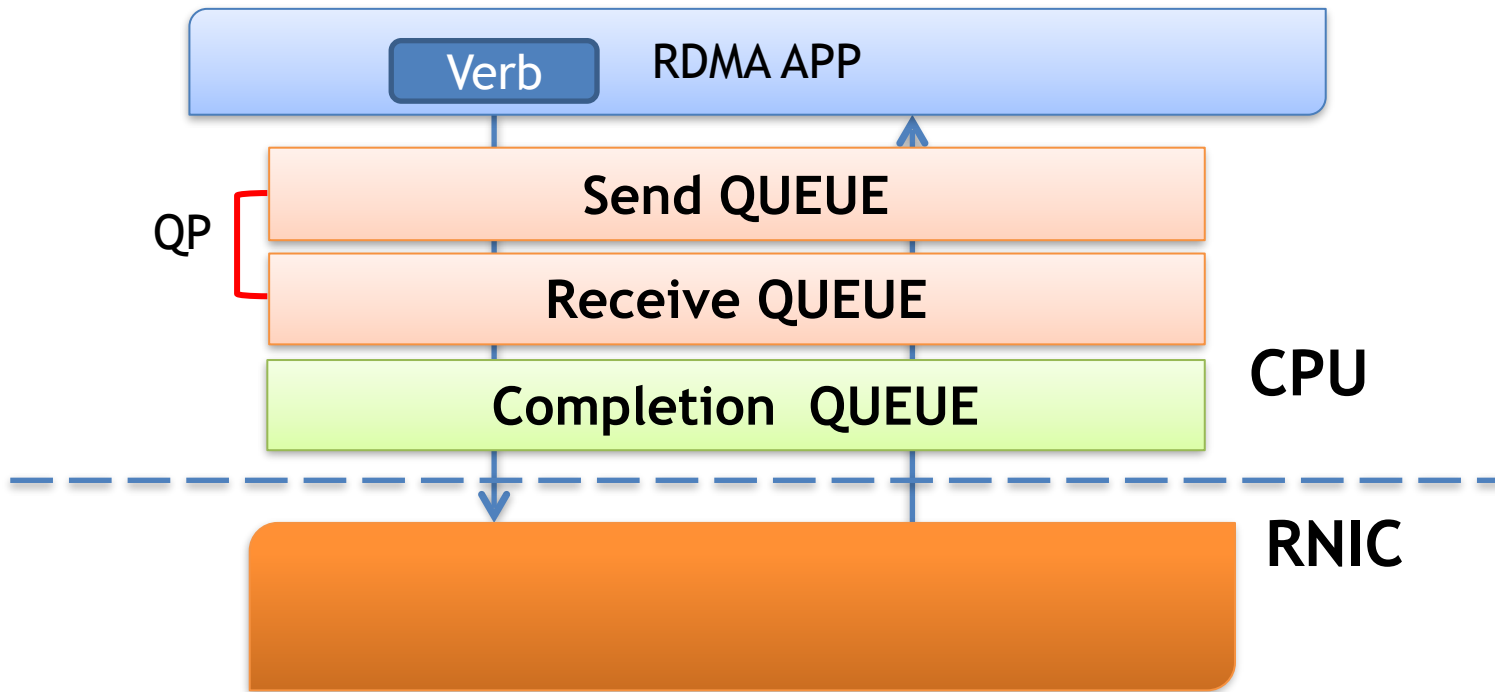
RoCE + PFC

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RoGUE

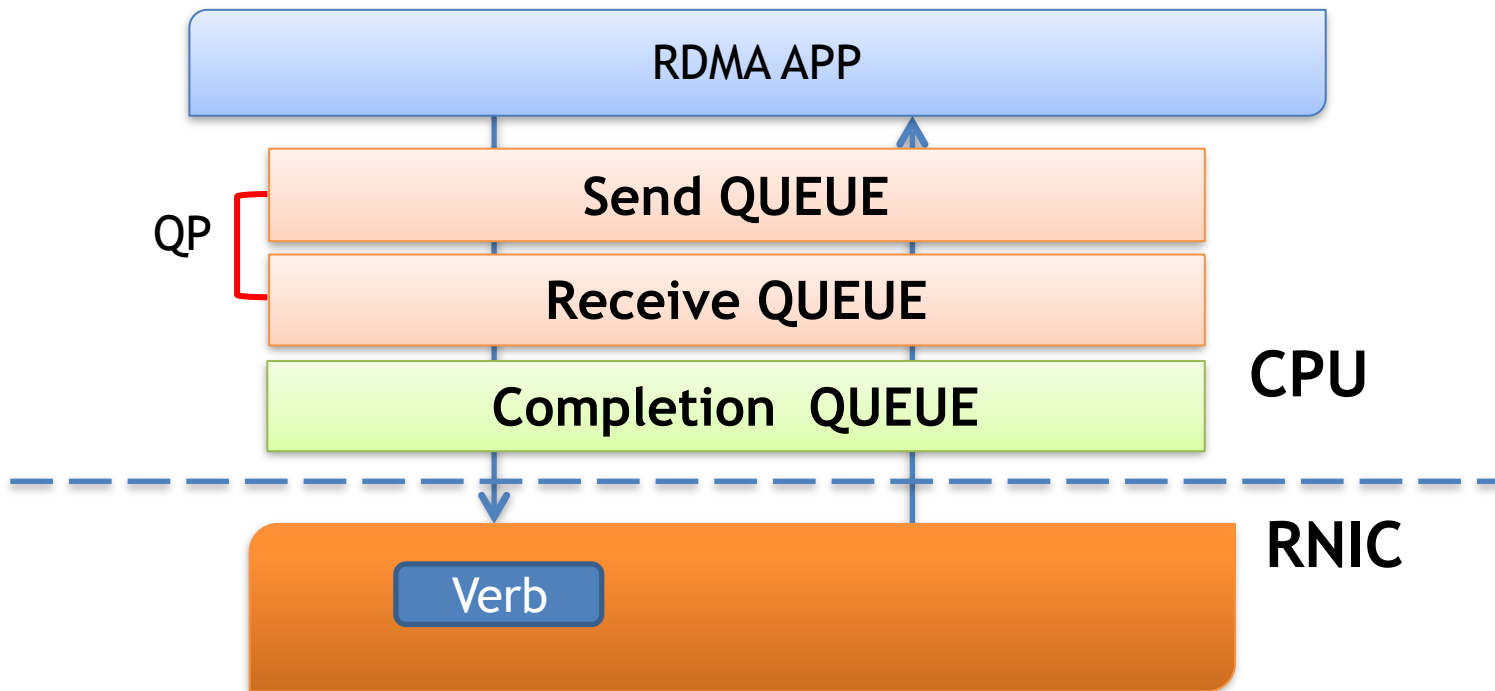
Congestion Control
Retransmission
yet retain low latency, CPU utilization

RoCE Overview



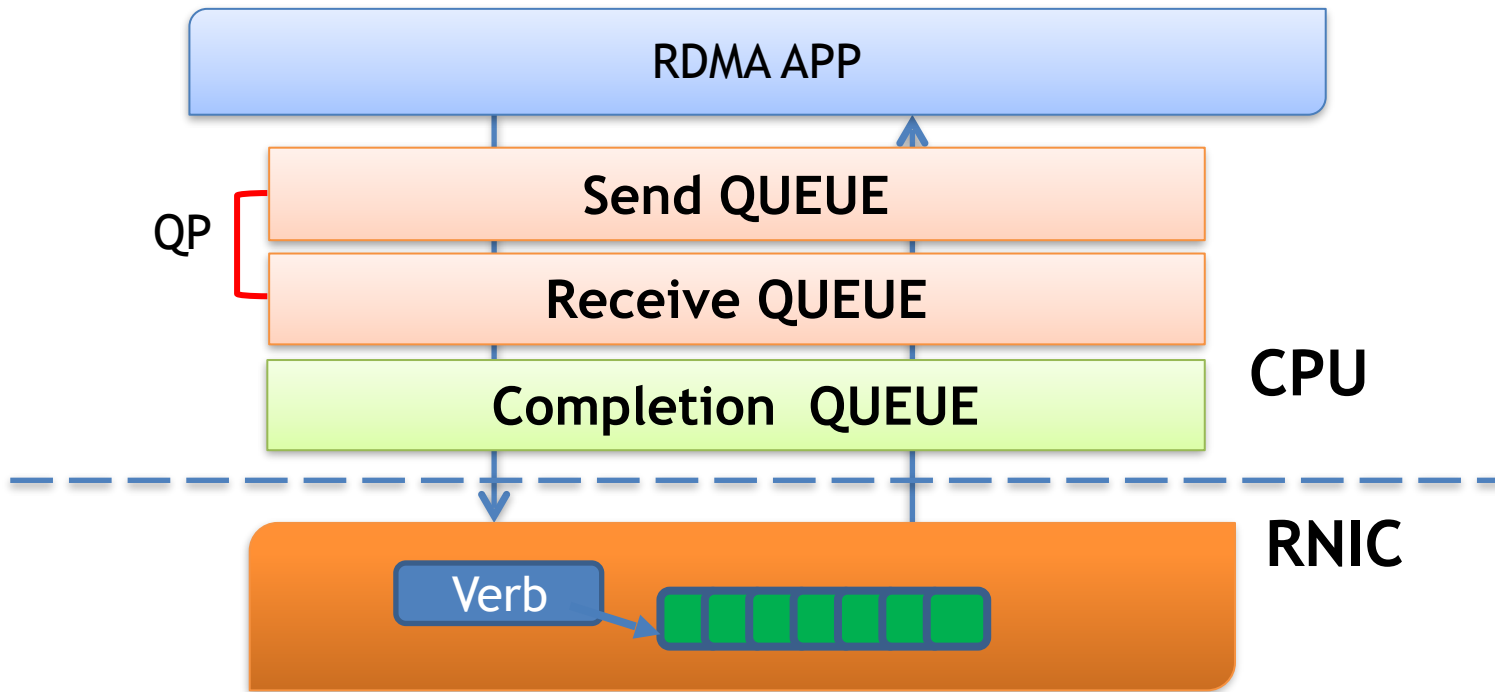
Brake the animations

RoCE Overview



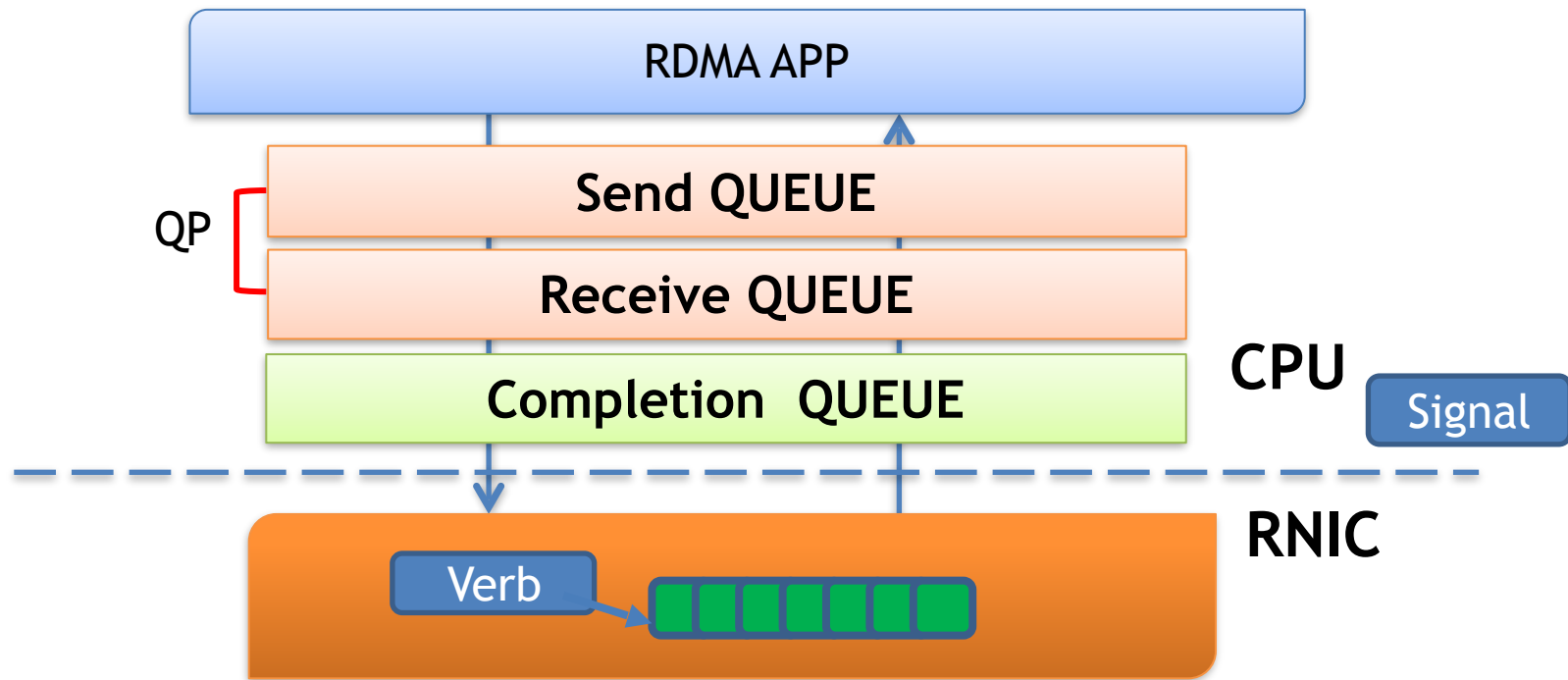
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RoCE Overview



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RoCE Overview



Brake the animations

Where to fix: HW or SW?

Hardware

- ✓ Low CPU utilization,
Low Latency
- ✗ It requires to work with
NIC vendor
- ✗ Heterogeneous network
hardware with non-
standard protocol
implementation
- ✗ Complicates network
evolution

Software

- ✓ Easy to implement
- ✗ Packet level congestion
signals are unavailable
- ✗ High CPU utilization if per-
packet operations

RoGUE Overview

Congestion Control

Loss Recovery

CPU

RNIC

RoGUE Overview

Congestion Control

Loss Recovery

Congestion Control loop

CPU-efficient
segmenting

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CPU

Hardware timestamp to measure RTT

Hardware rate limiter to pace
packets

RNIC

RoGUE Overview

Congestion Control

Loss Recovery

Congestion Control loop

Shadow Queue Pair

CPU-efficient
segmenting

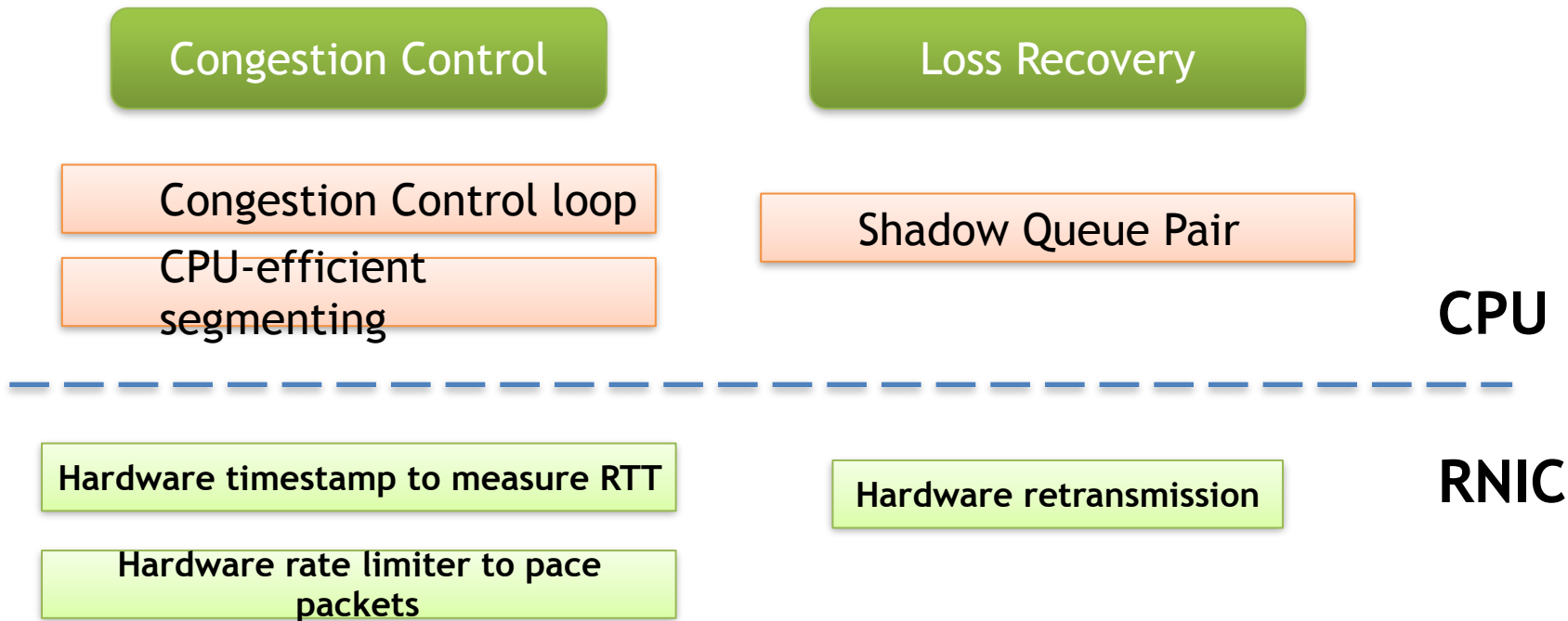
CPU

Hardware timestamp to measure RTT

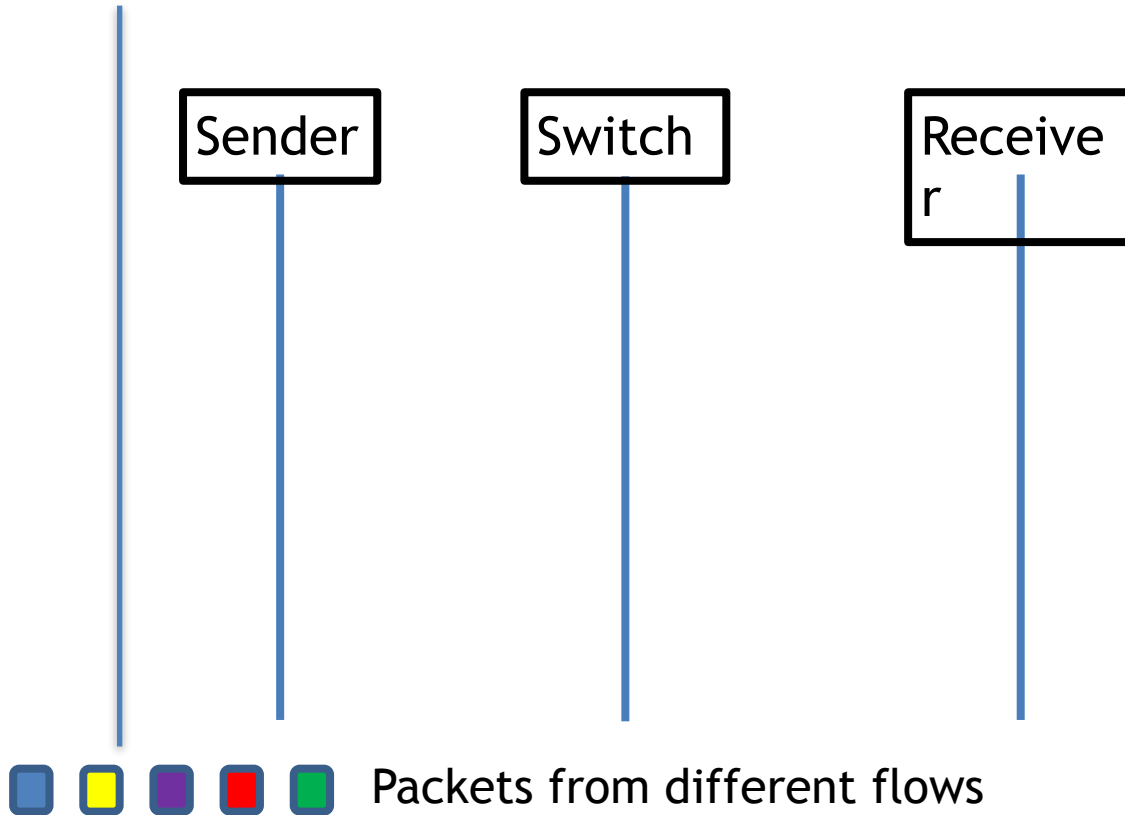
RNIC

Hardware rate limiter to pace
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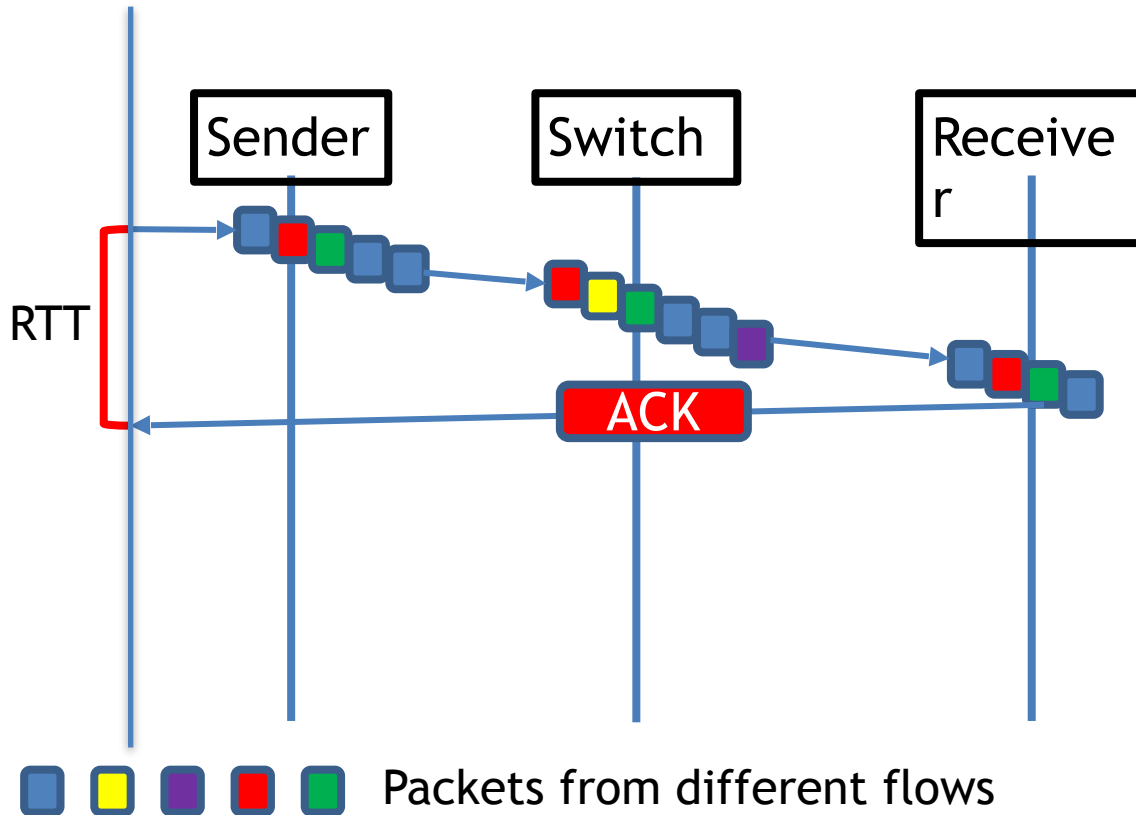
RoGUE Overview



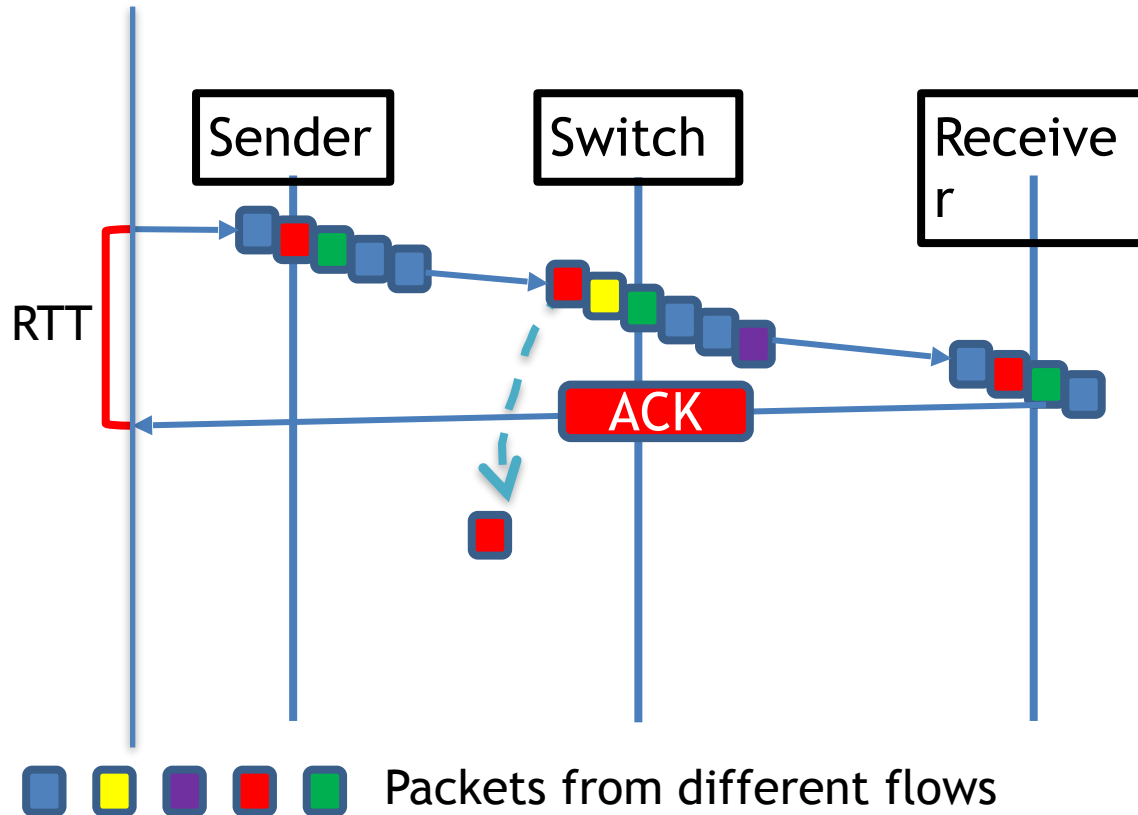
Congestion Signal



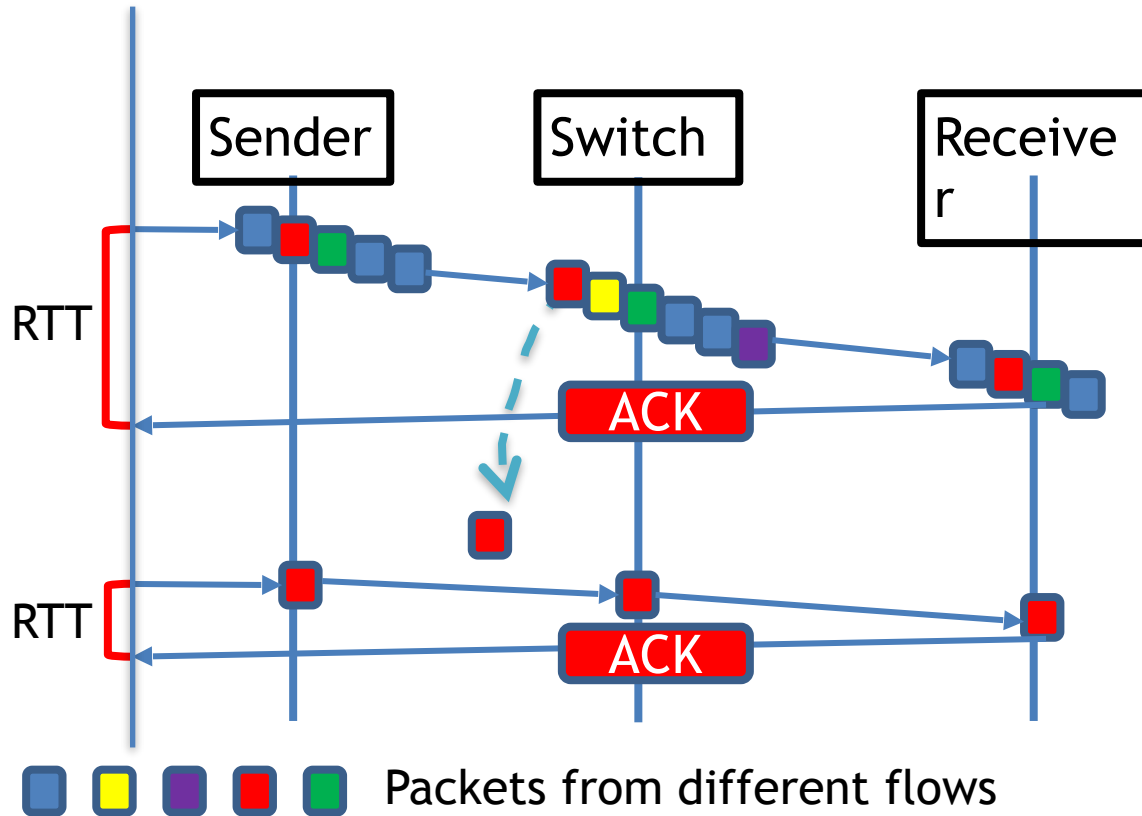
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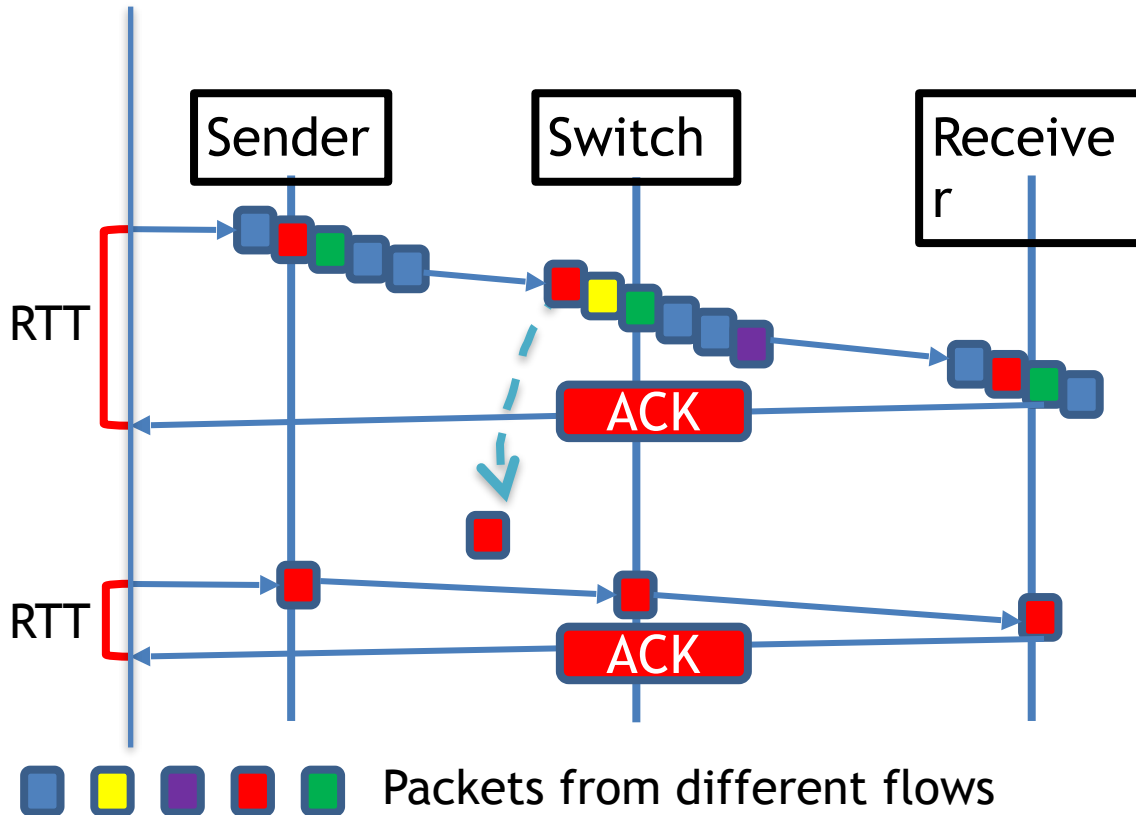
Congestion Signal



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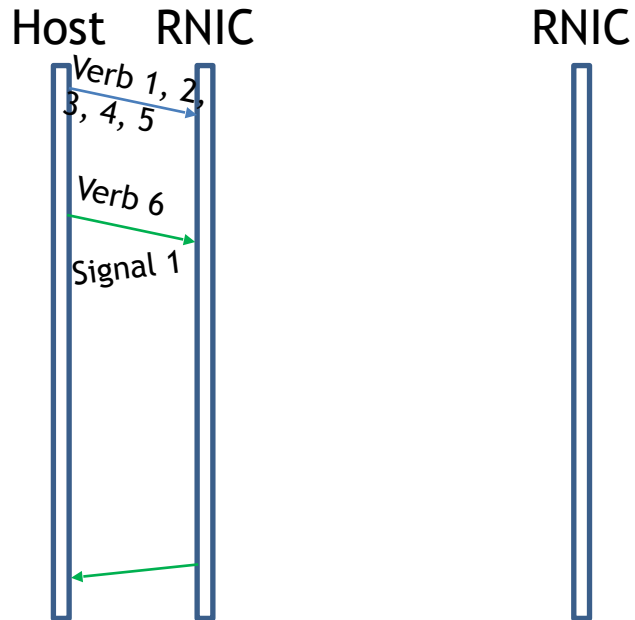
Congestion Signal



- RTT is high, the queue builds up, reduce the sending rate
- RTT is low, network is idle, increase the sending rate

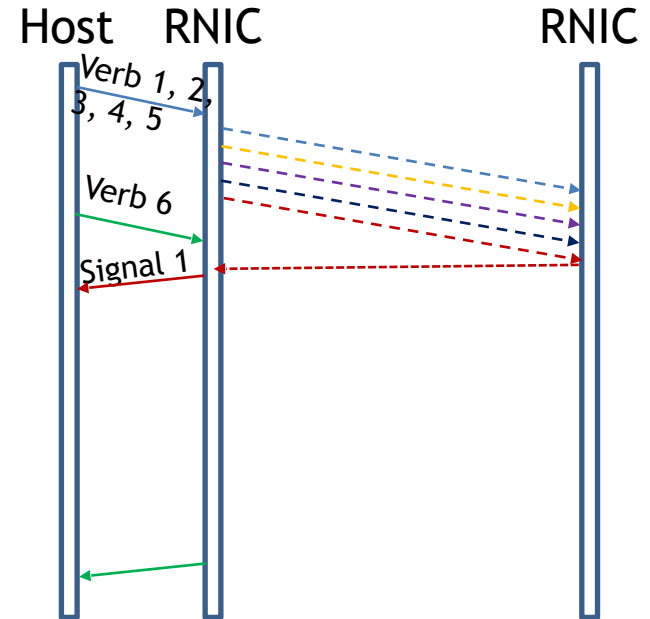
CPU Efficient Segmenting

- Two key questions
 - How large a verb should RoGUE send?
 - How often should the RNIC signaled?
- Small Verb (< 64KB)
 - signal every 64KB
 - CPU utilization (< 20%)
- Large Verb (≥ 64 KB)
 - chunk, and signal every 64KB.
 - CPU utilization (< 10%)



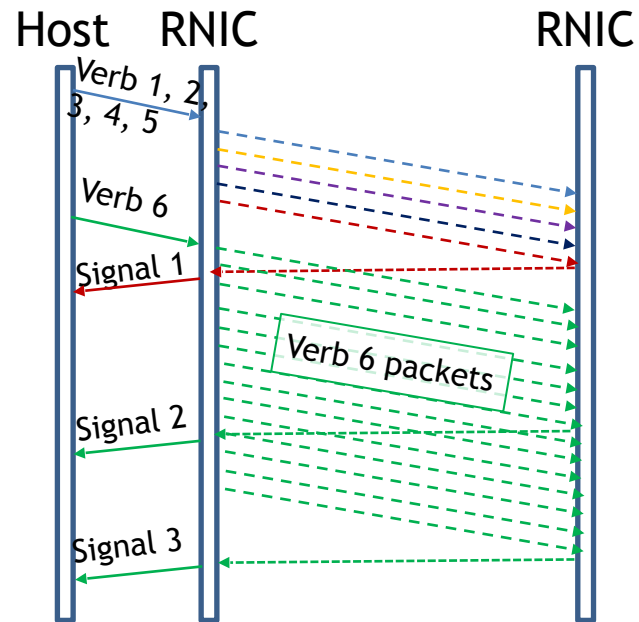
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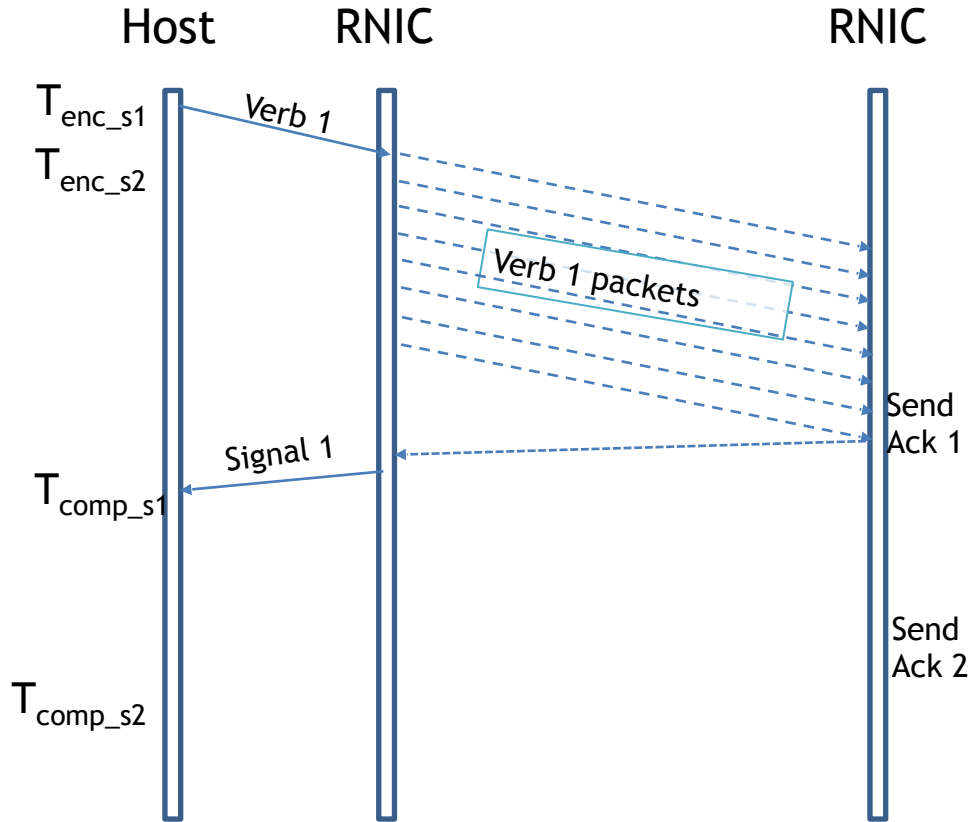


CPU Efficient Segmenting

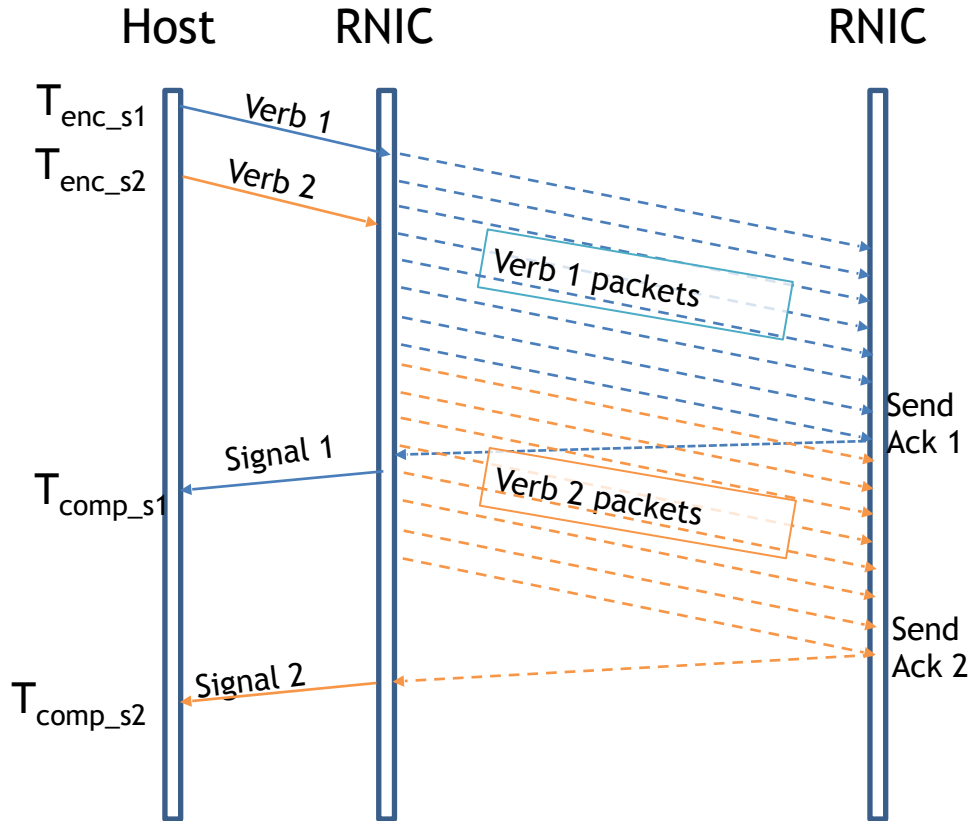
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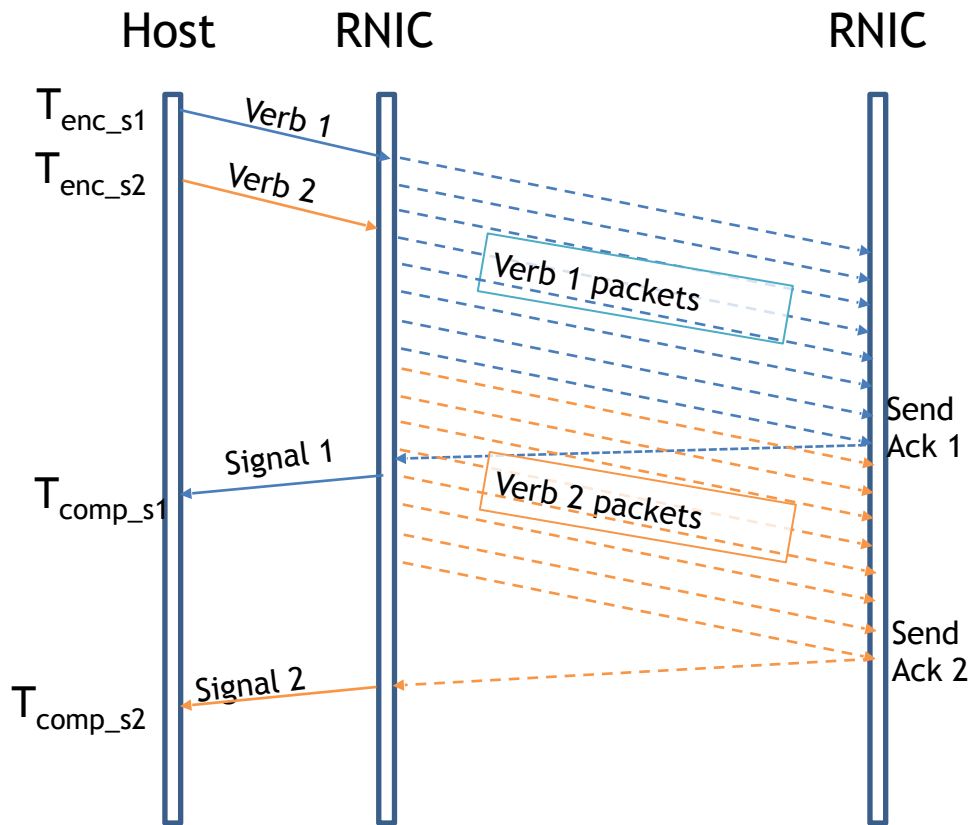
RTT measurement



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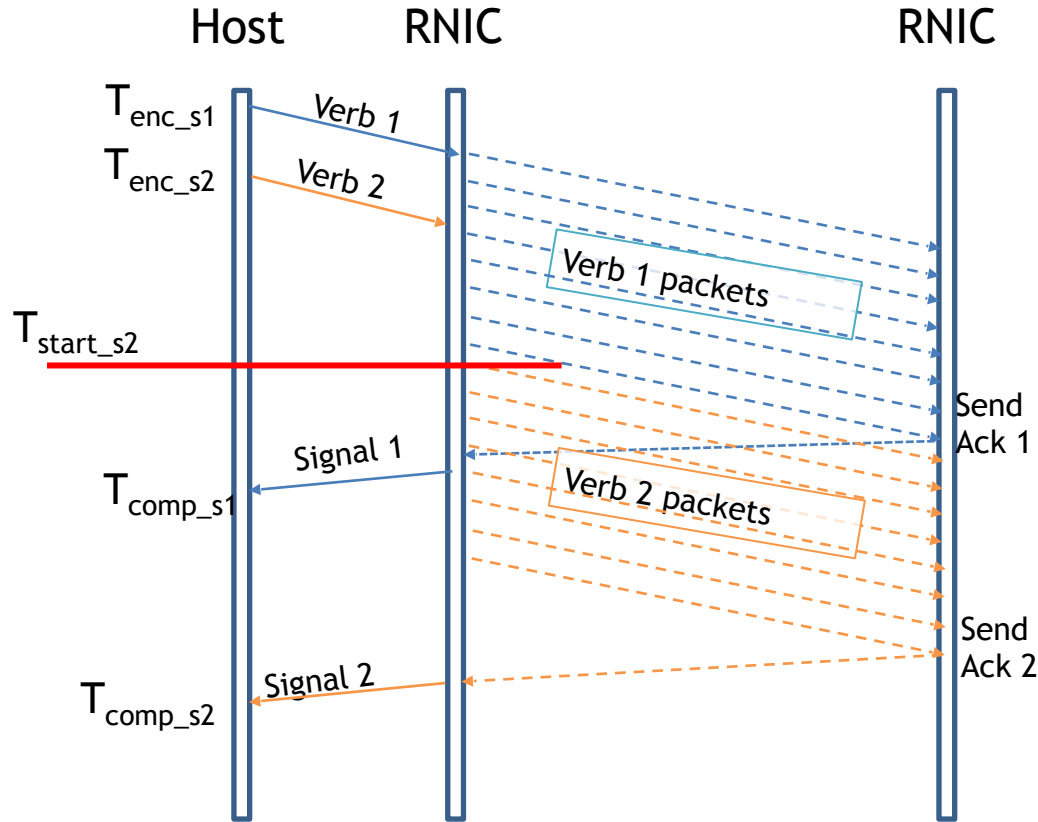


RTT measurement



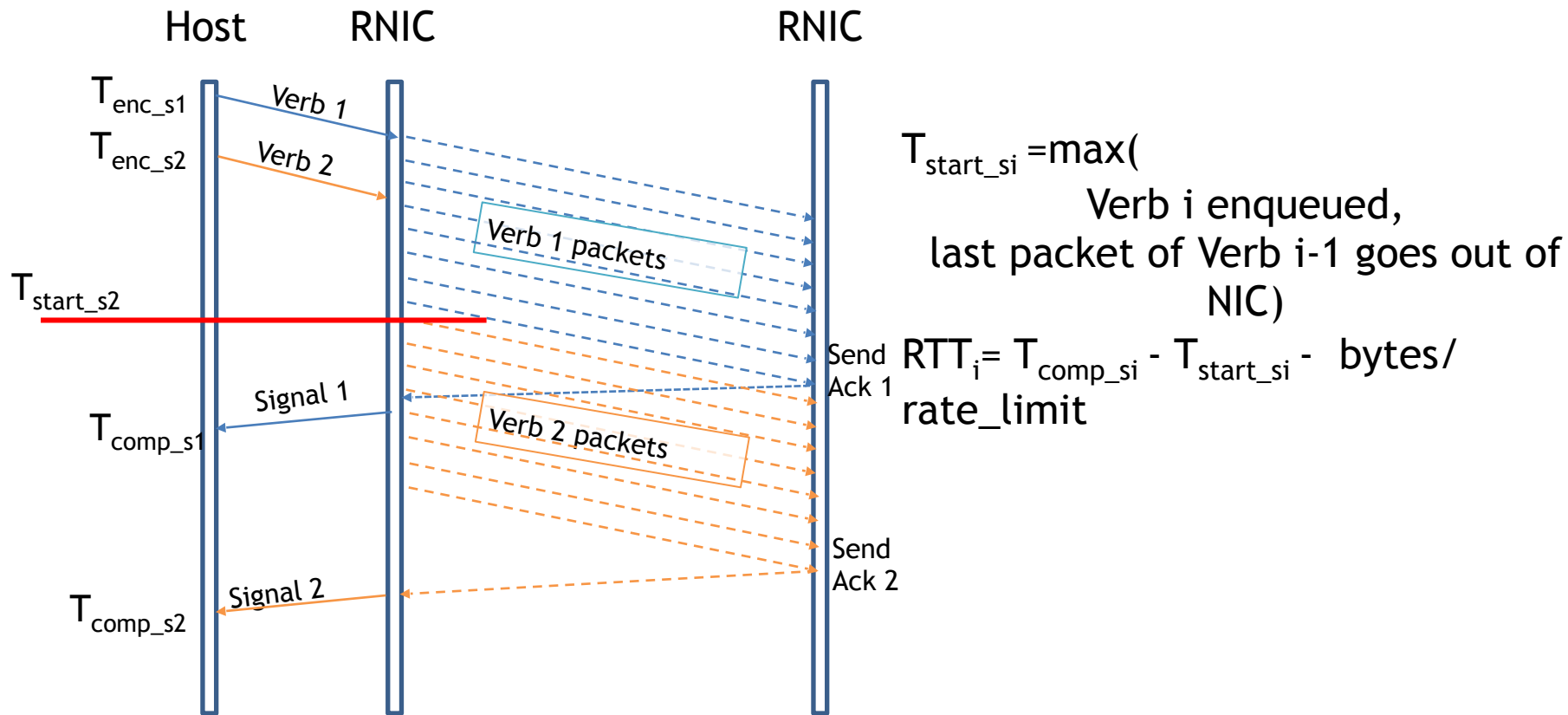
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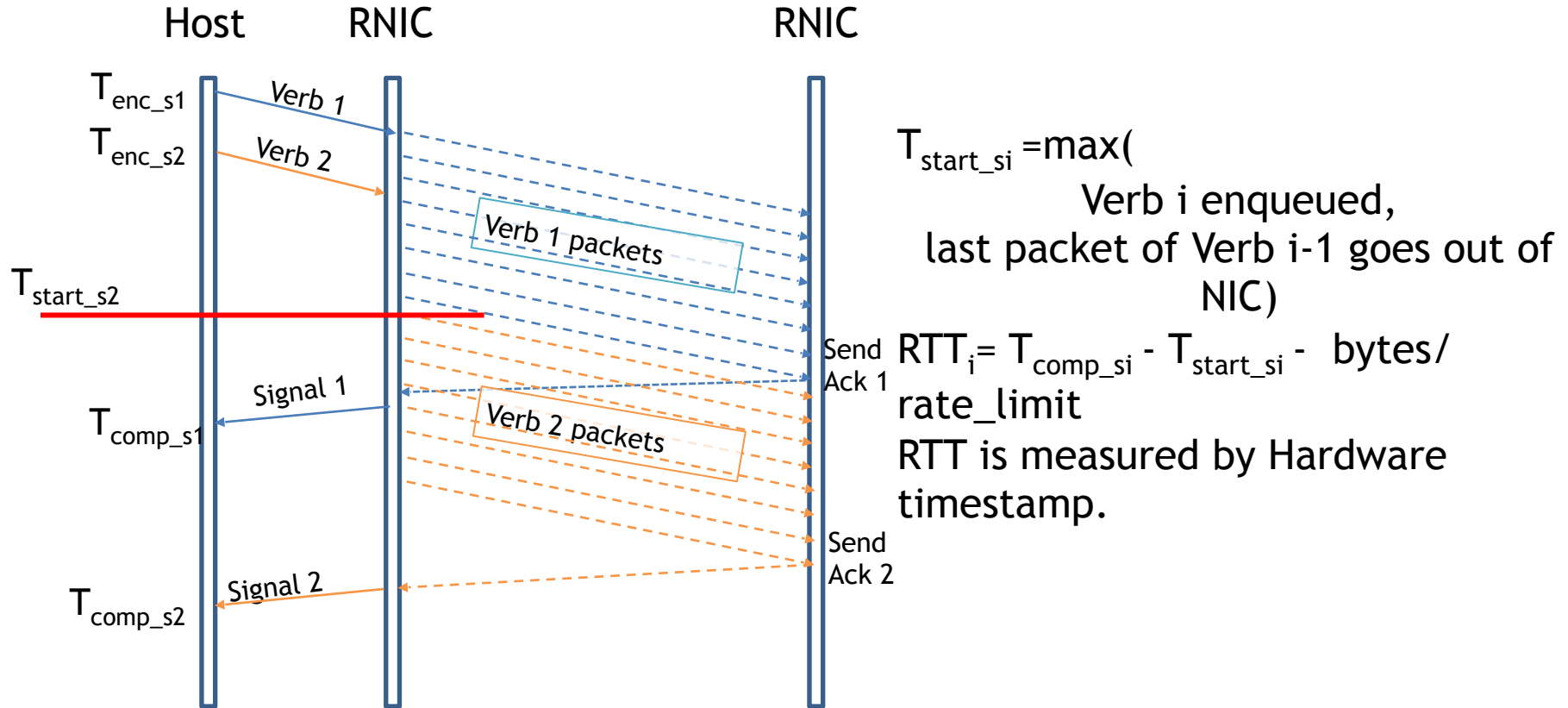


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Congestion Response

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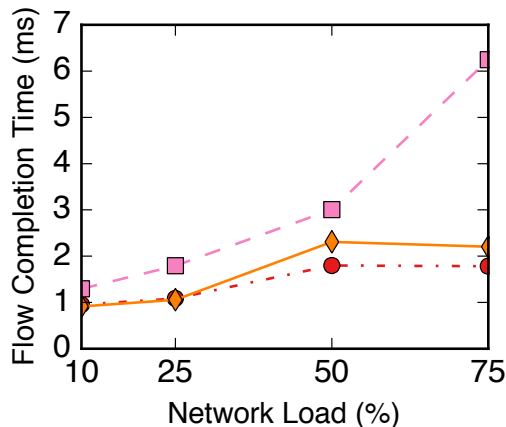
- Similar to TCP Vegas, and Timely
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- Rate limiter is offloaded to RNIC

Evaluation

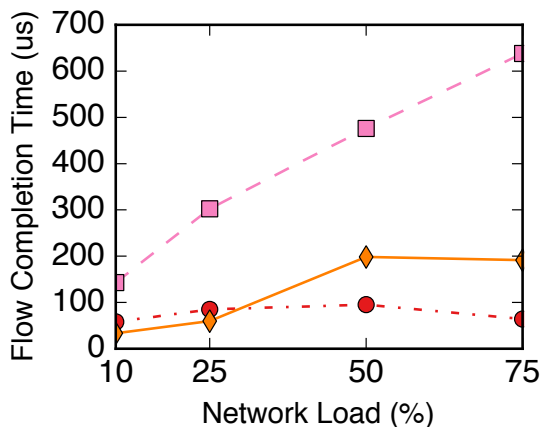
- Mellanox ConnectX-3 Pro 10Gbps RNICs, DCQCN
- Baselines: DCTCP, DCQCN

Evaluation-Cluster Experiments

- Each of 16 hosts generates 1MB RPC for random destinations and send 1KB RPC once every ten 1MB RPC

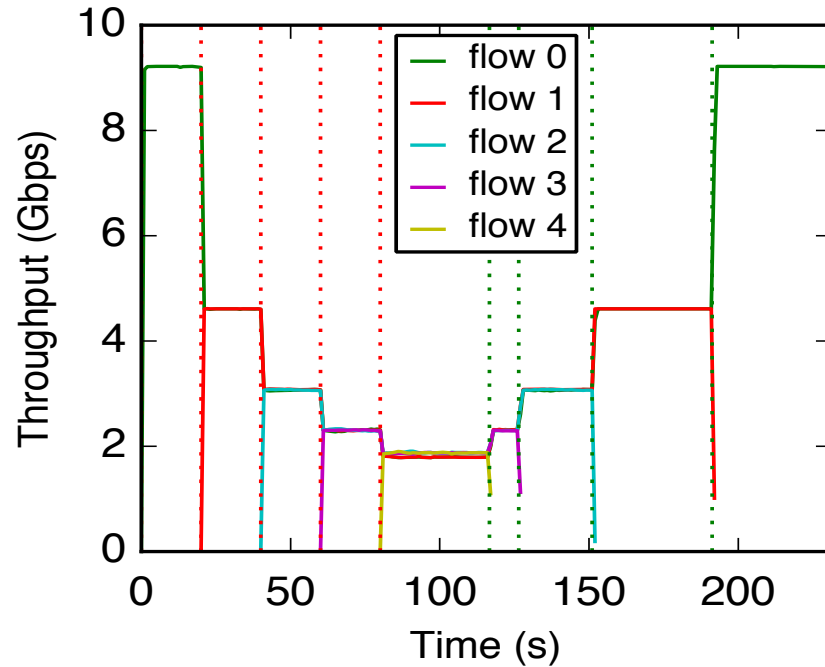


(a) Large RPCs (1MB) - Median FCT

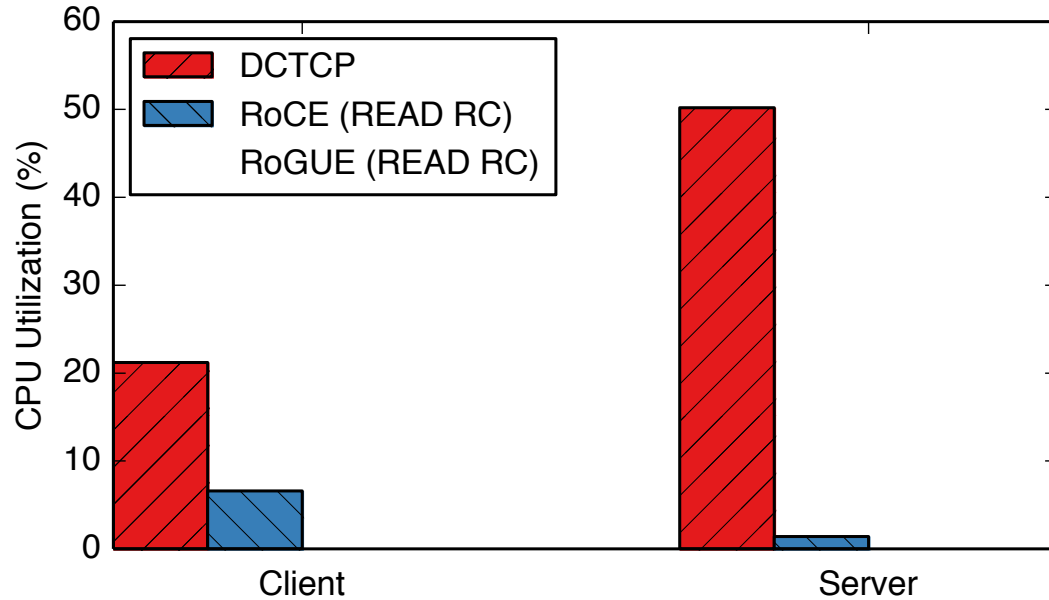


(b) Small RPCs (1KB) - 90th %ile FCT

Evaluation-Congestion Response



Evaluation-CPU Utilization



Summary

- It is possible to support RoCE without relying on PFC
- Judicious division of labor between SW and HW to do the congestion control and retransmission, yet retain a low CPU utilization
- RoGUE supports RC and UC transport types of CC
- Evaluation results validate that RoGUE has competitive performance with native RoCE

