

A semantics for concurrent separation logic

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THANKS

- Peter O'Hearn
- John Reynolds
- Josh Berdine

LANGUAGE

- Concurrent programs
 - shared mutable data
- Resources
 - mutual exclusion
- Synchronization
 - conditional critical regions

Hoare, Owicki/Gries

Race conditions

cause unpredictable behavior

- Concurrent write to shared variable
- Concurrent update/disposal of heap cell



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dangling pointer

SEMANTIC FRAMEWORK

- A process denotes a set of *action traces*
 - *actions have effect on state*
- Traces describe *interactive computations*
 - *fair interleaving*
 - *mutually exclusive resources*
 - *treat race condition as disaster*

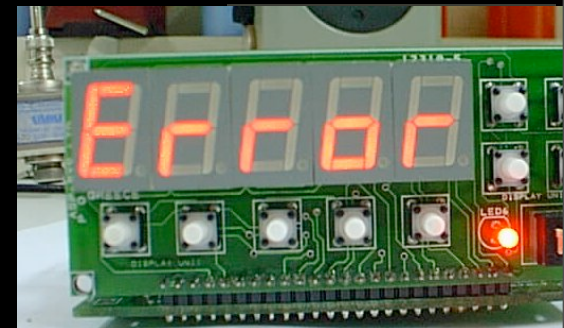
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ISSUES

concurrency + pointers

- Race conditions
 - unpredictable behavior
 - not statically detectable
- Partial and total correctness
- Safety and liveness properties
- Deadlock

ACTIONS

- δ idle
- $i=v, i:=v$ read, write
- $[v]=v', [v]:=v'$ lookup, update
- $alloc(v, L), disp(v)$ allocate, dispose
- $try(r), acq(r), rel(r)$ try, acquire, release
- $abort$ runtime error



ACTIONS

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λ ranges over actions



TRACES

- sequences of actions
 - finite or infinite
- concatenation
 - $\alpha \delta \beta = \alpha \beta$
 - $\alpha \text{ abort } \beta = \alpha \text{ abort}$



TRACES

- sequences of actions
 - finite or infinite
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 - $\alpha \text{ abort } \beta = \alpha \text{ abort}$

α, β range over traces

Tr is the set of all traces



CONCATENATION

$$T_1 T_2 = \{\alpha_1 \alpha_2 \mid \alpha_1 \in T_1 \ \& \ \alpha_2 \in T_2\}$$

ITERATION

$$T^* = \bigcup_{n=0}^{\infty} T^n$$

$$T^\infty = T^* \cup T^\omega$$

TRACE SEMANTICS

- Integer expressions

$$\llbracket e \rrbracket \subseteq \mathbf{Tr} \times V$$

- Boolean expressions

$$\llbracket b \rrbracket_{\text{true}}, \llbracket b \rrbracket_{\text{false}} \subseteq \mathbf{Tr}$$

- List expressions

$$\llbracket E \rrbracket \subseteq \mathbf{Tr} \times V^*$$

- Commands

$$\llbracket c \rrbracket \subseteq \mathbf{Tr}$$



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- Commands

$$\llbracket c \rrbracket \subseteq \mathbf{Tr}$$

defined denotationally



SEMANTIC DEFINITIONS

$$\llbracket \text{skip} \rrbracket = \{\delta\}$$

$$\llbracket i := e \rrbracket = \{\rho i := v \mid (\rho, v) \in \llbracket e \rrbracket\}$$

$$\llbracket c_1; c_2 \rrbracket = \llbracket c_1 \rrbracket \llbracket c_2 \rrbracket$$

$$\llbracket \text{if } b \text{ then } c_1 \text{ else } c_2 \rrbracket = \llbracket b \rrbracket_{\text{true}} \llbracket c_1 \rrbracket \cup \llbracket b \rrbracket_{\text{false}} \llbracket c_2 \rrbracket$$

$$\llbracket \text{while } b \text{ do } c \rrbracket = (\llbracket b \rrbracket_{\text{true}} \llbracket c \rrbracket)^* \llbracket b \rrbracket_{\text{false}} \cup (\llbracket b \rrbracket_{\text{true}} \llbracket c \rrbracket)^\omega$$

SEMANTIC DEFINITIONS

$$\llbracket i := [e] \rrbracket = \{ \rho \ [v] = v' \ i := v' \mid (\rho, v) \in \llbracket e \rrbracket \}$$

$$\llbracket i := \mathbf{cons} \ E \rrbracket = \{ \rho \ \mathit{alloc}(l, L) \ i := l \mid (\rho, L) \in \llbracket E \rrbracket \}$$

$$\llbracket [e] := e' \rrbracket = \{ \rho \ \rho' \ [v] := v' \mid (\rho, v) \in \llbracket e \rrbracket \ \& \ (\rho', v') \in \llbracket e' \rrbracket \}$$

$$\llbracket \mathbf{dispose}(e) \rrbracket = \{ \rho \ \mathit{disp}(l) \mid (\rho, l) \in \llbracket e \rrbracket \}$$

REGION

$$\llbracket \text{with } r \text{ when } b \text{ do } c \rrbracket = \text{wait}^* \text{ enter} \cup \text{wait}^\omega$$

where

$$\text{wait} = \{ \text{try}(r) \} \cup \text{acq}(r) \llbracket b \rrbracket_{\text{false}} \text{rel}(r)$$

$$\text{enter} = \text{acq}(r) \llbracket b \rrbracket_{\text{true}} \llbracket c \rrbracket \text{rel}(r)$$



LOCAL VARIABLES

$$\llbracket \text{local } i = e \text{ in } c \rrbracket = \{ \rho(\alpha \setminus i) \mid (\rho, v) \in \llbracket e \rrbracket \ \& \ \alpha \in \llbracket c \rrbracket_{i=v} \}$$

α sequential for i

$\dots i=v \dots i=v \ i:=v' \dots i=v' \dots$

local actions hidden in $\alpha \setminus i$

$i=0 \ x=1 \ i:=2 \ x:=2$



$x=1 \ x:=2$

SEQUENTIAL TRACES

assume no interference

- sequential for i
 - each read yields the most recently written value
- sequential for r
 - available at start
 - acquire before release

LOCAL RESOURCE

$$\llbracket \text{resource } r \text{ in } c \rrbracket = \{ \alpha \setminus r \mid \alpha \in \llbracket c \rrbracket_r \}$$

α sequential for r

local actions hidden in $\alpha \setminus r$

PARALLEL

$$[[c_1 || c_2]] = [[c_1]]\{\}\{\}\{c_2\}$$

- each process starts with no resources
- resources are mutually exclusive
- a race is an error

*mutex fairmerge
with race detection*



Parallel composition

- what each process can do depends on its resources and those of its environment

$$(A_1, A_2) \xrightarrow{\lambda} (A'_1, A_2)$$

- a race is interpreted as an error

$$\lambda_1 \bowtie \lambda_2$$

Mutex constraint

$$(A_1, A_2) \xrightarrow{\lambda} (A'_1, A_2)$$

Definition

$$(A_1, A_2) \xrightarrow{acq(r)} (A_1 \cup \{r\}, A_2) \quad \text{if } r \notin A_1 \cup A_2$$

$$(A_1, A_2) \xrightarrow{rel(r)} (A_1 - \{r\}, A_2) \quad \text{if } r \in A_1$$

$$(A_1, A_2) \xrightarrow{\lambda} (A_1, A_2) \quad \lambda \neq acq(r), rel(r)$$

process with resources A_1 can do λ
in environment holding A_2



Interfering actions

Definition

$$\begin{aligned} \lambda_1 \bowtie \lambda_2 \\ \text{iff} \\ \textit{free}(\lambda_1) \cap \textit{writes}(\lambda_2) \neq \{\} \\ \text{or} \\ \textit{free}(\lambda_2) \cap \textit{writes}(\lambda_1) \neq \{\} \end{aligned}$$

concurrent read/write
concurrent write/write
to store or heap



Mutex fairmerge

$$\alpha_{1A_1} \parallel_{A_2} \alpha_2$$

Inductive definition

$$\begin{aligned} \alpha_{A_1} \parallel_{A_2} \epsilon &= \{ \alpha \mid (A_1, A_2) \xrightarrow{\alpha} \cdot \} \\ \epsilon_{A_1} \parallel_{A_2} \alpha &= \{ \alpha \mid (A_2, A_1) \xrightarrow{\alpha} \cdot \} \end{aligned}$$

$$\begin{aligned} (\lambda_1 \alpha_1)_{A_1} \parallel_{A_2} (\lambda_2 \alpha_2) &= \{ abort \} \quad \text{if } \lambda_1 \bowtie \lambda_2 \\ &= \{ \lambda_1 \beta \mid (A_1, A_2) \xrightarrow{\lambda_1} (A'_1, A_2) \ \& \ \beta \in \alpha_{1A'_1} \parallel_{A_2} (\lambda_2 \alpha_2) \} \\ &\cup \{ \lambda_2 \beta \mid (A_2, A_1) \xrightarrow{\lambda_2} (A'_2, A_1) \ \& \ \beta \in (\lambda_1 \alpha_1)_{A_1} \parallel_{A'_2} \alpha_2 \} \\ &\quad \text{otherwise} \end{aligned}$$

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READS & WRITES

$$\text{writes}([v]=v') = \{\}$$

$$\text{writes}([v]:=v') = \{v\}$$

$$\text{writes}(\text{alloc}(v, [v_0, \dots, v_n])) = \{v, \dots, v + n\}$$

$$\text{writes}(\text{disp}(v)) = \{v\}$$

$$\text{writes}(i=v) = \{\}$$

$$\text{writes}(i:=v) = \{i\}$$

$$\text{free}([v]=v') = \{v\}$$

$$\text{free}([v]:=v') = \{v\}$$

$$\text{free}(\text{alloc}(v, [v_0, \dots, v_n])) = \{v, \dots, v + n\}$$

$$\text{free}(\text{disp}(v)) = \{v\}$$

$$\text{free}(i=v) = \{i\}$$

$$\text{free}(i:=v) = \{i\}$$

EXAMPLES

$$\llbracket x:=1 \parallel y:=1 \rrbracket = \{x:=1 y:=1, y:=1 x:=1\}$$

$$\llbracket x:=1 \parallel x:=1 \rrbracket = \{abort\}$$

$$\llbracket x:=x+1 \parallel x:=x+1 \rrbracket = \{x=v abort \mid v \in V\}$$



PUT

with *buf* **when** $\neg full$ **do**
 $(c := x; full := \mathbf{true})$

Typical trace

$acq(buf) \text{ full} = \mathbf{false} \text{ put}(v) \text{ rel}(buf)$

where

$put(v) =_{\text{def}} x = v \ c := v \ full = \mathbf{true}$



GET

with *buf* **when full** **do**
(y:=c; full:=false)

Typical trace

*acq(buf) full=***true** *get(v') rel(buf)*

where

get(v') $\stackrel{\text{def}}{=} c=v' y:=v' full:=$ **false**



DEADLOCK

resource r_1, r_2 in

**with r_1 do with r_2 do $x:=1$
|| with r_2 do with r_1 do $y:=1$**

has traces

$\{x:=1 y:=1, y:=1 x:=1, \delta^\omega\}$

GLOBAL STATE

(s, h, A)

- store $s : \text{Ide} \rightarrow V$
- heap $h : \text{Loc} \rightarrow V$
- resources A held by program



GLOBAL STATE

(s, h, A)

- store $s : \text{Ide} \rightarrow V$
- heap $h : \text{Loc} \rightarrow V$
- resources A held by program

(s, h) when resource set is empty



EFFECTS

- State may *enable* action
- Enabled action causes *state change* or *error*

$$(s, h, A) \xRightarrow{\lambda} (s', h', A')$$

$$(s, h, A) \xRightarrow{\lambda} \text{abort}$$



EFFECTS

- State may *enable* action
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$$(s, h, A) \xRightarrow{\lambda} (s', h', A')$$

$$(s, h, A) \xRightarrow{\lambda} \text{abort}$$

defined by cases



EFFECTS

of store actions

Definition

$$(s, h, A) \xRightarrow{i=v} (s, h, A)$$

if $(i, v) \in s$

$$(s, h, A) \xRightarrow{i:=v} ([s \mid i : v], h, A)$$

if $(i, v) \in \text{dom}(s)$



EFFECTS

of heap actions

Definition

$$(s, h, A) \xrightarrow{[v]=v'} (s, h, A) \quad \text{if } (v, v') \in h$$

$$(s, h, A) \xrightarrow{[v]:=v'} (s, [h \mid v : v'], A) \quad \text{if } v \in \text{dom}(h)$$

$$(s, h, A) \xrightarrow{\text{alloc}(v, [v_0, \dots, v_n])} (s, [h \mid v : v_0, \dots, v + n : v_n], A) \\ \text{if } v, \dots, v + n \notin \text{dom}(h)$$

$$(s, h, A) \xrightarrow{\text{disp}(v)} (s, h \setminus v, A) \quad \text{if } v \in \text{dom}(h)$$

EFFECTS

of resource actions

Definition

$$(s, h, A) \xrightarrow{acq(r)} (s, h, A \cup \{r\}) \quad \text{if } r \notin A$$

$$(s, h, A) \xrightarrow{rel(r)} (s, h, A - \{r\}) \quad \text{if } r \in A$$

$$(s, h, A) \xrightarrow{try(r)} (s, h, A)$$



EFFECTS

causing error

Definition

$$(s, h, A) \xRightarrow{i=v} \mathbf{abort} \quad \text{if } i \notin \text{dom}(s)$$

$$(s, h, A) \xRightarrow{i:=v} \mathbf{abort} \quad \text{if } i \notin \text{dom}(s)$$

$$(s, h, A) \xRightarrow{[v]=v'} \mathbf{abort} \quad \text{if } v \notin \text{dom}(h)$$

$$(s, h, A) \xRightarrow{[v]:=v'} \mathbf{abort} \quad \text{if } v \notin \text{dom}(h)$$



EFFECTS

Definition

$$(s, h, A) \xRightarrow{\text{abort}} \text{abort}$$

$$\text{abort} \xRightarrow{\lambda} \text{abort}$$

GLOBAL COMPUTATION

- Consecutive sequence of actions
- A *sequential* trace
 - no interference between steps

$$(s, h, A) \xRightarrow{\alpha} (s', h', A')$$

$$(s, h, A) \xRightarrow{\alpha} \mathbf{abort}$$



GLOBAL COMPUTATION

- Consecutive sequence of actions
- A *sequential* trace
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$$(s, h, A) \xRightarrow{\alpha} \mathbf{abort}$$

defined by composition



A global computation

of PUT || (GET; dispose y)

$$\begin{aligned} & ([x : v, y : _, full : \mathbf{false}, c : _], [v : _], \{\}) \\ \xrightarrow{acq(buf)} & ([x : v, y : _, full : \mathbf{false}, c : _], [v : _], \{buf\}) \\ \xrightarrow{full=\mathbf{false} \text{ put}(v)} & ([x : v, y : _, full : \mathbf{true}, c : v], [v : _], \{buf\}) \\ \xrightarrow{rel(buf)} & ([x : v, y : _, full : \mathbf{true}, c : v], [v : _], \{\}) \\ \xrightarrow{acq(buf)} & ([x : v, y : _, full : \mathbf{true}, c : v], [v : _], \{buf\}) \\ \xrightarrow{full=\mathbf{true} \text{ get}(v)} & ([x : v, y : v, full : \mathbf{false}, c : v], [v : _], \{buf\}) \\ \xrightarrow{rel(buf)} & ([x : v, y : v, full : \mathbf{false}, c : v], [v : _], \{\}) \\ \xrightarrow{y=v \text{ disp}(v)} & ([x : v, y : v, full : \mathbf{false}, c : v], [], \{\}) \end{aligned}$$



ERROR-FREE

Definition

c is error-free from (s, h)

if

$$\forall \alpha \in \llbracket c \rrbracket. \neg((s, h) \xrightarrow{\alpha} \mathbf{abort})$$



ERROR-FREE

Definition

c is error-free from (s, h)
if
 $\forall \alpha \in \llbracket c \rrbracket. \neg((s, h) \xrightarrow{\alpha} \text{abort})$

EXAMPLE

$\text{dispose}(x) \parallel \text{dispose}(y)$

is error-free iff

$s(x) \neq s(y) \ \& \ s(x), s(y) \in \text{dom}(h)$



ERROR-FREE?

- PUT || (GET; dispose y)
if $s(full) = true \ \& \ s(c) \in dom(h)$
or $s(full) = false \ \& \ s(x) \in dom(h)$
- (PUT; dispose x) || GET
if $s(full) \in \{true, false\} \ \& \ s(x) \in dom(h)$
- (PUT; dispose x) || (GET; dispose y)
never



ERROR-FREE?



PUT || (GET; dispose y)

if $s(full) = true$ & $s(c) \in dom(h)$

or $s(full) = false$ & $s(x) \in dom(h)$

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ERROR-FREE?



PUT || (GET; dispose y)

if $s(full) = true$ & $s(c) \in dom(h)$

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(PUT; dispose x) || GET

if $s(full) \in \{true, false\}$ & $s(x) \in dom(h)$

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ERROR-FREE?



PUT || (GET; dispose y)

if $s(full) = true$ & $s(c) \in dom(h)$

or $s(full) = false$ & $s(x) \in dom(h)$



(PUT; dispose x) || GET

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(PUT; dispose x) || (GET; dispose y)

never



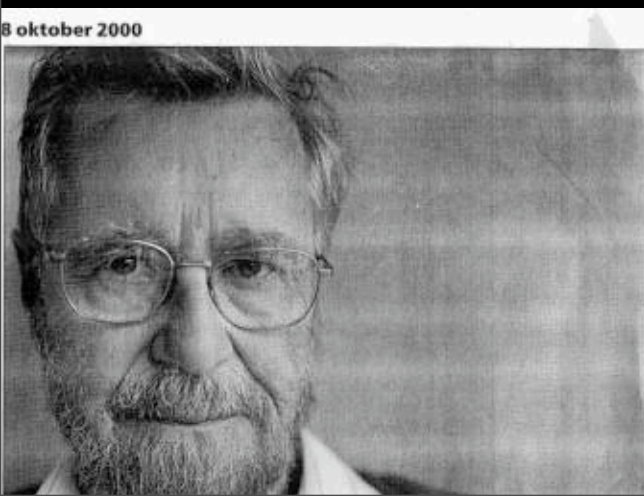
So far...

- Trace semantics
 - compositional
 - allows race detection
- Hard to use directly by itself...
 - doesn't reflect “loosely connected” principle



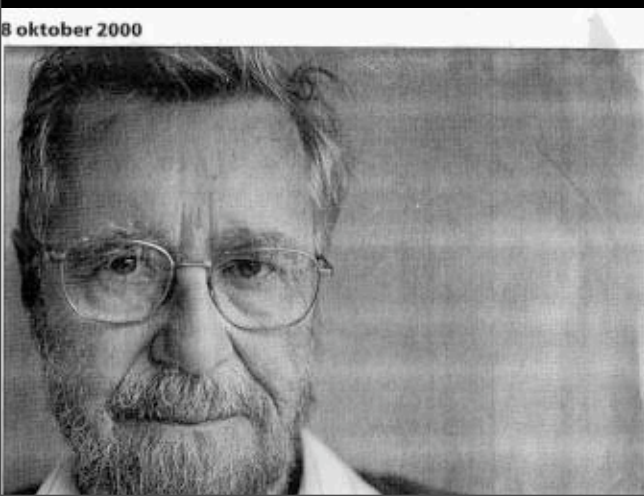
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So far...

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 - compositional
 - allows race detection
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*well-designed programs
should be easier
to prove correct...*



CONCURRENT SEPARATION LOGIC

- Resource-sensitive partial correctness
 - for *loosely coupled* processes
 - proof rules guarantee race-freedom

*building on ideas of
Dijkstra,
Hoare, Owicki/Gries,
Reynolds, O'Hearn*



Owicki/Gries/Hoare

- Partition the *critical identifiers*
 - among processes, available resources
- Maintain *conjunction* of resource invariants
 - for the available resources
- Rules enforce this discipline
 - rely/guarantee at synchronization points
 - scoped protection of critical storage

Owicki/Gries/Hoare

**NOT SOUND
FOR**

POINTER PROGRAMS

- Partition the *critical identifiers*
 - among processes, available resources
- Maintain *conjunction* of resource invariants
 - for the available resources
- Rules enforce this discipline
 - rely/guarantee at synchronization points
 - scoped protection of critical storage

O'Hearn

- Partition the *critical identifiers and the heap*
 - among processes, available resources
- Maintain *separate conjunction* of resource invariants
 - for available resources, *in sub-heap*
- Rules enforce this discipline
 - rely/guarantee at synchronization points
 - *dynamic* transfer of critical storage *and heap*



DESIGN RULES

- Associate resources with *protection lists*
 - *critical* variables must be protected
 - *protected* data must be accessed inside region
- Hide details with *resource invariant*

Hoare, Owicki/Gries

- Use *separation logic*

O'Hearn, Reynolds

RESOURCE CONTEXTS

$$\Gamma ::= r_1(X_1):R_1, \dots, r_k(X_k):R_k$$

satisfying *modularity properties*

$$i \neq j \Rightarrow X_i \cap X_j = \{\}$$

$$i \neq j \Rightarrow \mathbf{free}(R_i) \cap X_j = \{\}$$

- resource names
- protection lists
- invariants

$$\mathit{dom}(\Gamma) = \{r_1, \dots, r_k\}$$

$$\mathit{owned}(\Gamma) = X_1 \cup \dots \cup X_k$$

$$\mathit{inv}(\Gamma) = R_1 * \dots * R_k$$



PRECISION

We assume each invariant is precise

R is *precise* if
for all states (s, h)
there is at most one $h' \subseteq h$
such that
 $(s, h') \models R$

*A precise resource invariant uniquely
determines a heap portion...*



SPECIFICATIONS

$$\Gamma \vdash \{p\}c\{q\}$$

Well-formed when

- critical identifiers of c are protected
- protected identifiers only accessed in region
- free identifiers of invariants only changed in region
- p and q don't mention protected data

$$\text{free}(p, q) \cap \text{owned}(\Gamma) = \{\}$$

Properties enforced by the inference rules



PARALLEL RULE

$$\frac{\Gamma \vdash \{p_1\} c_1 \{q_1\} \quad \Gamma \vdash \{p_2\} c_2 \{q_2\}}{\Gamma \vdash \{p_1 * p_2\} c_1 \parallel c_2 \{q_1 * q_2\}}$$

- if
- $free(c_1) \cap writes(c_2) \subseteq owned(\Gamma)$
 - $free(c_2) \cap writes(c_1) \subseteq owned(\Gamma)$
 - $free(p_2, q_2) \cap writes(c_1) = \{\}$
 - $free(p_1, q_1) \cap writes(c_2) = \{\}$



REGION RULE

$$\frac{\Gamma \vdash \{(p * R) \wedge b\} c \{q * R\}}{\Gamma, r(X):R \vdash \{p\} \mathbf{with} \ r \ \mathbf{when} \ b \ \mathbf{do} \ c \{q\}}$$

if R precise

and $free(p, q) \cap X = \{\}$

$X \cap owned(\Gamma) = \{\}$

$X \cap free(\Gamma) = \{\}$



RESOURCE RULE

$$\frac{\Gamma, r(X):R \vdash \{p\}c\{q\}}{\Gamma \vdash \{p * R\} \mathbf{resource} \ r \ \mathbf{in} \ c\{q * R\}}$$



VALIDITY?

$$\Gamma \vdash \{p\}c\{q\}$$

Every finite *computation* of c
from a global state satisfying

$$p * inv(\Gamma)$$

is error-free,

and ends in a state satisfying

$$q * inv(\Gamma)$$



VALIDITY?

$$\Gamma \vdash \{p\}c\{q\}$$

Every finite *computation* of c
from a global state satisfying

$$p * inv(\Gamma)$$

is error-free,

and ends in a state satisfying

$$q * inv(\Gamma)$$

NOT COMPOSITIONAL



VALIDITY

$$\Gamma \vdash \{p\}c\{q\}$$

Every finite *interactive computation* of c
in an environment that respects Γ
from a global state satisfying

$$p * inv(\Gamma)$$

is error-free, respects Γ ,
and ends in a state satisfying

$$q * inv(\Gamma)$$

An informal working definition for now...



INFERENCE RULES

based on

- Hoare, Owicki-Gries
 - concurrency, no pointers
- Reynolds, O'Hearn
 - pointers, no concurrency

- O'Hearn

$\wedge \mapsto *$

a simple trick
with deep ramifications

SKIP

$$\overline{\Gamma \vdash \{p\} \mathbf{skip} \{p\}}$$

if $free(p) \cap owned(\Gamma) = \{\}$

ASSIGNMENT

$$\overline{\Gamma \vdash \{[e/i]p\} i := e \{p\}}$$

if $i \notin \text{owned}(\Gamma) \cup \text{free}(\Gamma)$

and $\text{free}(p, e) \cap \text{owned}(\Gamma) = \{\}$

LOOKUP

$$\Gamma \vdash \{ [e/i]p \wedge e \mapsto e' \} i := [e] \{ p \wedge e \mapsto e' \}$$

if $i \notin \text{free}(\Gamma) \cup \text{owned}(\Gamma)$

and $i \notin \text{free}(e, e')$

and $\text{free}(p, e, e') \cap \text{owned}(\Gamma) = \{ \}$

UPDATE

$$\overline{\Gamma \vdash \{e \mapsto -\} [e] := e' \{e \mapsto e'\}}$$

if $free(e, e') \cap owned(\Gamma) = \{\}$

ALLOCATE

$$\overline{\Gamma \vdash \{\mathbf{emp}\} i := \mathbf{cons}(E) \{i \mapsto E\}}$$

if $i \notin \mathit{free}(E)$

and $\mathit{free}(E) \cap \mathit{owned}(\Gamma) = \{\}$

and $i \notin \mathit{free}(\Gamma) \cup \mathit{owned}(\Gamma)$

DISPOSE

$$\Gamma \vdash \{e \mapsto -\} \mathbf{dispose}(e) \{ \mathbf{emp} \}$$

if $free(e) \cap owned(\Gamma) = \{\}$

RENAMING

$$\frac{\Gamma \vdash \{p\} \text{resource } r' \text{ in } [r'/r]c\{q\}}{\Gamma \vdash \{p\} \text{resource } r \text{ in } c\{q\}}$$

if $r' \notin \text{res}(c)$

AUXILIARY

$$\frac{\Gamma \vdash \{p\}c\{q\}}{\Gamma \vdash \{p\}c \setminus X \{q\}}$$

if $X \cap \text{free}(p, q) = \{\}$
 X auxiliary for c

AUXILIARY VARIABLES

- A set X is auxiliary for c if each free occurrence in c of an identifier from X is in an assignment whose target is in X
 - no effect on control flow
 - no effect on other variables

FRAME

$$\frac{\Gamma \vdash \{p\}c\{q\}}{\Gamma \vdash \{p*I\}c\{q*I\}}$$

if $free(I) \cap writes(c) = \{\}$
 $free(I) \cap owned(\Gamma) = \{\}$

EXPANSION

$$\frac{\Gamma \vdash \{p\}c\{q\}}{\Gamma, \Gamma' \vdash \{p\}c\{q\}}$$

if Γ, Γ' disjoint

and $free(p, c, q) \cap owned(\Gamma') = \{\}$

and $writes(c) \cap free(\Gamma') = \{\}$

CONTRACTION

$$\frac{\Gamma, \Gamma' \vdash \{p\}c\{q\}}{\Gamma \vdash \{p\}c\{q\}}$$

if Γ, Γ' disjoint

and $res(c) \subseteq dom(\Gamma)$

CONSEQUENCE

$$\frac{p \Rightarrow p' \quad \Gamma \vdash \{p'\} c \{q'\} \quad q' \Rightarrow q}{\Gamma \vdash \{p\} c \{q\}}$$

CONCURRENT DISPOSAL

$\Gamma \vdash \{p\}\mathbf{dispose}(x) \parallel \mathbf{dispose}(y)\{q\}$

valid if

$$p \Rightarrow (x \mapsto -) * (y \mapsto -) * q$$

PUT and GET

$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \mathbf{emp})$

$\Gamma \vdash \{x \mapsto _ \} \text{PUT} \{ \mathbf{emp} \}$

$\Gamma \vdash \{ \mathbf{emp} \} \text{GET} \{ y \mapsto _ \}$

$\Gamma \vdash \{ \mathbf{emp} \}$

$(x := \mathbf{cons}(_); \text{PUT}) \parallel (\text{GET}; \mathbf{dispose} \ y)$
 $\{ \mathbf{emp} \}$

valid formulas



OWNERSHIP

- Correctness proofs involve **dynamic transfer**
 - heap associated with resources
 - **determined by invariants**
- Key concept underpinning soundness proof
 - must show that transfer policy is safe
- Difficult to manage using **global state**
- Solution: **local state, local computations**



KEY IDEAS

- A process starts with only *non-critical* data in its local state
- Local state *grows* when resource is *acquired*
- Local state *shrinks* when resource is *released*
- Error if program breaks design rules



LOCAL STATES

(s, h, A)

- Local visibility

$$\text{dom}(s) \cap \text{owned}(\Gamma) = \text{owned}(\Gamma \upharpoonright A)$$

- An action has a local effect

$$(s, h, A) \xrightarrow[\Gamma]{\lambda} (s', h', A')$$

$$(s, h, A) \xrightarrow[\Gamma]{\lambda} \text{abort}$$



LOCAL EFFECTS

on local states

$$(s, h, A) \xrightarrow[\Gamma]{i=v} (s, h, A) \quad \text{if } (i, v) \in \mathbf{dom}(s)$$

$$(s, h, A) \xrightarrow[\Gamma]{i:=v} ([s \mid i : v], h, A) \\ \text{if } i \in \mathbf{dom}(s) - \mathit{free}(\Gamma \setminus A)$$



LOCAL EFFECTS

following the design rules

$$(s, h, A) \xrightarrow[\Gamma]{acq(r)} (s \cdot s', h \cdot h', A \cup \{r\})$$

if $r(X) : R \in \Gamma$

and $s \perp s', h \perp h', \text{dom}(s') = X,$
 $(s \cdot s', h') \models R$

$$(s, h, A) \xrightarrow[\Gamma]{rel(r)} (s \setminus X, h - h', A - \{r\})$$

if $r(X) : R \in \Gamma$

and $h' \subseteq h, (s, h') \models R$



LOCAL ERRORS

breaking the design rules

$$(s, h, A) \xrightarrow[\Gamma]{i:=v} \mathbf{abort} \quad \text{if } i \in \mathit{free}(\Gamma \setminus A) \\ \text{or } i \notin \mathit{dom}(s)$$

$$(s, h, A) \xrightarrow[\Gamma]{\mathit{rel}(r)} \mathbf{abort} \\ \text{if } r(X) : R \in \Gamma \\ \text{and } \forall h' \subseteq h. (s, h') \models \neg R$$



LOCAL COMPUTATION

- What a *process* sees of an interactive computation in an environment that respects the resource context
- Interference only on synchronization

$$(s, h, A) \xrightarrow[\Gamma]{\alpha} (s', h', A')$$

$$(s, h, A) \xrightarrow[\Gamma]{\alpha} \mathbf{abort}$$

defined by composition



A local computation

of PUT || (GET; dispose y)

$$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \mathbf{emp})$$

$$([x : v, y : _], [v : _], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma} \rightarrow ([x : v, y : _, \text{full} : \mathbf{false}, c : _], [v : _], \{\text{buf}\})$$

$$\frac{\text{full}=\mathbf{false} \text{ put}(v)}{\Gamma} \rightarrow ([x : v, y : _, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma} \rightarrow ([x : v, y : _], [], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma} \rightarrow ([x : v, y : _, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{full}=\mathbf{true} \text{ get}(v)}{\Gamma} \rightarrow ([x : v, y : v, \text{full} : \mathbf{false}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma} \rightarrow ([x : v, y : v], [v : _], \{\})$$

$$\frac{y=v \text{ disp}(v)}{\Gamma} \rightarrow ([x : v, y : v], [], \{\})$$

A local computation of PUT

$$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \mathbf{emp})$$

$$\begin{array}{l} ([x : v], [v : _], \{\}) \\ \frac{\text{acq}(\text{buf})}{\Gamma} \rightarrow ([x : v, \text{full} : \mathbf{false}, c : _], [v : _], \{\text{buf}\}) \\ \frac{\text{full}=\mathbf{false} \text{ put}(v)}{\Gamma} \rightarrow ([x : v, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\}) \\ \frac{\text{rel}(\text{buf})}{\Gamma} \rightarrow ([x : v], [], \{\}) \end{array}$$

A local computation of GET; dispose y

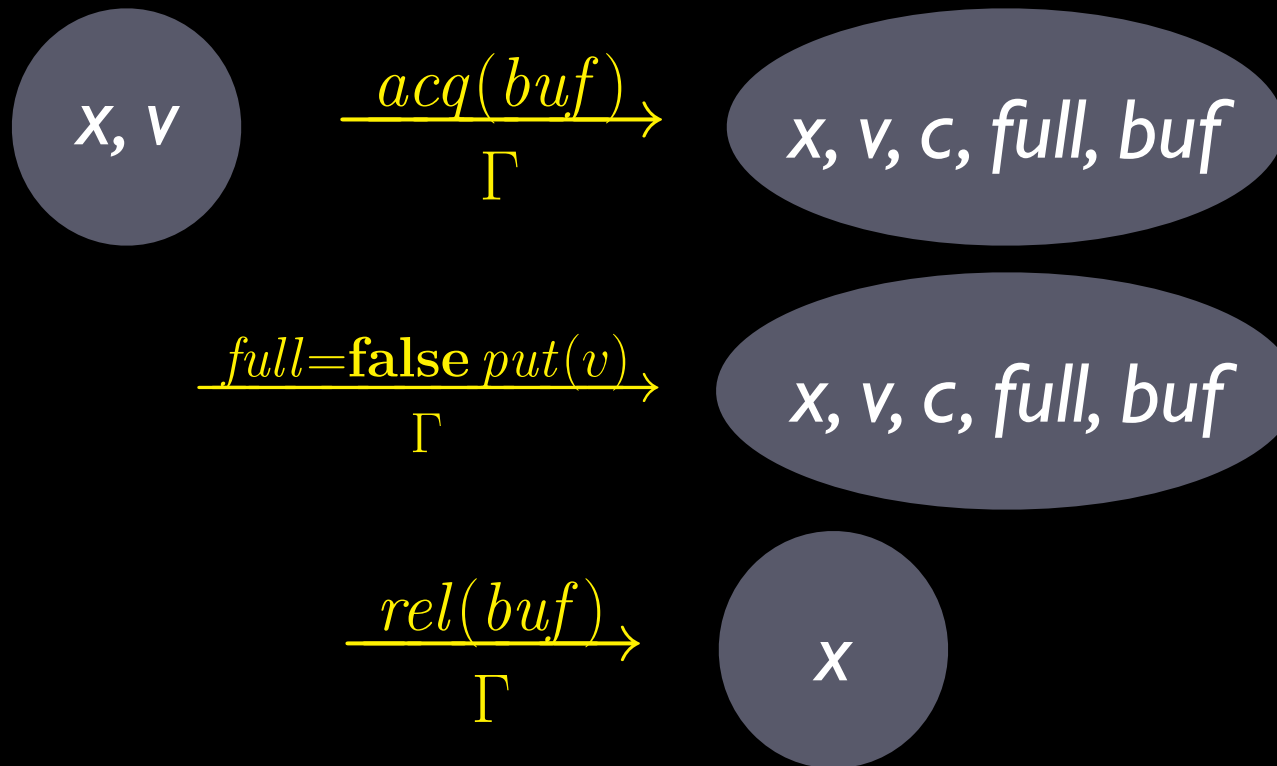
$$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \mathbf{emp})$$

$$\begin{array}{l} ([y : _], [], \{\}) \\ \frac{\text{acq}(\text{buf})}{\Gamma} \rightarrow ([y : _, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\}) \\ \frac{\text{full}=\mathbf{true} \text{ get}(v)}{\Gamma} \rightarrow ([y : v, \text{full} : \mathbf{false}, c : v], [v : _], \{\text{buf}\}) \\ \frac{\text{rel}(\text{buf})}{\Gamma} \rightarrow ([y : v], [v : _], \{\}) \\ \frac{y=v \text{ disp}(v)}{\Gamma} \rightarrow ([y : v], [], \{\}) \end{array}$$



A local computation of PUT

$$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \text{emp})$$



A local computation of GET

$$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \text{emp})$$

$$y \xrightarrow[\Gamma]{\text{acq}(\text{buf})} y, v, c, \text{full}, \text{buf}$$

$$\xrightarrow[\Gamma]{\text{full}=\text{true } \text{get}(v)} y, v, c, \text{full}, \text{buf}$$

$$\xrightarrow[\Gamma]{\text{rel}(\text{buf})} y, v$$



A local computation

of $(\text{PUT}(x); \text{dispose } x) \parallel \text{GET}(y)$

$$\Gamma' = \text{buf}(c, \text{full}) : (\text{full} \wedge \text{emp}) \vee (\neg \text{full} \wedge \text{emp})$$

$$([x : v, y : _], [v : _], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma'} \rightarrow ([x : v, y : _, \text{full} : \mathbf{false}, c : _], [v : _], \{\text{buf}\})$$

$$\frac{\text{full}=\mathbf{false} \text{ put}(v)}{\Gamma'} \rightarrow ([x : v, y : _, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma'} \rightarrow ([x : v, y : _], [v : _], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma'} \rightarrow ([x : v, y : _, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{full}=\mathbf{true} \text{ get}(v)}{\Gamma'} \rightarrow ([x : v, y : v, \text{full} : \mathbf{false}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma'} \rightarrow ([x : v, y : v], [v : _], \{\})$$

$$\frac{x=v \text{ disp}(v)}{\Gamma'} \rightarrow ([x : v, y : v], [], \{\})$$

Local computation

of (PUT(x); dispose x)

$$\Gamma' = \text{buf}(c, \text{full}) : (\text{full} \wedge \mathbf{emp}) \vee (\neg \text{full} \wedge \mathbf{emp})$$

$$([x : v], [v : _], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma'} \rightarrow ([x : v, \text{full} : \mathbf{false}, c : _], [v : _], \{\text{buf}\})$$

$$\frac{\text{full} = \mathbf{false} \text{ put}(v)}{\Gamma'} \rightarrow ([x : v, \text{full} : \mathbf{true}, c : v], [v : _], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma'} \rightarrow ([x : v], [v : _], \{\})$$

$$\frac{x = v \text{ disp}(v)}{\Gamma'} \rightarrow ([x : v], [], \{\})$$

Local computation

of **GET(y)**

$$\Gamma' = \text{buf}(c, \text{full}) : (\text{full} \wedge \mathbf{emp}) \vee (\neg \text{full} \wedge \mathbf{emp})$$

$$([y : _], [], \{\})$$

$$\frac{\text{acq}(\text{buf})}{\Gamma'} \rightarrow ([y : _, \text{full} : \mathbf{true}, c : v], [], \{\text{buf}\})$$

$$\frac{\text{full}=\mathbf{true} \text{ get}(v)}{\Gamma'} \rightarrow ([y : v, \text{full} : \mathbf{false}, c : v], [], \{\text{buf}\})$$

$$\frac{\text{rel}(\text{buf})}{\Gamma'} \rightarrow ([y : v], [], \{\})$$

VALIDITY

$$\Gamma \vdash \{p\}c\{q\}$$

Every finite *local computation* of c
from a *local state* satisfying p
is error-free
and
ends in a state satisfying q

$$\forall \alpha \in \llbracket c \rrbracket.$$

$$\forall s : \text{dom}(s) \supseteq \text{free}(c) - \text{owned}(\Gamma).$$

$$(s, h) \models p \ \& \ (s, h) \xrightarrow[\Gamma]{\alpha} \sigma' \Rightarrow \sigma' \models q$$



LOCALIZATION

$$\begin{array}{ccc} (s, h, A) & \longrightarrow & (s \downarrow A, h, A) \\ \text{global} & & \text{local} \end{array}$$

$$\begin{aligned} s \downarrow A &= s \setminus \text{owned}(\Gamma) \cup s \uparrow \text{owned}(\Gamma \uparrow A) \\ &= s \setminus \text{owned}(\Gamma \setminus A) \end{aligned}$$

Special cases

$$s \downarrow \{\} = s \setminus \text{owned}(\Gamma)$$

$$s \downarrow \text{dom}(\Gamma) = s$$

SOUNDNESS

- Every provable formula is valid
 - proof uses local states, local effects
 - show that each rule preserves validity
 - for PARALLEL rule use Parallel Lemma



FRAME LEMMA

Suppose $h = h_1 \cdot h_2, A = A_1 \cdot A_2$
and $(A_1, A_2) \xrightarrow{\lambda} (A'_1, A_2)$

If $(s, h, A) \xrightarrow[\Gamma]{\lambda} (s', h', A')$
and $\neg (s \downarrow A_1, h_1, A_1) \xrightarrow[\Gamma]{\lambda} \mathbf{abort}$

then $(s \downarrow A_1, h_1, A_1) \xrightarrow[\Gamma]{\lambda} (s' \downarrow A'_1, h'_1, A'_1)$
 $h' = h'_1 \cdot h_2, A' = A'_1 \cdot A_2$

PARALLEL LEMMA

When c_1 and c_2 are loosely coupled...

- A local computation of $c_1 || c_2$ decomposes into local computations of c_1 and c_2
- A local error of $c_1 || c_2$ is caused by a local error of c_1 or c_2 (not by interference)
- A successful local computation of $c_1 || c_2$ is consistent with any successful local computations of c_1 and c_2

*Loosely connected processes
mind their own business*



PARALLEL LEMMA

Suppose $free(c_1) \cap writes(c_2) \subseteq owned(\Gamma)$
 $free(c_2) \cap writes(c_1) \subseteq owned(\Gamma)$
 $\alpha_1 \in \llbracket c_1 \rrbracket, \alpha_2 \in \llbracket c_2 \rrbracket, \alpha \in \alpha_1 \parallel \alpha_2, h = h_1 \cdot h_2$

If

$(s, h) \xrightarrow[\Gamma]{\alpha} \mathbf{abort}$

then

$(s \setminus writes(c_2), h_1) \xrightarrow[\Gamma]{\alpha_1} \mathbf{abort}$

or

$(s \setminus writes(c_1), h_2) \xrightarrow[\Gamma]{\alpha_2} \mathbf{abort}$



PARALLEL LEMMA

Suppose $free(c_1) \cap writes(c_2) \subseteq owned(\Gamma)$
 $free(c_2) \cap writes(c_1) \subseteq owned(\Gamma)$
 $\alpha_1 \in \llbracket c_1 \rrbracket, \alpha_2 \in \llbracket c_2 \rrbracket, \alpha \in \alpha_1 \parallel \alpha_2, h = h_1 \cdot h_2$

If

$$(s, h) \xrightarrow[\Gamma]{\alpha} (s', h')$$

$$(s \setminus writes(c_2), h_1) \xrightarrow[\Gamma]{\alpha_1} (s'_1, h'_1)$$

$$(s \setminus writes(c_1), h_2) \xrightarrow[\Gamma]{\alpha_2} (s'_2, h'_2)$$

then

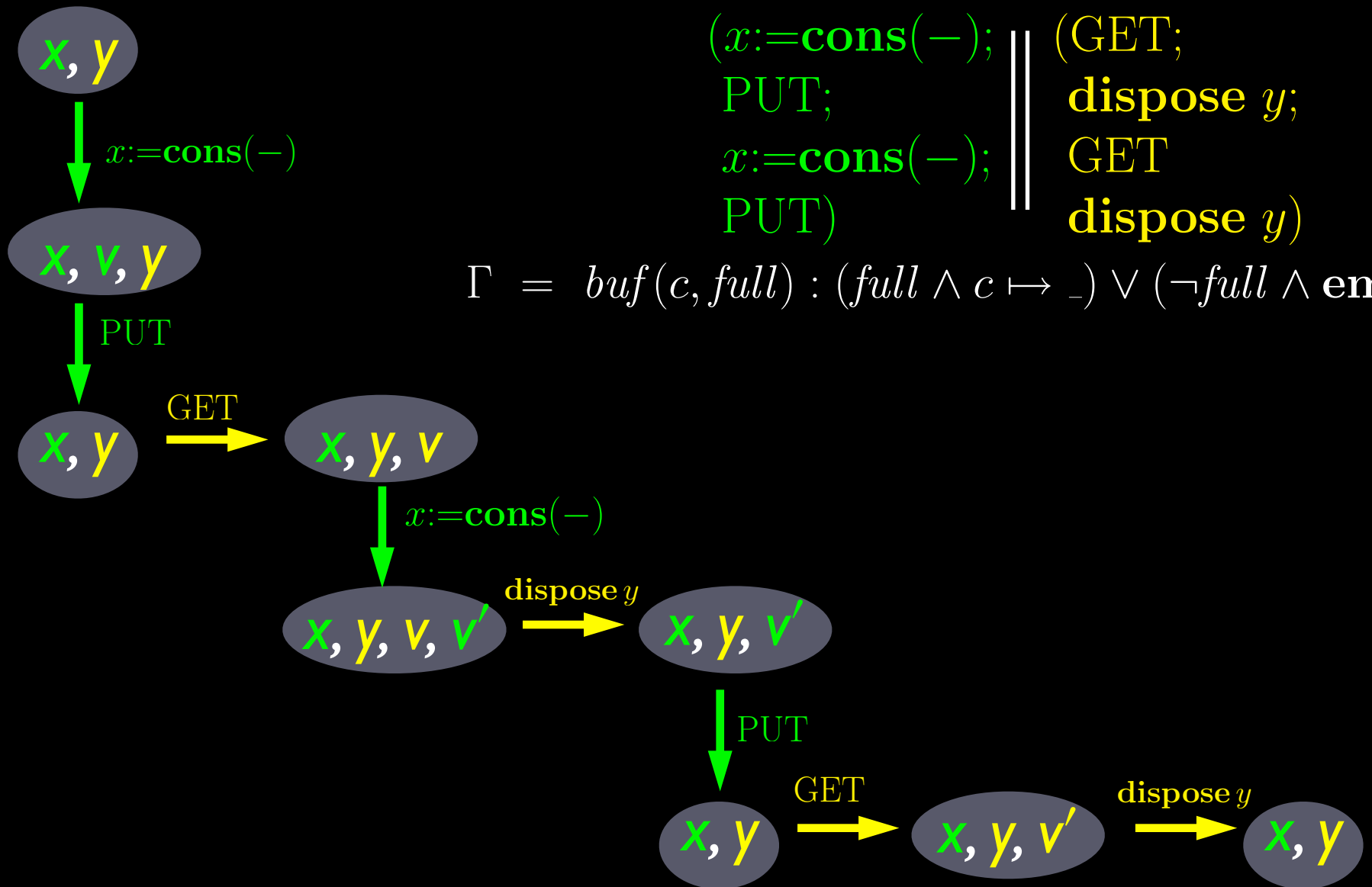
$$s'_1 = s' \setminus writes(c_2)$$

$$s'_2 = s' \setminus writes(c_1)$$

$$h' = h'_1 \cdot h'_2$$



A LOCAL PARALLEL COMPUTATION

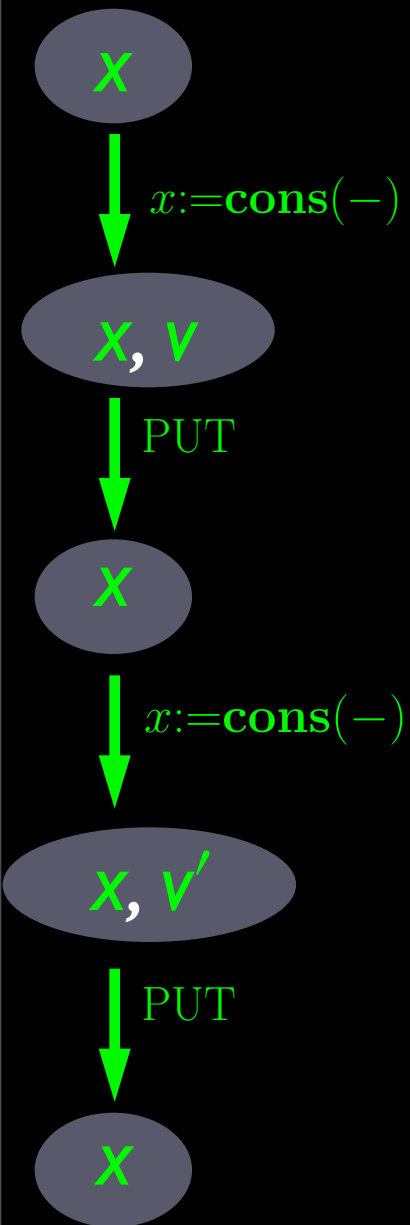


A LOCAL PARALLEL COMPUTATION

$(x := \mathbf{cons}(-);$
 $\mathbf{PUT};$
 $x := \mathbf{cons}(-);$
 $\mathbf{PUT})$ \parallel $(\mathbf{GET};$
 $\mathbf{dispose } y;$
 \mathbf{GET}
 $\mathbf{dispose } y)$

$\Gamma = \mathit{buf}(c, \mathit{full}) : (\mathit{full} \wedge c \mapsto _) \vee (\neg \mathit{full} \wedge \mathbf{emp})$

A LOCAL PARALLEL COMPUTATION



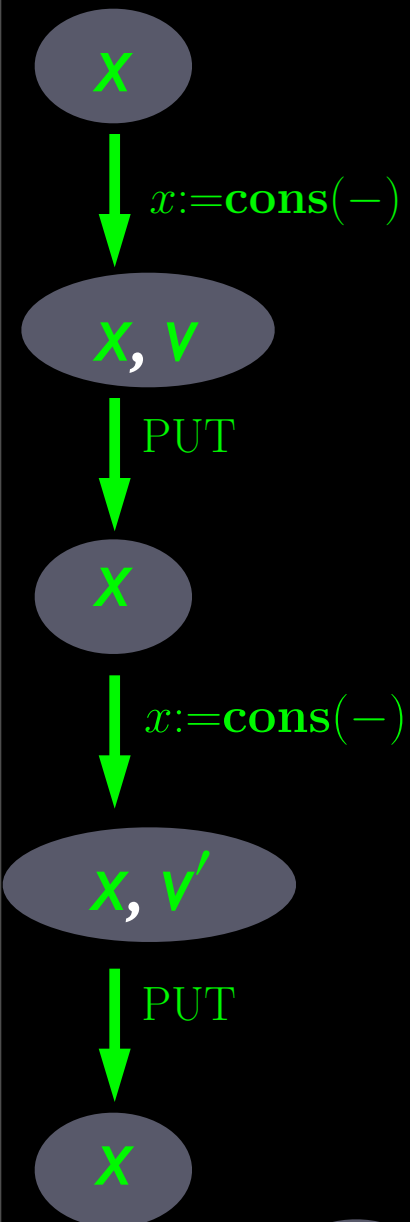
$(x := \mathbf{cons}(-);$
 $\mathbf{PUT};$
 $x := \mathbf{cons}(-);$
 $\mathbf{PUT})$

\parallel

$(\mathbf{GET};$
 $\mathbf{dispose } y;$
 \mathbf{GET}
 $\mathbf{dispose } y)$

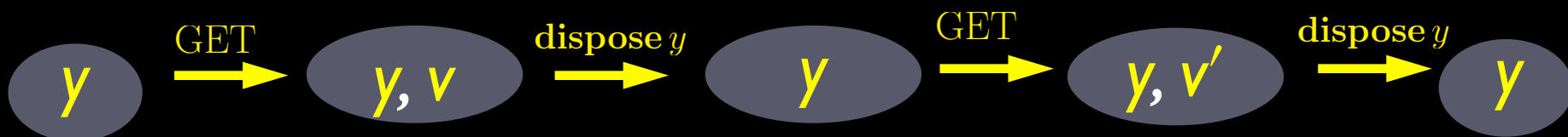
$\Gamma = \text{buf}(c, \text{full}) : (\text{full} \wedge c \mapsto _) \vee (\neg \text{full} \wedge \mathbf{emp})$

A LOCAL PARALLEL COMPUTATION



$(x:=\mathbf{cons}(-);$
 $\mathbf{PUT};$
 $x:=\mathbf{cons}(-);$
 $\mathbf{PUT}) \parallel$
 $(\mathbf{GET};$
 $\mathbf{dispose } y;$
 \mathbf{GET}
 $\mathbf{dispose } y)$

$\Gamma = \mathit{buf}(c, \mathit{full}) : (\mathit{full} \wedge c \mapsto _) \vee (\neg \mathit{full} \wedge \mathbf{emp}$



PARALLEL DECOMPOSITION

Assume

$$(s, h, A) \xrightarrow[\Gamma]{\alpha} (s', h', A')$$

$$h = h_1 \cdot h_2, \quad A = A_1 \cdot A_2$$

$$\alpha \in \alpha_1 \parallel_{A_1} \parallel_{A_2} \alpha_2$$

If

$$\neg(s_1, h_1, A_1) \xrightarrow[\Gamma]{\alpha_1} \mathbf{abort}$$

$$\neg(s_2, h_2, A_2) \xrightarrow[\Gamma]{\alpha_2} \mathbf{abort}$$

then

$$\exists h'_1, h'_2, A'_1, A'_2.$$

$$h' = h'_1 \cdot h'_2, \quad A' = A'_1 \cdot A'_2,$$

$$(s_1, h_1, A_1) \xrightarrow[\Gamma]{\alpha_1} (s'_1, h'_1, A'_1)$$

$$(s_2, h_2, A_2) \xrightarrow[\Gamma]{\alpha_2} (s'_2, h'_2, A'_2)$$

CONNECTION

- Soundness shows that provable formulas are valid
- Validity refers to local computations
- Need to connect with conventional notions
 - global state
 - traditional partial correctness

*Show that local computations
are consistent with global view...*

CONNECTION LEMMA

Suppose

$$\alpha \in \llbracket c \rrbracket, h = h_1 \cdot h_2, (s, h_2) \models \text{inv}(\Gamma)$$

If

$$(s, h) \xRightarrow{\alpha} \text{abort}$$

then

$$(s \setminus \text{owned}(\Gamma), h_1) \xrightarrow[\Gamma]{\alpha} \text{abort}$$



CONNECTION LEMMA

Suppose

$$\alpha \in \llbracket c \rrbracket, h = h_1 \cdot h_2, (s, h_2) \models \text{inv}(\Gamma)$$

If

$$(s, h) \xrightarrow{\alpha} (s', h')$$

$$(s \setminus \text{owned}(\Gamma), h_1) \xrightarrow[\Gamma]{\alpha} (s'_1, h'_1)$$

then

$$s'_1 = s' \setminus \text{owned}(\Gamma)$$

$$\exists h'_2. h' = h'_1 \cdot h'_2 \ \& \ (s', h'_2) \models \text{inv}(\Gamma)$$



DECOMPOSITION LEMMA

If

$$(s, h, A) \xrightarrow[\Gamma]{\alpha} (s', h', A')$$
$$\neg (s_1, h_1, A_1) \xrightarrow[\Gamma]{\alpha_1} \mathbf{abort}$$
$$\neg (s_2, h_2, A_2) \xrightarrow[\Gamma]{\alpha_2} \mathbf{abort}$$

then

$$(s_1, h_1, A_1) \xrightarrow[\Gamma]{\alpha_1} (s'_1, h'_1, A'_1)$$
$$(s_2, h_2, A_2) \xrightarrow[\Gamma]{\alpha_2} (s'_2, h'_2, A'_2)$$
$$h' = h'_1 \cdot h'_2, \quad A' = A'_1 \cdot A'_2$$

COROLLARY

If

$$(s, h) \xrightarrow[\Gamma]{\alpha} (s', h')$$

$$h = h_1 \cdot h_2$$

$$\neg (s \setminus \text{writes}(c_2), h_1) \xrightarrow[\Gamma]{\alpha_1} \mathbf{abort}$$

$$\neg (s \setminus \text{writes}(c_1), h_2) \xrightarrow[\Gamma]{\alpha_2} \mathbf{abort}$$

then

$$(s \setminus \text{writes}(c_2), h_1) \xrightarrow[\Gamma]{\alpha_1} (s' \setminus \text{writes}(c_2), h'_1)$$

$$(s \setminus \text{writes}(c_1), h_2) \xrightarrow[\Gamma]{\alpha_2} (s' \setminus \text{writes}(c_1), h'_2)$$

$$h' = h'_1 \cdot h'_2$$

... hence Parallel Rule is sound

COROLLARY

$$\Gamma \vdash \{p\}c\{q\}$$

Validity implies error-freedom:

Every finite *computation* of c
from a global state satisfying
 $p * inv(\Gamma)$
is error-free,
and ends in a state satisfying
 $q * inv(\Gamma)$

cf. traditional notion of validity



LOSING PRECISION

$r : \text{true} \vdash \{\text{emp} \vee \text{one}\} \text{with } r \text{ do skip}\{\text{emp}\}$

INVALID

but would be provable
if we drop precision constraint

Reynolds

BEYOND PRECISION

$$\frac{\Gamma \vdash \{(p * R) \wedge b\} c \{q * R\}}{\Gamma, r(X):R \vdash \{p\} \text{with } r \text{ when } b \text{ do } c \{q\}}$$

R supported
 p, q intuitionistic

INTUITIONISTIC

p is intuitionistic if
for all states (s, h)
if $(s, h) \models p$ and $h \subseteq h'$
then $(s, h') \models p$

SUPPORTED

p is supported if
for all states (s, h)
if $(s, h) \models p$
there is a unique minimal $h' \subseteq h$
such that $(s, h') \models p$

SOUNDNESS

- Modify local semantics
 - transfer *minimal* heap
- Makes no change if R precise
- Soundness proof still works
- Can use precise or supported/intuitionistic

CONCLUSIONS

- Concurrent separation logic
 - generalizes Owicki-Gries, Hoare
- Traces + local semantics
 - models ownership transfer, loose coupling
 - yields soundness proof
 - embodies Dijkstra's Principle



FUTURE WORK

- Deadlock, total correctness, safety, liveness
- Monitors, more general semaphores
- Passification
 - semantics already treats store, heap alike
- Concurrent procedures
 - Parallel Algol + pointers?