

ISSUE 30
APPENDIX B

LUNAR EXCURSION MODULE AGC SUBSYSTEM
INPUT AND OUTPUT SIGNALS

All input and output lines or signals passing through connector A51 of the AGC and connector J9 of the DSKY are listed in table 30B-1 of this appendix. Information is arranged by order of line or signal numbers frequently referred to in the main body of this issue. The numbers of the connecting pins of connectors A51 and J9 are given in columns 2 and 3 of the table while various signal names are listed in columns 4 and 5. The various types of interface circuits provided are described in figure 30B-1. A short description of the signals is contained in the last column of table 30B-1. Numbers preceded by an A or B indicate module numbers.

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
001	516 616	-	CLK { XC001H XC001L	MASTER CLOCK	Gate 37137 of the Timer generates 1.024 Mpps signal CLK which controls interface circuit 1XT of A25. Master clock output pulses are used in various spacecraft systems.
002	358 458	-	ALT1 { XA002H XA002L	ALT MTER '1'	Gates 46109 and 46110 of the Altitude Meter Control (par 30-123 through 30-125) generate serial data pulses ALT0 and ALT1 which control interface circuits 5XT and 4XT of A29.
003	357 457	-	ALT0 { XA003H XA003L	ALT MTER '0'	
004	558	-	DE004 ULLTHR	ABORT	Interface circuit 1D of A29 receives DC signals from the spacecraft and feeds gate 44101 of bit position 1 of channel 30 (table 30-5D).
007	657	-	DE007 LFTOFF	LIFT OFF NC	Interface circuit 2D of A29 receives DC signals from the spacecraft and feeds gate 44113 of bit position 5 of channel 30 (table 30-5D).
008	354 454	-	THRST+ { XA008H XA008L	INCR THR RATE DESC EN	Gates 46247 and 46248 of the EMS and Thrust Drive Control (par 30-100 through 30-104) generate signals THRST+ and THRST- which control interface circuits 8XT and 9XT of A29.
009	353 453	-	THRST- { XA009H XA009L	DECR THR RATE DESC EN	
010	356 456	-	ALRT1 { XA010H XA010L	ALT RATE MTR '1'	Gates 46111 and 46112 of the Altitude Meter Control (par 30-123 through 30-125) generate serial data pulses ALRT0 and ALRT1 which control interface circuits 7XT and 6XT of A29.
012	355 455	-	ALRT0 { XA012H XA012L	ALT RATE MTR '0'	
011	613	-	ENON CB011	ENG ON ASC OR DESC	Bit position 7 of channel 12 (table 30-5A) controls gates 43228 and 43229 to generate signals ENON and ENOFF which control interface circuits 19C and 18C of A26. DC output signals control the spacecraft engine.
013	513	-	ENOFF CB013	ENG OFF ASC OR DESC	
014	133 233	-	Y-014H } Y-014L } DKSTRT	DLNK START	Interface circuits 1Y, 2Y, and 3Y of A27 receive pulses from the NA programmer and feed gates 47101, 45447, and 47227 of the Downlink Converter (par 30-126 and 30-127).
015	132 232	-	Y-015H } Y-015L } DKEND	DLNK END	
016	130	-	Y-016H } Y-016L } DKBSNC	DLNK SYNC	
	230	-			

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings $\triangle 1$	Name Used in DD Memo 185 $\triangle 3$	Signal Description
	A51 of AGC	J9 of DSKY			
017	352 452	-	DKDATA { X-017H X-017L	DLNK DATA	Gate 47256 of the Downlink Converter (par 30-126 and 30-127) generates signal DKDATA which controls interface circuit 1XT of A28. The serial data pulses are fed into the NA Programmer.
018 019 020 021 022 023	655 555 654 554 653 553	-	DE018 TRAN+X DE019 TRAN-X DE020 TRAN+Y DE021 TRAN-Y DE022 TRAN+Z DE023 TRAN-Z	+X TRANS COMM (MAN) -X TRANS COMM (MAN) +Y TRANS COMM (MAN) -Y TRANS COMM (MAN) +Z TRANS COMM (MAN) -Z TRANS COMM (MAN)	Interface circuits 4D through 9D of A29 receive DC signals from the manual translation control and feed bit positions 7 through 12 (gates 44216 through 44220, and 44232) of channel 31 (table 30-5E).
024 025	129 229 128 228	-	YG024H } UPL0 YG024L } YG025H } UPL1 YG025L }	ULNK0 ULNK1	Interface circuits 4Y and 5Y of A27 receive serial data pulses from the uplink equipment and feed gates 46304 and 46305 of the Inlink Control (par 30-116 through 30-119).
028	413	-	ALTSNC CB028	ALT METER SYNC NC	Gate 46132 of the Altitude Meter Control (par 30-123 through 30-125) generates signal ALTSNC which controls interface circuit 17C of A26.
029 030	350 450 349 449	-	EMS+ { XA029H XA029L EMS- { XA030H XA030L	MONITOR INCR (+) NC MONITOR INCR (-) NC	Gates 46254 and 46258 of the EMS and Thrust Drive Control (par 30-100 through 30-104) generate signals EMS+ and EMS- which control interface circuits 10XT and 11XT of A29.
031 032	127 227 126 226	-	YG031H } RRIN0 YG031L } YG032H } RRIN1 YG032L }	RRDR IN0 RRDR IN1	Interface circuits 6Y and 7Y of A27 receive serial data pulses from the rendezvous radar and feed gates 45349 and 44347 of the Radar Control (par 30-105 through 30-109).
033 034	- -	- -	- -	SPARE CHN 32-12 NC SPARE CHN 32-13 NC	Interface circuits 3D and 4D of A26 are able to receive DC signals and feed bit positions 12 and 13 (gates 44134 and 44137) of channel 32 (table 30-5F).
039	630	-	DE039 LVDAGD	LRDR VEL DATA GOOD	Interface circuit 17D of A28 receives a DC signal from landing radar and feeds bit position 8 (gate 44123) of channel 33 (table 30-5G).
040	641	-	DE040 HOLFUN	AUTO THROTTLE	Interface circuit 7D of A28 receives DC signal and feeds bit position 13 (gate 44233) of channel 31 (table 30-5E).

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LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description		
	A51 of AGC	J9 of DSKY					
041	348	-	RRRANG	{ XA041H XA041L	RRDR RANGE GATE	Gates 45328 through 45333, 45345, and 45346 of the Radar Control (par 30-105 through 30-109) generate control signals (90 msec bursts of 100 kpps) RRRANG, RRRARA, LRXVEL, LRYVEL, LRZVEL, LRRANG, RRSYNC, and LRSYNC which control interface circuits 4XT through 11XT of A28. Gates 49218 and 49219 generate timing signals (3.2 kpps) RRRST and LRRST which control interface circuits 2XT and 3XT of module A25.	
042	347	-	RRRARA	{ XA042H XA042L	RRDR RANGE RATE GATE		
043	344	-	LRXVEL	{ XA043H XA043L	LRDR XA VELOCITY GATE		
044	343	-	LRYVEL	{ XA044H XA044L	LRDR YA VELOCITY GATE		
045	342	-	LRZVEL	{ XA045H XA045L	LRDR ZA VELOCITY GATE		
046	341	-	LRRANG	{ XA046H XA046L	LRDR RANGE GATE		
047	346	-	RRSYNC	{ XA047H XA047L	RRDR SYNC FOR READOUT		
048	345	-	RRRST	{ XC048H XC048L	RRDR GATE RESET		
051	340	-	LRSYNC	{ XA051H XA051L	LRDR SYNC FOR READOUT		
052	339	-	LRRST	{ XC052H XC052L	LRDR GATE RESET		
060	523	-	DE060	LEMATT	ATTITUDE HOLD MODE		Interface circuit 14D of A27 receives DC signals and feeds bit position 11 (gate 44131) of channel 32 (table 30-5F).
061	530	-	DE061	LRRLSC	LR RANGE LOW SCALE		Interface circuit 18D of A28 receives DC signals from the landing radar and feeds bit position 9 (gate 44126) of channel 33 (table 30-5G).
064	629	-	DE064	RRPONA	RR POWER ON/AUTO		Interface circuit 1D of A27 receives DC signal from the rendezvous radar and feeds bit position 2 (gate 44105) of channel 33 (table 30-5G).
065	541	-	DE065	FREFUN	AUTO STABILIZATION	Interface circuit 8D of A28 receives DC signal and feeds bit position 14 (gate 44233) of channel 31 (table 30-5E).	
066	640	-	DE066	S4BSAB	ABORT STAGE	Interface circuits 9D, 10D of A28, and 3D of A29 receive DC signals from the spacecraft and feed bit positions 4, 2, and 6 (gates 44110, 44104, and 44116) of channel 30 (table 30-5D).	
067	540	-	DE067	SMSEPR	STAGE VERIFY		
068	557	-	DE068	GUIREL	DISPLAY INERTIAL DATA		

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
069	529	-	DE069 RRRLSC	RR RANGE LOW SCALE	Interface circuit 2D of A27 receives DC signals from the rendezvous radar and feeds bit position 2 (gate 44108) of channel 33 (table 30-5G).
071	125 225	-	YG071H } LRIN0 YG071L }	LRDR IN0	Interface circuits 8Y and 9Y of A27 receive serial data pulses from the landing radar and feed gates 45355 and 45353 of the Radar Control (par 30-105 through 30-109).
072	124 224	-	YG072H } LRIN1 YG072L }	LRDR IN1	
078	518 618	-	OTLNK0 { XA078H XA078L	"0" BIT CROSS OUT NC	Gates 46147 and 46149 of Outlink Control (par 30-120 through 30-122) generate serial data pulses OTLNK0 and OTLNK1 which control interface circuits 6XT and 7XT of A27. Outputs are provided for the cross link equipment but not connected.
079	517 617	-	OTLNK1 { XA079H XA079L	"1" BIT CROSS OUT NC	
080	123 223	-	YG080H } XLNK0 YG080L }	"0" BIT CROSS IN NC	Interface circuits 10Y and 11Y of A27 are able to receive serial data pulses from the cross link equipment and feed gates 46306 and 46307 of the Inlink Control (par 30-116 through 30-119). Inputs are not connected.
081	122 222	-	YG081H } XLNK1 YG081L }	"1" BIT CROSS IN NC	
082	636	-	DE082 SPSRDY	ENGINE ARMED	Interface circuit 13D of A28 receives DC signals from the spacecraft and feeds bit position 3 (gate 44107) of channel 30 (table 30-5D).
083	538	-	DE083 BLKUPL/	ACCEPT UPLNK	Interface circuit 12D of A28 receives DC signals from the UPTTEL switch and feeds gate 46308 of Inlink Control (par 30-116 through 30-119).
084	638	-	DE084 IN3008	SPARE CHN 30-08 NC	Interface circuit 11D of A28 is able to receive DC signals and feeds bit position 8 (gate 44122) of channel 32 (table 30-5D).
085	351 451	-	DKDATB { X-085H X-085L	DLNK DATA (AGS)	Gate 47261 of the Downlink Converter (par 30-126 and 30-127) generates signal DKDATB which controls interface circuit 1XT of A29. The signal is identical to signal 017.
086	639	-	+28VDC RD086	65/60/95	Resistor 5R of A28 provides +28VDC for external equipment.

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LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings $\triangle 1$	Name Used in DD Memo 185 $\triangle 3$	Signal Description
	A51 of AGC	J9 of DSKY			
087	647	-	+28COM RD087	4/66/82	Resistors 1R and 2R of A28 in parallel provide +28VDC for external equipment. Provided with signal 087.
088	-	-	- -	4/66/82	
091	537	-	+28COM RD091	159/40	Resistor 8R of A28 provides +28VDC for external equipment.
093	628	-	DE093 MANR+P	+EL (LPD) NC	Interface circuits 3D through 8D of A27 receive DC signals from spacecraft and feed bit positions 1 through 6 (gates 44201 through 44205, and 44215) of channel 31 (table 30-5E).
094	528	-	DE094 MANR-P	-EL (LPD) NC	
095	627	-	DE095 MANR+Y	ATT CONTROL OUT OF DET.	
096	527	-	DE096 MANR-Y	NC	
097	626	-	DE097 MANR+R	+AZ (LPD) NC	
098	526	-	DE098 MANR-R	-AZ (LPD) NC	
101	620	-	+28COM RD101	102/103/182	
102	549	-	DE102 MARK	RATE OF DESCENT (+)	Interface circuit 13D of A29 receives DC signals from the radars and feeds bit position 6 (gate 45225) of channel 16 (table 30-5J).
103	550	-	D-103 MRKRST	RATE OF DESCENT RESET	Interface circuit 11D of A29 receives DC signals from the radars and resets bit positions 6 and 7 (via gate 45233) of channel 16 (table 30-5J).
104	649	-	DE104 ZEROP	RRDATA GOOD	Interface circuits 12D, 10D, and 14D of A29 receive DC signals from the radars and feed bit positions 4, 5, and 6 (gates 44111, 44114, and 44117) of channel 33 (table 30-5G).
106	650	-	DE106 OPMSW2	LRDR RANGE DATA GOOD	
107	648	-	DE107 OPMSW3	LR POSITION 1 (DESC)	
108	152	-	YG108H } SHAFTP	+RR SHAFT	Interface circuits 7Y through 10Y of A29 receive incremental pulses from the rendezvous radar CDU's and feed the counter priority control gates 31302, 31309, 31215, and 31224, and the alarm control gates 49439 and 49440, par 30-141L.
	252	-	YG108L }		
109	151	-	YG109H } SHAFTM	-RR SHAFT	
	251	-	YG109L }		
110	150	-	YG110H } TRNP	+RR TRUNNION	
	250	-	YG110L }		
111	149	-	YG111H } TRNM	-RR TRUNNION	
	249	-	YG111L }		

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description	
	A51 of AGC	J9 of DSKY				
112	525	-	DE112	OPCDFL	RR CDU FAIL	Interface circuits 10D and 9D of A27 receive DC signals from the rendezvous radar CDU's and the ISS, and feed bit positions 7 and 14 (gates 44119 and 44139) of channel 30 (table 30-5D).
113	625	-	DE113	ISSTOR	ISS TURN ON REQUEST	
114	338 438	-	SHFTDP	{ XA114H XA114L	+RR SHAFT	Gates 48348, 48349, 48336, and 48339 of the CDU Drive Control (par 30-90 through 30-94) generate drive pulses SHFTDP, SHFTDM, TRNDP, and TRNDM which control interface circuits 8XT through 11XT of A27. The output pulses drive the rendezvous radar CDU's.
115	337 437	-	SHFTDM	{ XA115H XA115L	-RR SHAFT	
116	336 436	-	TRNDP	{ XA116H XA116L	+RR TRUNNION	
117	335 435	-	TRNDM	{ XA117H XA117L	-RR TRUNNION	
118	148 248	-	YG118H } YG118L }	CDUXP	+X CDU (OUT GMBL)	Interface circuits 11Y of module A29, and 1Y through 5Y of A28 receive incremental pulses from the IMU CDU's and feed the counter priority control gates 31102, 31109, 31115, 31124, 31202, and 31209, and the alarm control gates 49437 and 49438 (par 30-141L).
119	147 247	-	YG119H } YG119L }	CDUXM	-X CDU (OUT GMBL)	
120	146 246	-	YG120H } YG120L }	CDUY P	+Y CDU (INN GMBL)	
121	145 245	-	YG121H } YG121L }	CDUYM	-Y CDU (INN GMBL)	
122	144 244	-	YG122H } YG122L }	CDUZP	+Z CDU (MID GMBL)	
123	143 243	-	YG123H } YG123L }	CDUZM	-Z CDU (MID GMBL)	
124	624	-	DE124	CDUFAL	CDU FAIL (ISS)	
125	524	-	DE125	TEMPIN	TEMP WITHIN LIMITS	Interface circuit 12D of A27 receives DC signals from the IMU and feeds bit position 15 (gate 44142) of channel 30 and gate 45262 of the Alarm Control.

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
126	334	-	CDUXDP { XA126H XA126L	CDU +X (OUT GMBL)	Gates 48308, 48309, 48318, 48319, 48326, and 48329 of the CDU Drive Control (par 30-90 through 30-94) generate drive pulses CDUXDP through CDUZDM (column 4) which control interface circuits 4XT through 7XT of A25 and 2XT and 3XT of module A26. The output pulses drive the IMU CDU's.
127	333	-	CDUXDM { XA127H XA127L	CDU -X (OUT GMBL)	
128	332	-	CDUYDP { XA128H XA128L	CDU +Y (INN GMBL)	
129	330	-	CDUYDM { XA129H XA129L	CDU -Y (INN GMBL)	
130	329	-	CDUZDP { XA130H XA130L	CDU +Z (MID GMBL)	
131	429	-	CDUZDM { XA131H XA131L	CDU -Z (MID GMBL)	
132	328	-			
132	139	-	YG132H } PIPAX+	+DVX (STROBED)	Interface circuits 6Y through 11Y of A28 receive incremental pulses from the PIPA's and feed gates 53461, 53361, 53362, 54461, 54361, and 54362 of the Counter Priority Control. The Counter Priority Control generates signals PIPXP, PIPXM, PIPYP, PIPYM, PIPZP, and PIPZM which set the priority cells and which are fed into gates 49440 through 49442 of the Alarm Control (par 30-141L).
133	239	-	YG132L } PIPAX-	-DVX (STROBED)	
134	138	-	YG133H } PIPAY+	+DVY (STROBED)	
135	238	-	YG133L } PIPAY-	-DVY (STROBED)	
136	137	-	YG134H } PIPAZ+	+DVZ (STROBED)	
137	237	-	YG134L } PIPAZ-	-DVZ (STROBED)	
139	136	-	YG135H } PIPINT { XC139H XC139L	PIPA INTERROGATE 3200SB4	
140	236	-	YG135L } PIPASW { XC140H XC140L	PIPA SWITCHING 3200SB1	
141	135	-	YG136H } DE141 IMUFAL	IMU FAIL	Interface circuit 13D of A27 receives DC signals from the IMU and feeds bit position 13 (gate 44136) of channel 30 (table 30-5D).
	235	-	YG136L }		
	134	-	YG137H }		
	234	-	YG137L }		

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
142	121	-	GYXP { XB142H	+X GYRO SELECT	Gates 46424 through 46427, 46432, 46433, and 46443 of the Gyro Drive Control (par 30-95 through 30-99) generate drive pulses GYXP through GYZM (column 4) and reset pulses GYRRST which control interface circuits 2XT and 3XT of A27, A28, and A29, and 5XT of A27. The output pulses drive and reset the gyros in the IMU.
143	221	-	GYXM { XB142L	-X GYRO SELECT	
	120	-	GYXP { XB143H		
	220	-	GYXM { XB143L		
144	119	-	GYYP { XB144H	+Y GYRO SELECT	
	219	-	GYYP { XB144L		
145	118	-	GYYM { XB145H	-Y GYRO SELECT	
	218	-	GYYM { XB145L		
146	321	-	GYZP { XB146H	+Z GYRO SELECT	
	421	-	GYZP { XB146L		
147	320	-	GYZM { XB147H	-Z GYRO SELECT	
	420	-	GYZM { XB147L		
148	318	-	GYRRST { XA148H	GYRO RESET	
	418	-	GYRRST { XA148L		
149	323	-	800 SET { XC149H	800 PPS SET	Gates 49209 and 49210 of the Timer generate 800 pps signals which control interface circuits 10XT and 11XT of A26. The timing pulses are provided for external equipment.
	423	-	800 SET { XC149L		
150	322	-	800 RST { XC150H	800 PPS RESET	
	422	-	800 RST { XC150L		
151	316	-	3200A { XC151H	3.2 KPPS A	Gates 49211 through 49213 and 49215 of the Timer generate 3200 pps signals which control interface circuits 4XT through 7XT of A26. The timing pulses are provided for external equipment.
	416	-	3200A { XC151L		
152	315	-	3200B { XC152H	3.2 KPPS RESET B	
	415	-	3200B { XC152L		
153	-	-	3200C { XC153H	3.2 KPPS RESET C NC	
	-	-	3200C { XC153L		
154	-	-	3200D { XC154H	3.2 KPPS RESET D NC	
	-	-	3200D { XC154L		
155	117	-	12 KPPS { XC155H	12.8 KPPS PWR SUP SYNC	Gate 49217 of the Timer generates 12 kpps signals which control interface circuit 9XT of A26. The timing pulses are provided for external equipment.
	217	-	12 KPPS { XC155L		
158	534	-	DE158 CTLSAT	GUID. RELEASE NC	Interface circuits 18D of module A27 and 14D of A28 receive DC signals from the spacecraft and feed bit position 10 (gate 44128) of channel 30 (table 30-5D) and bit position 15 (gate 44235) of channel 31 (table 30-5E).
159	536	-	DE159 GCAPCL	G/N CONTROL OF S/C	

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LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings $\triangle 1$	Name Used in DD Memo 185 $\triangle 3$	Signal Description
	A51 of AGC	J9 of DSKY			
160	112	-	S4BTAK CB160	+PITCH GIMBAL TRIM	Bit positions 9, 8, and 3 (gates 43332, 43160, and 43312) of channel 12 (table 30-5A) generate signals S4BTAK, TVCNAB, and STARON which control interface circuits 20C, 23C, and 22C of A26. The interface circuits operate circuits in the spacecraft.
161	412	-	TVCNAB CB161	DISPLAY INERTIAL DATA	
162	312	-	STARON CB162	HORIZ VEL LO SCALE NC	
166	652	-	+28COM RD166	+28V COMP. (TP)	Resistor 6R of A29 provides +28VDC for external equipment.
167	{ 161 261 361 461 561 661	-	WD167	+28 A BUSS	AGC power inputs connected to power supplies, modules 30 and 31.
168	{ 159 259 359 459 559 659	-	WD168	+28 A BUSS	
169	{ 160 260 360 460 560 660	-	WD169	0V A BUSS	
171	646	-	DE171 MNIM+P	THRUSTER 4D/4S FAIL	Interface circuits 17D and 18D of A29 and 1D and 2D of A28 receive DC signals from the spacecraft and feed bit positions 1 through 4 (gates 44236, 44238, 44240, and 44242) of channel 32 (table 30-5F).
172	546	-	DE172 MNIM-P	THRUSTER 3U/3S FAIL	
173	645	-	DE173 MNIM+Y	THRUSTER 4U/4F FAIL	
174	545	-	DE174 MNIM-Y	THRUSTER 3D/3F FAIL	
175	552	-	+4VDC RD175	4VA (TP)	Resistors 7R, 8R, and 9R of A29 provide +4VDC, BPLUS, and 0VDC for external equipment.
176	651	-	BPLUS RD176	14VA (TP)	
177	551	-	0VDC RD177	0V (TP)	

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TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle 1	Name Used in DD Memo 185 \triangle 3	Signal Description	
	A51 of AGC	J9 of DSKY				
182	548	-	DE182	MRKREJ	RATE OF DESCENT (-)	Interface circuits 15D and 16D of A29 and 15D of A27 receive DC signals from the radars and the IMU and feed bit position 7 (gate 45229) of channel 16 (table 30-5J), bit positions 7 (gate 44120) of channel 33 (table 30-5G), and bit position 9 (gate 44125) of channel 30 (table 30-5D).
183	547	-	DE183	STRPRS	LR POSITION 2 (HOVER)	
184	622	-	DE184	IMUOPR	IMU OPERATE	
185	313	-	ZOPCDU	CB185	ZERO REN RDR CDU	Bit positions 1 and 2 (gates 43302 and 43310) of channel 12 (table 30-5A) generate signals ZOPCDU and ENEROP which control interface circuits 16C and 13C of A26. The interface circuits operate the rendezvous radar CDU's and the rendezvous radar.
186	614	-	ENEROP	CB186	ERR CNTR ENABLE REN RDR	
188 189	644 544	- -	DE188 DE189	MNIM+R MNIM-R	THRUSTER 1D/1S FAIL THRUSTER 1U/1F FAIL	Interface circuits 3D and 4D of A28 receive DC signals from the spacecraft and feed bit positions 5 and 6 (gates 44244 and 44237) of channel 32 (table 30-5F).
191	515 615	-	CDUCLK	{ XC191H XC191L	CDU CLOCK (51.2 KPPS)	Gate 49223 of the Timer generates pulses CDUCLK which control interface circuit 8XT of A25. The 51.2 kpps signals are provided for the CDU's.
192	317 417	-	GYENAB	{ XB192H XB192L	GYRO COMM ENABLE	Gate 46434 of the Gyro Drive Control (par 30-95 through 30-99) generates pulses GYENAB which control interface circuit 1XT of A26. Output pulses control gyros in the IMU.
193	522	-	DE193	IMUCAG	IMU CAGE	Interface circuit 16D of A27 receives DC signal from IMU and feeds bit position 11 (gate 44130) of channel 30 (table 30-5D).
194 195 196	514 414 314	- - -	COARSE ZIMCDU ENERIM	CB194 CB195 CB196	COARSE ALIGN ENABLE ZERO IMU CDU'S ERR CNTR ENABLE IMU	Bit positions 4, 5, and 6 (gates 43320, 43322, and 43330) of channel 12 (table 30-5A) generate signals COARSE, ZIMCDU, and ENERIM which control interface circuits 12C, 11C, and 10C of A26. The interface circuits operate the IMU mode control.
197	325 425	-	PIPDAT	{ XC197H XC197L	PIPA DATA PULSE 3200SB2	Gate 49206 of the Timer generates PIPDAT pulses which control interface circuit 11XT of A25. The 3.2 kpps signal is fed into PIPA's.

(cont)

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TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings $\triangle 1$	Name Used in DD Memo 185 $\triangle 3$	Signal Description
	A51 of AGC	J9 of DSKY			
198	319 419	-	GYRSET { XA198H XA198L	GYRO SET	Gate 46442 of the Gyro Drive Control (par 30-95 through 30-99) generates pulses GYRSET which control interface circuit 4XT of A27. Output pulses operate gyros.
199	102 202	-	RD199H RD199L	TEMP MON 1	Connected only to pins 309 and 409 of both plugs A62 and B62.
201	601	-	DE201 NKEY1 or KEY1	NC	DC signals DE201 through DE205 are received by interface circuits 4D through 8D of A25 which feed bit positions 1 through 5 (gates 45201, 45205, 45209, 45213, and 45217) of channel 16 (table 30-5J).
202	501	-	DE202 NKEY2 or KEY2	NC	
203	401	-	DE203 NKEY3 or KEY3	MARK X (AOT)	
204	301	-	DE204 NKEY4 or KEY4	MARK Y (AOT)	
205	201	-	DE205 NKEY5 or KEY5	REJECT MARK (AOT)	
206	103	49	D-206 MAINRS or KEYRST	KYBD RESET	DC signals D-206 through D-209 are generated by the keyboard of the DSKY and the spacecraft, and received by interface circuits 1D, 2D, 3D, and 9D of A25. Signal D-207 is caused by pressing the STBY key and operates the Standby Control (gate 45141, par 30-141AJ through 30-141AM). Signal D-208 is caused by pressing the RSET key and operates the start-stop logic (gate 45222) to reset the restart flip-flop (gates 41237/41238, par 30-131B). Signal D-206 is caused by pressing any key on the DSKY, except key STBY, and resets channel 15. Signal D-209 resets bit positions 1 through 5 of channel 16 (table 30-5J).
207	502	28	D-207 SBYBUT or STBY	STANDBY	
208	402	48	D-208 CAURST or RSET	RESET (LGHT)	
209	101	-	D-209 NAVRST or KEYRST	MARK RESET (AOT)	
210	115	75	W-210	SPARE	Spare connection.
211	302	30	+28COM RD211	+28 KYBD	Resistors 1R, 2R, and 3R of A25 in parallel provide +28VDC for the DSKY.
212	504	-	ELSNCN CC212	NC	Gate 49249 of the Timer generates signal ELSNCN (800 pps) which controls interface circuit 34C of A25.
213	-	74	W-213	SPARE	Spare pin in DSKY.

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle		Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY				
214	404	2	RLYB01	CE214	CHN 10-1	Bit positions 1 through 15 (gates 44305, 44311, 44317, 44323, 44329, 44335, 44341, 44347, 44353, 44359, 44405, 44411, 44417, 44423, and 44429) of channel 10 (table 30-5) generate signals RLYB01 through RYWD16 (column 4) which control interface circuits 33C through 19C of A25. The output DC signals operate the relay matrix in the DSKY (par 30-145A and 30-145B).
215	304	9	RLYB02	CE215	CHN 10-2	
216	204	22	RLYB03	CE216	CHN 10-3	
217	605	41	RLYB04	CE217	CHN 10-4	
218	505	66	RLYB05	CE218	CHN 10-5	
219	405	3	RLYB06	CE219	CHN 10-6	
220	305	10	RLYB07	CE220	CHN 10-7	
221	205	23	RLYB08	CE221	CHN 10-8	
222	606	42	RLYB09	CE222	CHN 10-9	
223	506	67	RLYB10	CE223	CHN 10-10	
224	406	12	RLYB11	CE224	CHN 10-11	
225	306	11	RYWD12	CE225	CHN 10-12	
226	206	24	RYWD13	CE226	CHN 10-13	
227	607	43	RYWD14	CE227	CHN 10-14	
228	507	68	RYWD16	CE228	CHN 10-15	
229	407	27	ISSWAR	CB229	ISS WARNING	Bit positions 1 and 2 (gates 43401 and 43412) of channel 11 (table 30-5) generate signals ISSWAR and COMACT which control interface circuits 18C and 17C of A25. The interface circuits operate relays ISS WARNING and COMP ACTY (par 30-145C).
230	307	26	COMACT	CB230	COMPUTER ACTIVITY	
231	207	25	SBYLIT or STBY	CB231	STANDBY LGHT	Gate 45157 of the Standby Control generates signal SBYLIT which controls interface circuit 16C of A25. The interface circuit operates relay STBY (par 30-145C).
232	608	44	RESTRT	C-232	RESTART	Gate 41240 of the start-stop logic (par 30-131B) generates signal RESTRT which controls interface circuit 15C of A25. The interface circuit operates relay RESTART (par 30-145C).
233	508	69	S4BSEQ	CB233	LRDR POS CMD	Bit positions 13 and 14 (gates 43451 and 43460) of channel 12 (table 30-5A) generate signals S4BSEQ and S4BOFF which control interface circuits 14C and 13C of A25. The interface circuits operate relays INJ SEQ START and CUTOFF (par 30-145C).
234	408	18	S4BOFF	CB234	RR ENABLE LOCK ON	

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
235	308	47	UPLACT CB235	UPLNK ACTIVITY	Bit positions 3 and 5 (gates 43413 and 43426/43427) of channel 11 (table 30-5) generate signals UPLACT and KYRLS which control interface circuits 12C and 11C of A25. The interface circuits operate relays UPLINK ACTY and KEY REL (par 30-145C).
236	208	46	KYRLS CB236	KEY RELEASE (FLASH)	
237	604	45	CGCWAR C-237	LGC WARNING	Gate 41227 of the Alarm Control generates signal CGCWAR which controls interface circuit 10C of A25. The interface circuit operates relay CIRCUIT (par 30-145C).
238	609	70	VNFLSH CB238	VERB/NOUN (FLASH)	Bit position 6 (gates 43435/43438) of channel 11 (table 30-5) generates signal VNFLSH which controls interface circuit 9C of A25. The interface circuit operates relay FLASH (par 30-145C).
239	{ 104 105	8	0VDC or SIGNAL GRD WD239	DISKY GRND	Ground and +14V connection between AGC and DSKY.
240		1	BPLSSW or +14VA WD240	14V B (SWITCHED)	
241	116	73	W-241	SPARE	Spare line.
242	509	7	ELSNM CC242	POWER SYNC M	Gate 49250 of the Timer generates signal ELSNMC (800 pps) which controls interface circuit 8C of A25. The interface circuit pulses the DSKY power supply.
243	106	21	WD243 +28COM or +28V	+28V (M)	+28VDC output provided by DSKY. See signal 355.
244	409	72	OPEROR CB244	OPERATOR ERROR (FLASH)	Bit position 7 (gates 43440/43441) of channel 11 (table 30-5) generates signal OPEROR which controls interface circuit 7C of A25. Interface circuit operates relay OPR ERROR (par 30-145C).
246	-	13	W-246	SPARE	Spare pin on DSKY.
251	602	-	+28COM RD251	+28 (203-205/209)	Resistors 1R, 2R, and 3R of A26 in parallel provide +28VDC.

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description	
	A51 of AGC	J9 of DSKY				
253 254 255 256 257	603 503 403 303 203	51 78 77 76 79	DE253 MKEY1 DE254 MKEY2 DE255 MKEY3 DE256 MKEY4 DE257 MKEY5	KEY CODE 1 KEY CODE 2 KEY CODE 3 KEY CODE 4 KEY CODE 5	DC signals DE253 through DE257 are generated by the keyboard of the DSKY (pressing any key except key STBY) and received by interface circuits 6D through 10D of A26 which feed bit positions 1 through 5 (gates 45101, 45105, 45109, 45113, and 45117) of channel 15 (table 30-5).	
258	309	71	TMPCAU CB258	TEMP CAUTION	Gate 41230 of the Alarm Control generates signal TMPCAU when signal TMPOUT or signal TEMPIN/ is present. Signal TMPCAU controls interface circuit 6C of A25 which operates relays TEMP CAUTION (par 30-145C). Signal TMPOUT (temperature out of limits) is supplied by bit position 4 of channel 11; signal TEMPIN (temperature within limits) is supplied by line 125 which also feeds bit position 15 of channel 30.	
407A 407C 407O	- - -	37 19 \triangle 61	SD407A } WD407C } WD407O }	INJ SEQ START	LRDR POS CMD (HOVER) None LR HOVER POS RET	Connections to contacts of relay INJ SEQ START which is operated by signal 233.
409A 409C 409O	- - -	36 85 \triangle 60	SD409A } WD409C } WD409O }	CUTOFF	RR AUTO TRK ENABLE RR AUTO ANGLE ENABLE RET None NC	Connections to contacts of relay CUTOFF which is operated by signal 234.
437 438	- -	38 40	W-437 W-433	115V 400 ~ 115V 400 ~ RET	115V VARIABLE 400 CPS SIG 115V VARIABLE 400 CPS RET	115VAC power for illumination of keys.
439A 439C	- -	83 54	SD439A } WD439C }	ISS WARNING	ISS WARNING LGHT None NC	Connections to contacts of relay ISS WARNING which is operated by signal 229.
440A 440C	- -	31 \triangle 53 \triangle	SD440A WD440C	G&N CAUTION RESTART	PGNS CAUTION LGHT NC	Line 440A is connected to contacts of relays GIMBAL LOCK, TRACKER, PROG CAUTION, RESTART, and TEMP CAUTION (par 30-145B and 30-145C) which control the G&N caution indicator. Line 440C is connected to a contact of relay RESTART. Line 450 is connected to contacts of relays GIMBAL LOCK, TRACKER, PROG CAUTION, RESTART, TEMP CAUTION, CIRCUIT, and ISS WARNING.

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUT PUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
441A 441O	- -	84 55	SD441A } WD441C } CIRCUIT	LGC WARNING LGHT NC	Connections to contacts of relay CIRCUIT which is operated by signal 237. Contacts control AGC warning indicator on the condition annunciator.
442 443	- -	5 4	RD442 DIMMER WD443 DIMMER	DSKY DIMMER CONTROL SIG DSKY DIMMER CONTROL RET	Connections from power supply to dimmer control.
444A 444C 444O	- - -	56 16 33	SD444A } WD444C } SPARE WD444O }	SPARE (1100-5) (WL) NC None NC None NC	Connections to contacts of relay SPARE of bank 14 which is controlled by bit 5 (par 30-145B).
445A 445C 445O	- - -	34 6 17	SD445A } WD445C } SPARE WD445O }	SPARE (1100-7) NC None NC None NC	Connections to contacts of relay SPARE of bank 14 which is controlled by bit 7 (par 30-145B).
446 447 448 449	- - - -	62 63 64 65	WD446 5V CAUT POWER (LVYHI) WD447 5V CAUT POWER RET (LVYLO) WD448 5V STATUS POWER (LVWHI) WD449 5V STATUS POWER RET (LVWLO)	5V CAUT LGHT PWR HI 5V CAUT LGHT PWR LO 5V STATUS LGHT PWR HI 5V STATUS LGHT PWR LO	5VDC power for illumination of indicators.
450	-	57	WD450 ALARM COMMON	RETURN 439-441	See lines 440A and 440C.
451A 451C 451O	- - -	32 14 15	SD451A } WD451C } CIRCUIT WD451O }	AGC WARNING (LMP) INHIBIT PIPA (PLS TORQ) (LMP/) None NC	Connections to contacts of relay CIRCUIT which is operated by signal 237.

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description	
	A51 of AGC	J9 of DSKY				
452A 452C 452O	- - -	81 \triangle 82 \triangle 80 \triangle	SD452A } WD452C } WD452O }	ISS WARNING (LMP) None None	NC NC NC	Connections to contacts of relay ISS WARNING which is operated by signal 229.
453A 453C 453O	- - -	35 59 58	SD453A WD453C WD453O	STANBY None None	NC NC NC	Connections to contacts of relay STBY which is operated by signal 231.
801 802 803 804 805 806 807 808 809 810 811 812 813 814 815 816	209 109 610 510 410 310 210 110 611 511 411 311 211 111 612 512	- - - - - - - - - - - - - - - -	CB801 CB802 CB803 CB804 CB805 CB806 CB807 CB808 CB809 CB810 CB811 CB812 CB813 CB814 CB815 CB816	RC+X+P RC+X-P RC+X+Y RC+X-Y RC-X+P RC-X-P RC-Y+X RC-X-Y RC+Y+R RC+Y-R RC-Y+R RC-Y-R RC+Z+R RC+Z-R RC-Z+R RC-Z-R	-X/-P/+R, 4U No. 1 +X/-R/-P, 3D No. 6 -X/+P/-R, 2U No. 9 +X/+R/+P, 1D No. 14 -X/+R/+P, 3U No. 5 +X/+P/-R, 4D No. 2 -X/-R/-P, 1U No. 3 +X/-P/+R, 2D No. 10 +Y/+YAW, 2S No. 12 +Y/-YAW, 1S No. 16 -Y/+YAW, 4S No. 4 -Y/-YAW, 3S No. 8 +Z/+YAW, 3F No. 7 +Z/-YAW, 2F No. 11 -Z/+YAW, 1F No. 15 -Z/-YAW, 4F No. 3	Bit positions 1 through 8 of channels 5 and 6 (gates 43105, 43111, 43117, 43123, 43129, 43135, 43141, 43147, 43259, 43254, 43249, 43244, 43239, 43234, 43223, and 43205) generate signals RC+X+P through RC-Z-R (column 4) which control interface circuits 5C through 1C of A25 and 34C through 24C of A26 which operate the reaction control system.
817 818 819 820 821 822	158 258 157 257 156 256 155 255 154 254 153 253	- - - - - - - - - - - -	YG817H } YG817L } YG818H } YG818L } YG819H } YG819L } YG820H } YG820L } YG821H } YG821L } YG822H } YG822L }	BMGXP BMGXM BMGYP BMGYM BMGZP BMGZM	NC NC NC NC NC NC	Interface circuits 1Y through 6Y of A29 receive pulses from the body-mounted acceleration gyros and feed gates 46337, 46338, 46346, 46347, 46356, and 46357 of the BMAG/RHC Control (par 30-110 through 30-112).
831 832	643 543	- -	DE831 DE832	TRST9 TRST10	THRUSTER 2U/2S FAIL THRUSTER 2D/2F FAIL	Interface circuits 5D and 6D of A28 receive DC signals from the spacecraft and feed bit position 7 and 8 (gates 44239 and 44241) of channel 32 (table 30-5F).

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(cont)

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TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings \triangle	Name Used in DD Memo 185 \triangle	Signal Description
	A51 of AGC	J9 of DSKY			
840	621	-	DE840 IN3301	SPARE CHN 33-01 NC	Interface circuit 17D of A27 is able to receive DC signals and feeds bit position 1 (gate 44102) of channel 33 (table 30-5G). Not used, spare.
841	213	-	MROLGT CB841	-ROLL GIMBAL TRIM	Bit position 12 (gate 43450) of channel 12 (table 30-5A) generates signal MROLGT which controls interface circuits 15C of A26. The interface circuit operates external equipment
842 843	532 632	- -	DE842 PCHGOF DE843 ROLGOF	PITCH GIMBAL OFF ROLL GIMBAL OFF	Interface circuits 16D and 15D of A28 receive DC signals from the spacecraft and feed bit positions 9 and 10 (gates 44243 and 44245) of channel 32 (table 30-5F).
844 845	- -	- -	DE844 IN3214 DE845 IN3216	SPARE CHN 32-14 NC SPARE CHN 32-16 NC	Interface circuits 5D of A26 and 10D of A26 are able to receive DC signals and feed bit positions 14 and 15 (gates 44140 and 44143) of channel 32 (table 30-5F). Not used, spares.
848 850 851 852 853 854 855	- - - - - - -	- - - - - - -	OT1108 CB848 OT1110 CB850 OT1111 CB851 OT1112 CB852 OT1113 CB853 OT1114 CB854 OT1116 CB855	SPARE CHN 11-08 NC SPARE CHN 11-10 NC SPARE CHN 11-11 NC SPARE CHN 11-12 NC SPARE CHN 11-13 NC SPARE CHN 11-14 NC SPARE CHN 11-16 NC	Bit positions 8 and 10 through 15 (gates 48422, 49252, 49253, 49254, 48427, 48432, and 48437) of channel 11 (table 30-5) generate signals OT1108 and OT1110 through OT1116 (column 4) which control interface circuits 6C through 1C and 9C of A26. Not used, spares.
858 859 860	142 242 141 241 140 240	- - - -	A-858H } A-858L } A-859H } A-859L } A-860H } A-860L }	PROP PITCH RATE CMD PROP ROLL RATE CMD PROP YAW RATE CMD	Each line is connected to an RHC converter (one each in A27, A28, and A29). Each feeds the BMAG/RHC Control (figure 30-6 and par 30-113 through 30-115).
861 863 865 866 867 868 869 870	520 637 539 635 535 634 642 619	- - - - - - - -	+28COM RD861 +28COM RD863 +28COM RD865 +28COM RD866 +28COM RD867 +28COM RD868 +28COM RD869 +28COM RD870	160/841/902/906 83 173/174 188/189 831/832 NC 67/68 NC	Resistor 7R of A27 Resistor 7R of A28 Resistor 6R of A28 Resistor 9R of A28 Resistor 1R of A27 Resistor 2R of A27 Resistor 3R of A28 Resistor 8R of A27 } provide +28VDC

(cont)

TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

Line or Signal Number	Connecting Pins at		Name Used on Drawings $\triangle 1$	Name Used in DD Memo 185 $\triangle 3$	Signal Description
	A51 of AGC	J9 of DSKY			
874 875 876 877 878 879	658 - 633 656 533 519	- - - - - -	+28COM RD874 - +28COM RD876 +28COM RD877 +28COM RD878 +28COM RD879	104/64/69 104/64/69 106/107/183 842/843 39/61 7/158 NC	Resistor 1R and 2R of A29 in parallel - Resistor 3R of A27 Resistor 3R of A29 Resistor 4R of A27 Resistor 9R of A27 provide +28VDC
901	324 424	-	25 KPPS { XC901H XC901L	25.6 KPPS (PWR SUP SYNC)	Gate 49220 of the Timer generates 25KPPS pulses which control interface circuit 8XT of A26. Pulses are provided to operate power supplies in the spacecraft.
902	212	-	DISDAC CB902	+ROLL GIMBAL TRIM	Bit position 11 (gate 43342) of channel 12 (table 30-5A) generates signal DISDAC which controls interface circuit 21C of A26. The interface circuit operates the IMU.
903 904	521 542	- -	+28COM RD903 +28COM RD904	14VB (AGC OPERATE) 171/172	Resistors 5R of A27 and 4R of A28 provide +28VDC.
906	113	-	ZEROPT CB906	-PITCH GIMBAL TRIM	Bit position 10 (gate 43340) of channel 12 (table 30-5A) generates signal ZEROPT which controls interface circuit 14C of A26. The interface circuit operates the IMU.
907	556	-	RD907 +28COM	18-23	Resistors 4R and 5R of A29 in parallel provide +28VDC.
908	-	-	-	18-23	
909	114	-	ISSTDC CB909	ISS TURN ON DELAY CMPLT	Bit position 15 (gate 48445) of channel 12 (table 30-5A) generates signal ISSTDC which controls interface circuit 8C of A26. The interface circuit operates the ISS.
910 911	216 215	- -	W-910 W-911	CNTRL 1 (ACE) (LOW) CNTRL 2 (ACE) (HIGH)	

(cont)

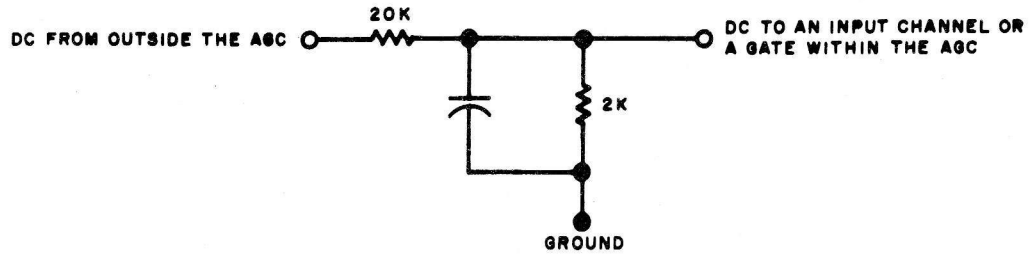
TABLE 30B-1

LUNAR EXCURSION MODULE AGC SUBSYSTEM INPUT AND OUTPUT SIGNALS (cont)

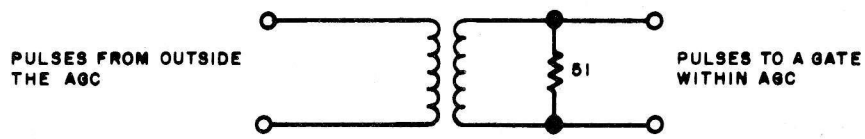
Line or Signal Number	Connecting Pins at		Name Used on Drawings ^{△1}	Name Used in DD Memo 185 ^{△3}	Signal Description
	A51 of AGC	J9 of DSKY			
912	214	-	D-912 NHVFAL	INHIBIT POWER FAIL	Interface circuit 1D of A26 receives DC signals from spacecraft and feeds gates 41204 and 41205 of the Alarm Control (par 30-141R).
<p>^{△1} Pin numbers and signal names were taken from NASA drawings 2005020, 2005021, 2005900, and 2005950; and from the AGC BLOCK II COMPUTER WIRE LIST.</p> <p>^{△2} For DSKY's 200B, 200C, 200M, and 600M, pin numbers apply to 85 pin connector of adapter cable.</p> <p>^{△3} NC means AGC not connected to external equipment.</p>					

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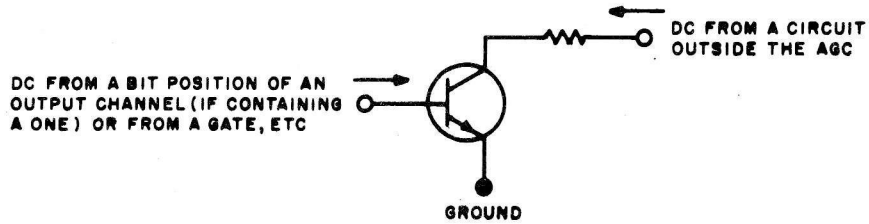
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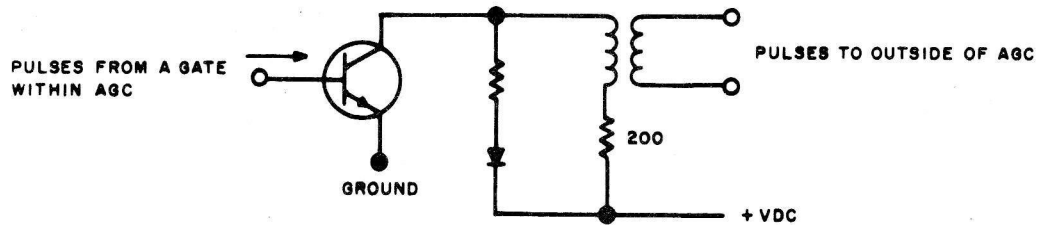
INPUT CIRCUIT D



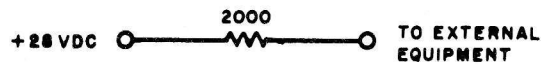
INPUT CIRCUIT Y



OUTPUT CIRCUIT C



OUTPUT CIRCUIT XT



OUTPUT CIRCUIT R

Figure 30B-1. Interface Circuits

