MIT/IL
Guidance and Navigation
System Test Group Memo No.: 478

To: Distribution

From: A. Laats

Subject: ERASABLE MEMORY ZEROING PROGRAM

Date: 27 August 1965

The possibility of having a bad parity erasable word when the AGC
is initially turned on requires that the erasable memory be exercised
after turn-on and prior to conducting an AGC self-check. An AGC
self-check with bad parity erasable will result in a failure of the test
even though there is no failure of the AGC.

The erasable memory zeroing program initially written resulted in
a parity fail at the end of test. This parity fail was due to a +zero
being loaded into the priority registers which results in addressing of
a non-existent location when the executive program scans the priority
registers at the end of the job. (Subroutine EJSCAN)

To eliminate the confusion resulting from getting any fail indication
that isn't a fail indication, the program will be changed below to ter-
minate with a "fresh start" instead of an "end of job."

The program should be changed to be as follows:

1)	V36 Enter		11)	00053	E, E
2)	V21 N 01, E		12)	00055	E, E
3)	00044	E	13)	50044	E, E
4)	01716	E, E	14)	00045	E, E
5)	00045	E	15)	02516	E, E
6)	35500	E	16)	10744	E
7)	N15	E	17)	V21 N 26	E
8)	20044	E, E	18)	02000	E
9)	50057	E, E	19)	V20 N 01	E
10)	10044	E, E	20)	00045	E

Ain Laats

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