

## Massive Scaling of MASSIF: Algorithm Development for Hooke's Law Simulations on Distributed GPU systems

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## GPUs and the need for algorithm re-design

### **Motivation**

Common characteristics of scientific codes:

 FFT-based simulations involve all-to-all communication

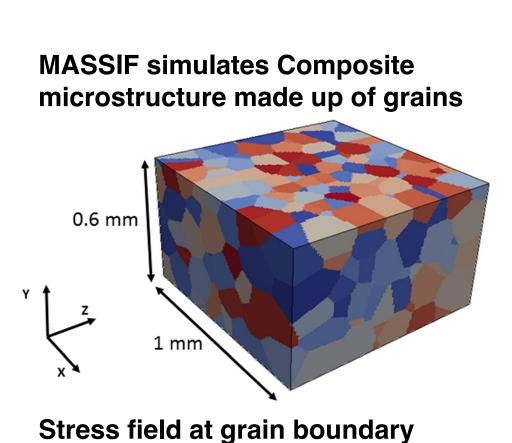
High memory requirement

### Incompatibility with GPUs:

- GPUs haves small on-chip memory (~16GB)
- Various communication latencies

### Case study: MASSIF

- Partial Differential Equation solved by Green's function method
- FFT-based convolution and tensor contraction between rank-2 tensors and rank-4 Green's
- function



### Main issues faced:

As problem size increases for high resolution simulations, storage and communication requirements increase. Problems larger than 10243 grids not currently simulated.

 $\epsilon^0 \leftarrow E, \quad \sigma_{mn}^0(\mathbf{x}) \leftarrow C_{mnk\ell}(\mathbf{x}) : \epsilon_{k\ell}^0(\mathbf{x})$ 

Update strain:  $\hat{\epsilon}_{k\ell}^{(i+1)}(\boldsymbol{\xi}) \leftarrow \hat{\epsilon}_{k\ell}^{(i)}(\boldsymbol{\xi}) - \Delta \hat{\epsilon}_{k\ell}^{(i+1)}(\boldsymbol{\xi})$ 

Update stress:  $\sigma_{mn}^{(i+1)}(\mathbf{x}) \leftarrow C_{mnk\ell}(\mathbf{x}) : \epsilon_{k\ell}^{(i+1)}(\mathbf{x})$ 

Algorithm 1 MASSIF Inner loop

 $\hat{\sigma}_{mn}^{(i)}(\xi) \leftarrow \text{FFT}(\sigma_{mn}^{(i)}(\mathbf{x}))$ 

 $\Delta \hat{\epsilon}_{k\ell}^{(i+1)}(\boldsymbol{\xi}) \leftarrow \hat{\Gamma}_{k\ell mn}(\boldsymbol{\xi}) : \hat{\sigma}_{mn}^{(i)}(\boldsymbol{\xi})$ 

 $\epsilon_{k\ell}^{(i+1)}(\mathbf{x}) \leftarrow \mathrm{iFFT}(\hat{\epsilon}_{k\ell}^{(i+1)}(\boldsymbol{\xi}))$ 

Check convergence

1: Initialize:

2: **while**  $e_s > e_{\text{tol}}$  **do** 

### **Proposed Solution:**

Re-design the application algorithm using domain expertise and knowledge about high performance computing platforms.



Application

algorithm re-design

**HPC Knowledge** 

Compute power

Memory capacity

Communication bandwidth

Some Issues: No native support for accelerators (GPUs, Xeon

interface:

Challenges with **FFTW** 

PHI, FPGAs) and SIMT Parallel/MPI version does not scale beyond 32 nodes

FFTW is de-facto standard interface for FFT

Intel MKL, IBM ESSL, AMD ACML (end-of-life),

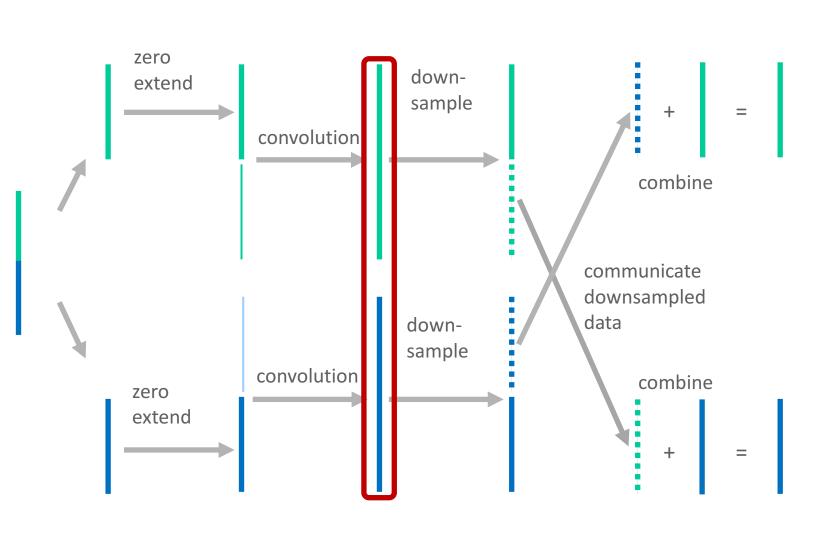
Vendor libraries support the FFTW 3.X

Nvidia cuFFT, Cray LibSci/CRAFFT

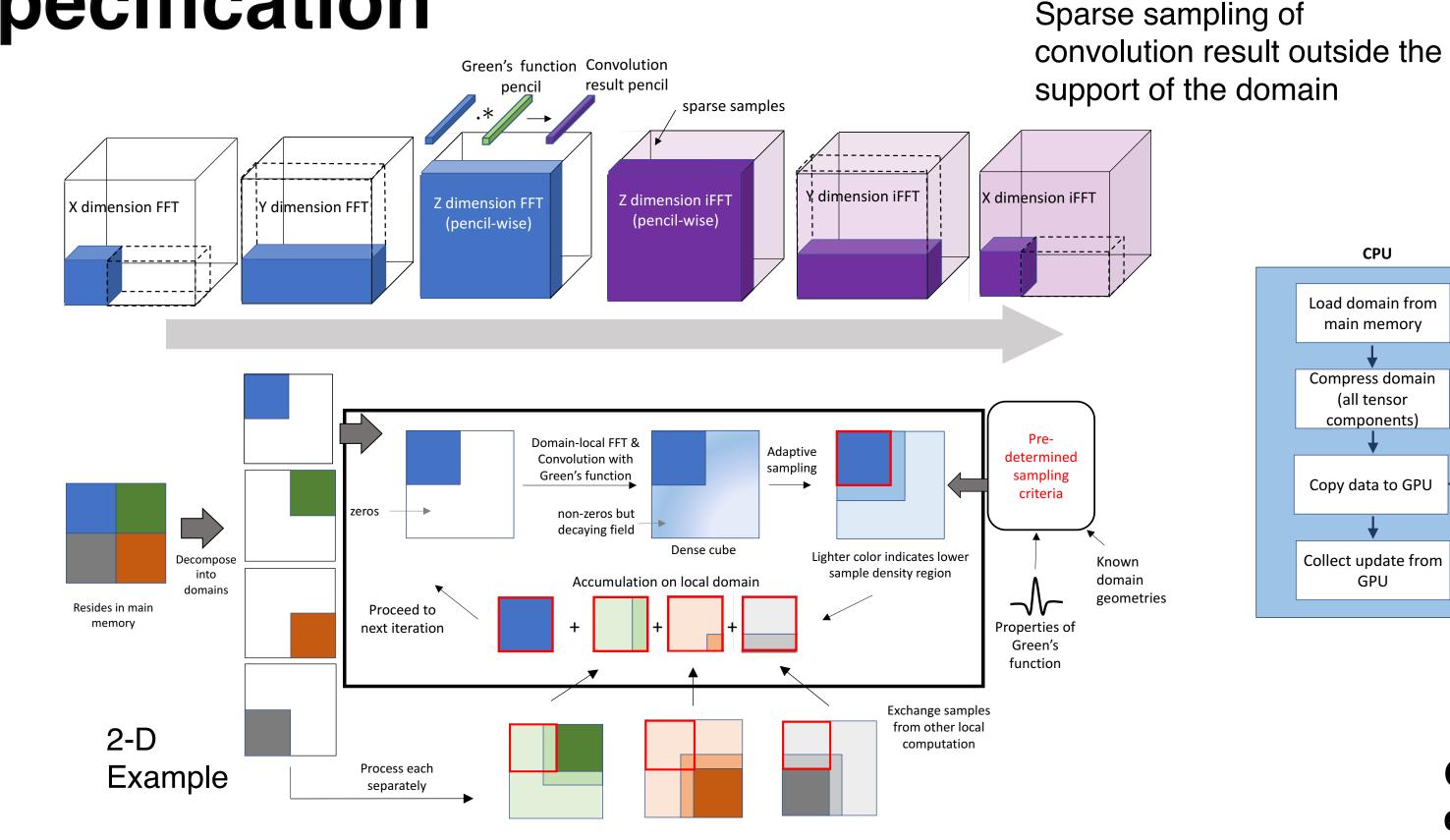
- No analogue to LAPACK for spectral method
- Complex data patterns may need to be expressed, FFTW currently falls short. But, extensions like FFTX could add new descriptors.

**Emerging interfaces like FFTX, extension of FFTW,** enables algorithm specification as composition of sub-

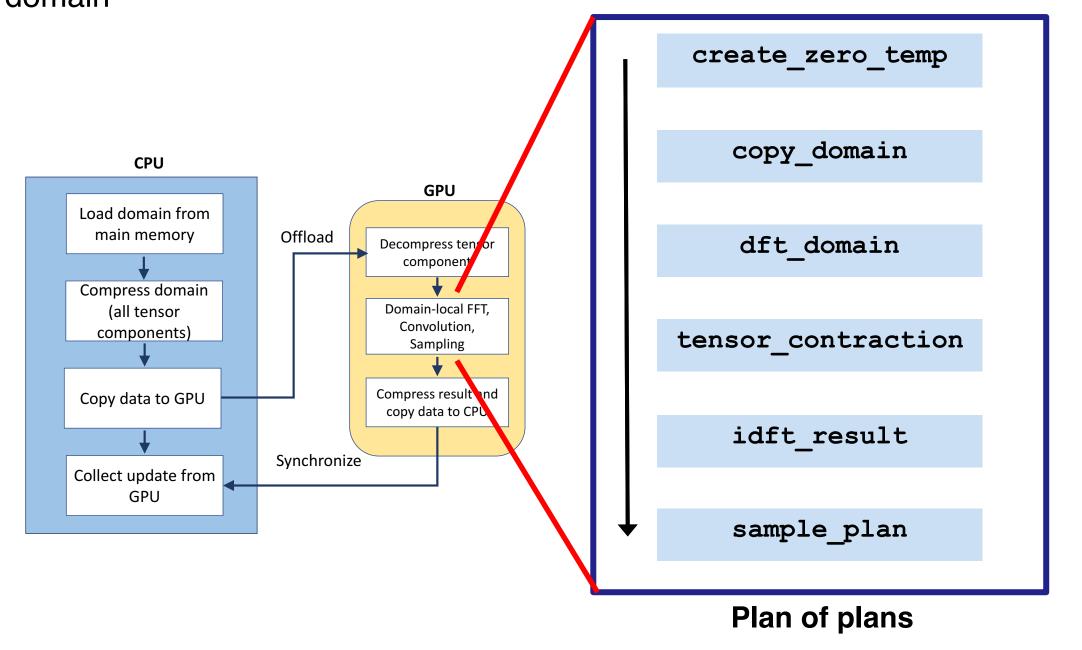
# Front end: Algorithm Specification



**Key Idea: Approximate Convolution.** Exploit locality arising due to domain decomposition + Properties of Green's function.



### Making algorithm specification easier for the user



Goal: Domain decomposition and sampling expressed easily using an FFTW-like interface

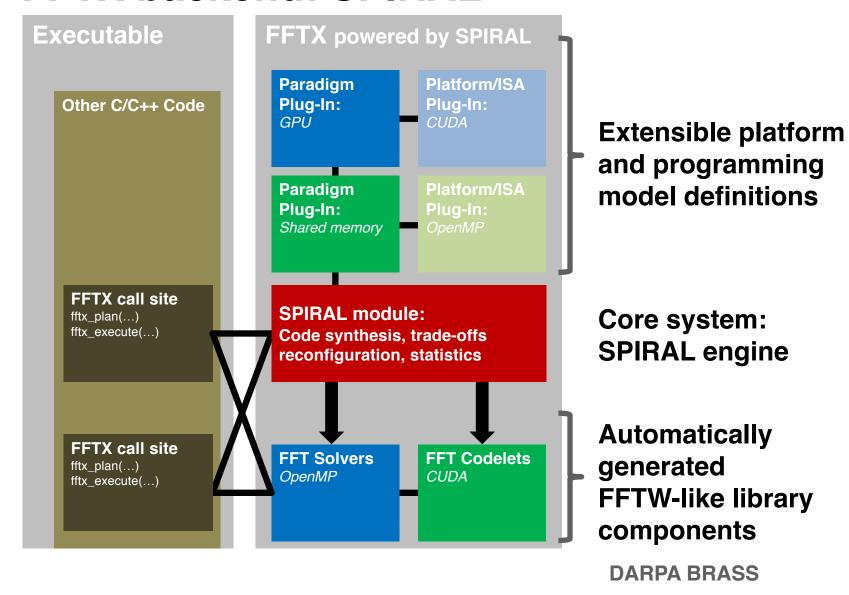
Translating an OL expression into code

### Back end: Code Optimization

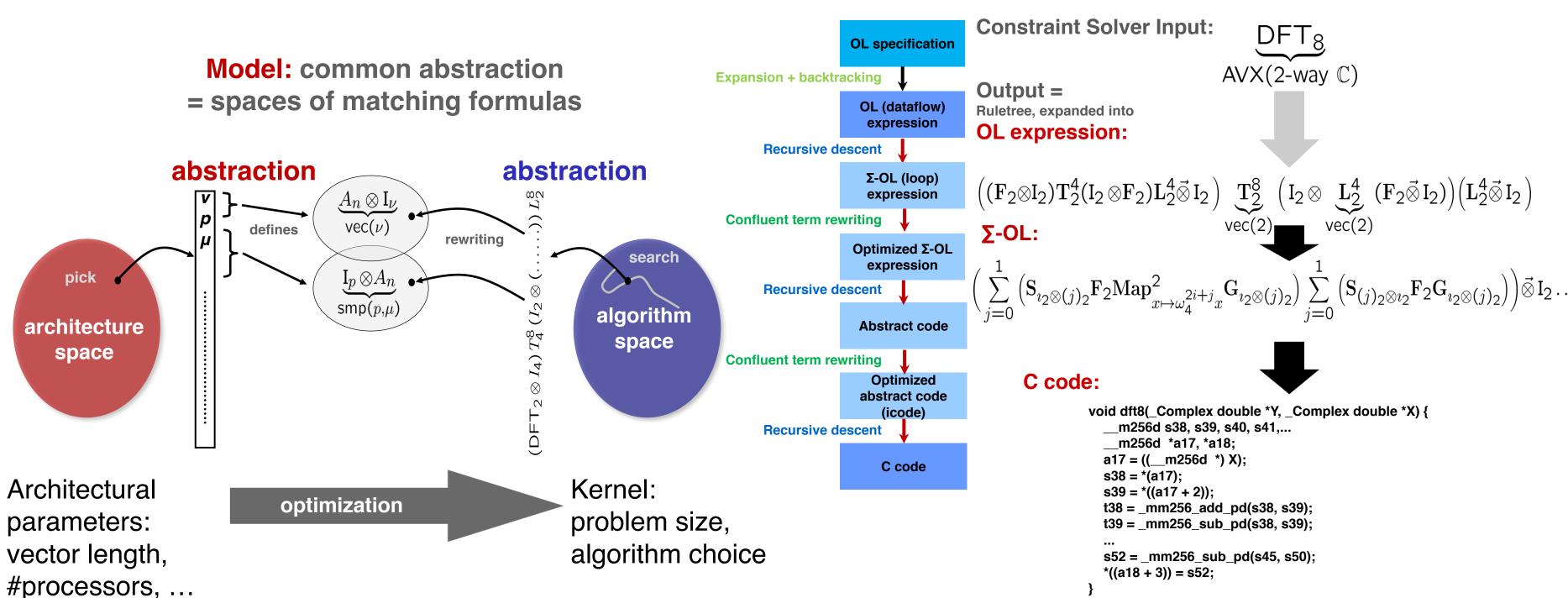
### FFTX is..

- Modernized FFTW-style interface
- Backwards compatible to FFTW 2.X and 3.X
- Small number of new features, familiar interface
- Code generation backend using **SPIRAL**
- Compilation and advanced performance optimization cross-call and cross library optimization, accelerator offloading,...

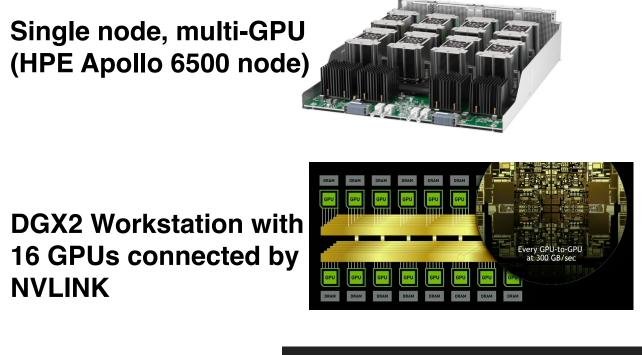
### FFTX backend: SPIRAL



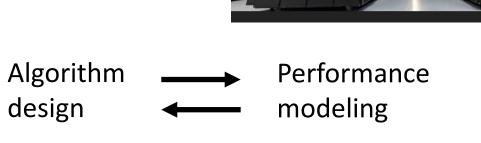
### Platform-aware formal program synthesis



### First-order Performance Analysis

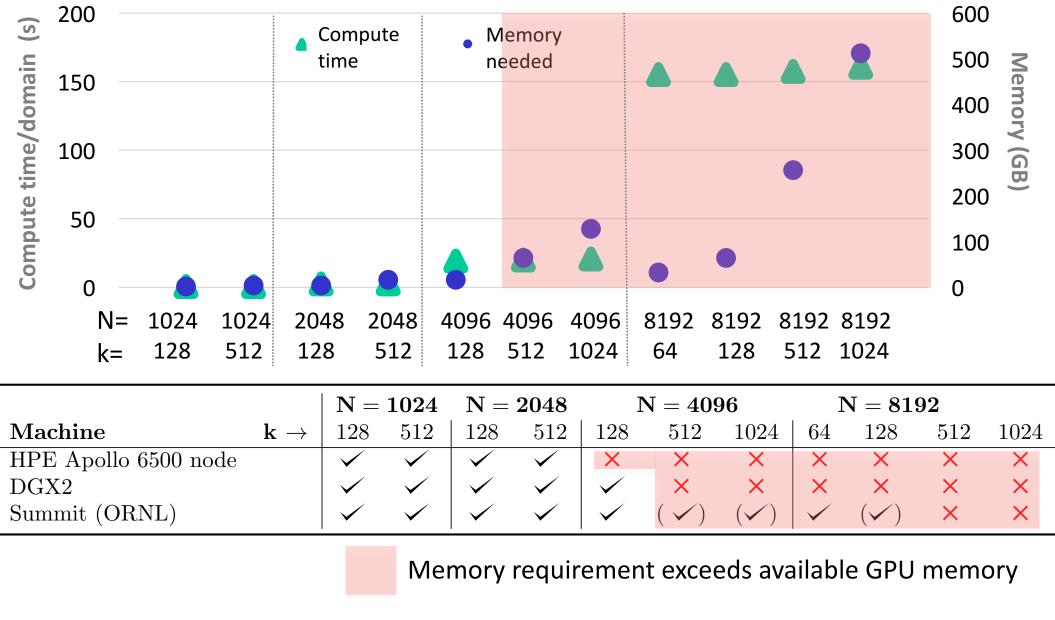


Summit supercomputer: many-node, with multiple GPUs/node.

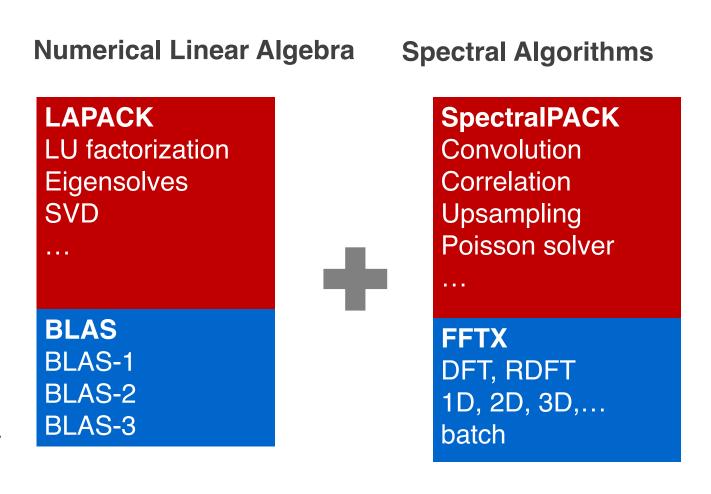


### Comparison of communication time and compute time Time in log(s) for problem size of 1024 x 1024 x 1024 Nvidia Tesla V100 movement time Compressed data novement time GPU compute 512 **Domain Size** Intra-node and Inter-node communication times Time in log(s) for communication of octree samples, 1024 x 1024 x 1024 grid \_\_\_ PCIE Infiniband (intra-node) (inter-node) 0.001 256 512

### **GPU** compute time & Memory requirement



### Future work: FFTX and SpectralPACK



- LAPACK for spectral algorithms
- Define FFTX as the analogue to BLAS
- Define class of numerical algorithms to be supported by SpectralPACK
- Define SpectralPACK functions

**FFTX** project:







**Domain size** 



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