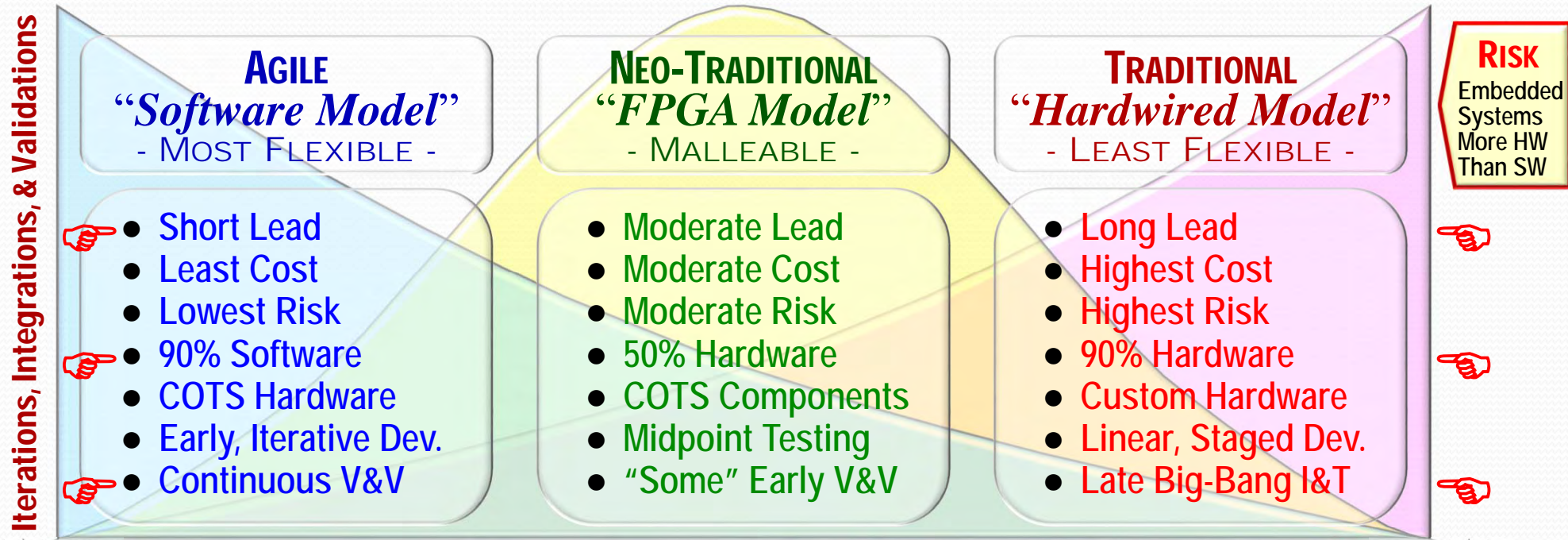


# Agile for EMBEDDED SYSTEMS

- 1st-generation systems used hardwired logic
- 2nd-generation systems used PROMS & FPGAs
- ☞ □ 3rd-generation systems use APP. SW & COTS HW



**GOAL – SHIFT FROM LATE HARDWARE TO EARLIER SOFTWARE SOLUTION**

**START**  
Competing  
With SW

**STOP**  
Competing  
With HW