About Credo

Credo (CRDO) has a mission is to deliver high-speed solutions to break bandwidth barriers on every wired connection in the data infrastructure market. We provide innovative, secure, high-speed connectivity solutions that deliver improved power and cost efficiency as data rates and corresponding bandwidth requirements increase exponentially throughout the data infrastructure market.

or you can find it in Company - Credo (credosemi.com)

> Company - Credo
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> According to International Data Corporation (IDC), the amount of data created, captured, copied, and consumed in the world is expected to increase by approximately 2.8 times, from 64 ZettaBytes (ZB), in 2020 to more than 179ZB in 2025.
>
> credosemi.com

Job Title: Analog Design Engineer

Duties

- Participate in a team to design and layout of high-speed ICs for optical fiber/wireline communications, Design, verification, simulation and optimization for amplifier, ADC/DAC, bandgap, analog buffer, LDO, charge pump, voltage reference, PLL, oscillator, SerDes, etc. Create support documents.

Requirements

- Bachelor degree or above in Electronic Engineering.
- Knowledge in analog and mixed-signal circuit design.
- Familiar with simulation tools such as SPICE, SPECTRE
- Experienced in high-speed circuit design is a plus Good programming in Perl, TCL and Shell programming is preferred.
- Knowledge in CMOS IC physical layout design and able to use various EDA tools.
- Good understanding of semiconductor theory and structures of devices.

Application Method

We are looking for people with good working attitude, team spirit and strong communication skills. If you are interested in Credo and meet any requirements above, it's our pleasure to have you join our work.

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