Huawei Hong Kong Research Center
Huawei Hong Kong Research Center was established in 2018, focusing on chipsets, software engineering, AI, and theoretical researches. We are currently seeking high-caliber Engineers and Researchers at all levels to join our new team.

(Intern) Digital Design & Verification Engineer

You will take part in the development of different major functional units of a processor/AI core, including but not limited to digital logic design, verification, logic synthesis, timing analysis, place&route in physical design. You also will be provided a solid ASIC design training for developing your own career path in IC industry.

Responsibilities
- Work on digital logic design and implementation for processor/AI core
- Work on verification testbench and implement function verification of ASIC to ensure functional correctness
- Design from Gate-level netlist to GDSII (include DFT, floorplan, placement, CTS, routing, power analysis, SI analysis, STA, physical verify, XRC)
- Support IC design teams for timing closure

Qualifications
- Studying Bachelor or higher degree in Electronic/Computer Engineering with preferably solid knowledge in digital IC design
- Good understanding in digital IC design flow including RTL design, verification, logic synthesis, timing analysis and physical design
- Good understanding of computer system architecture
- Good command of written and spoken English and Chinese (Mandarin)
- Good team player with strong communication skills

For interested parties, please send your full resume, cover letter and salary expectation to lam.ying.chee@huawei.com. Applicants who are not invited for interview within 6 weeks may assume their application unsuccessful.