Huawei Hong Kong Research Center

Huawei Hong Kong Research Center was established in 2018, with focus on CPU, Basic Software, AI related research and development, from algorithm, software to chip set development. We are looking for high caliber Engineers and Researchers at all levels to join our new team.

模拟版圖设计暑期实习生

【工作职责】

- 负责全定制模拟版图设计

- 负责版图验证（DRC/ERC/LVS/EMIR/EOS 等）及后仿真提取

- 负责最终 GDS 的 tape out 以及 mask 的 e_job view

【任职要求】

业务技能要求：

- 微电子、电子类相关专业本科以上学历

- 熟练应用主流 IC 版图设计及验证工具

- 具有良好的沟通能力、学习能力、分析能力和团队协作能力

专业知识要求：

- 熟悉集成电路 CMOS 工艺流程以及电路基础知识

- 熟练掌握版图设计知识并清晰理解寄生，噪声，器件，高精度匹配等知识

- 有 ESD、Latchup 处理经验，及熟悉 SKILL/Shell/Python 等脚本语言者优先

- 有成功投片经验与芯片量产经验优先

Analog Layout Design – Summer Intern

【Job Responsibilities】

- Responsible for custom analog layout design

- Responsible for layout verification (DRC/ERC/LVS/EMIR/EOS, etc.) and post-layout simulation extraction

- Responsible for the tape out of the final GDS and the e_job view of the mask

【Job Requirements】

Skills requirements:

- Bachelor degree or above in Microelectronics or Electronics related discipline

- Familiar with mainstream IC layout design and verification tools

- Good communication skills, learning skills, analytical skills and teamwork skills

Knowledge requirements:

- Familiar with integrated circuit CMOS process flow and basic knowledge of circuits

- Proficient in layout design knowledge, and clear understanding of parasitic, noise, devices, high-precision matching knowledge

- Experience in ESD, Latchup, and familiar with SKILL/Shell/Python scripting language are preferred

- Successful IC tape-out and mass production experience are preferred

For interested parties, please send your full resume with and salary expectation to lin.haoyao@huawei.com . Applicants who are not invited for interview within 6 weeks from the closing date may assume their application unsuccessful.