

#### **Huawei Hong Kong Research Center**

Huawei Hong Kong Research Center was established in 2018, with focus on CPU, Basic Software, Al related research and development, from algorithm, software to chip set development. We are looking for high caliber Engineers and Researchers at all levels to join our new team.

# 设计验证工程师

## 【工作职责】

- 开发,优化和维护编程脚本(如 Python、PERL、TCL)以提高 IC 设计项目的设计效率
- 分析日志和报告文件,确保正确配置工具以提供准确的结果
- 定期与世界各地的项目团队沟通解决问题,沟通状态和解决技术问题
- 提供技术支持,确保 IC 设计项目按照开发计划并在进度限制内获得成功
- 拥有成长,学习和适应新方法和技术的能力

## 【任职要求】

# 业务技能要求:

- 本科及以上学历; 电子工程专业
- 丰富的编程经验 (例如 Python、PERL、TCL)
- 熟悉 Cadence/Mentor/Synopsys EDA 工具以及 SKILL (Cadence) 和 Calibre (Mentor)
  者优先

## 专业知识要求:

- 基础电子电路知识
- 良好的软件和脚本能力
- Python/PERL/TCL 语言知识

# **Design Verification Engineer**

# [Job Responsibilities]

- Develop, enhance and maintain the programming script (e.g. Python, PERL,
  TCL) to increase the design efficiency of IC design project
- Provide the user guideline document and make sure the user got the clear picture for all of the programming script.
- Analyze log and report files to ensure tools are properly configured to provide accurate results
- Communicate regularly with the project teams world-wide to resolve issues,
  communicate status and solve technical problems
- Provide technical support to ensure IC design projects are successful as per development plans and within schedule constraints
- Possess ability to thrive on, learn and adapt new methodologies and technologies

# [Job Requirements]

# **Skills requirements:**

- Bachelor's degree or above; Electronic Engineering major
- Extensive experiences in programming (e.g. C#, Python, PERL, TCL)
- Knowledge with Cadence/Mentor/Synopsys EDA tools and SKILL (Cadence) and Calibre (Mentor) will be a plus

## **Knowledge requirements:**

Basic electronics circuit knowledge

- Good software and scripting skills
- Python/PERL/TCL language knowledge

For interested parties, please send your full resume and salary expectation to <a href="mailto:lin.haoyao@huawei.com">lin.haoyao@huawei.com</a>. Applicants who are not invited for interview within 6 weeks from the closing date may assume their application unsuccessful.