Field Programmable Gate Array (FPGA)

Dennis Lee, Sr. Marketing Director

Efinix Global

Worldwide Presence with 130+ Employees

www.freeworldmaps.net

Current Investors

AMD

SAMSUNG

Alibaba Entrepreneurs Fund
阿里巴巴創業者基金

恒 基

AIM

mavcap

Celebrating 10 Years of Innovation

- 10 Year Anniversary Celebration in 2022

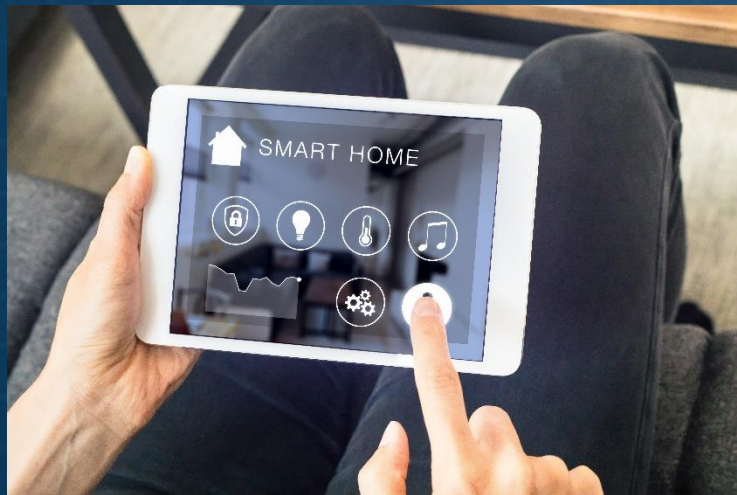- 12 Million+ Units Shipped in 2022

- 300 design win projects

One Scalable Platform

High Performance For Mainstream Markets

Ti35
Thirty-Five Thousand LE

Industry Lowest power consumption FPGA

Ti1000
One Million LE

- Intelligent Edge Devices
- Smart Home & IoT

- Vision Systems
- Industrial Automation

- Edge Servers
- Communications

EFINIX®

Improving Upon a Familiar Design Environment

ARM | Fixed Peripherals

ASIC / ASSP

EFINIX

RISC-V | Configurable Peripherals

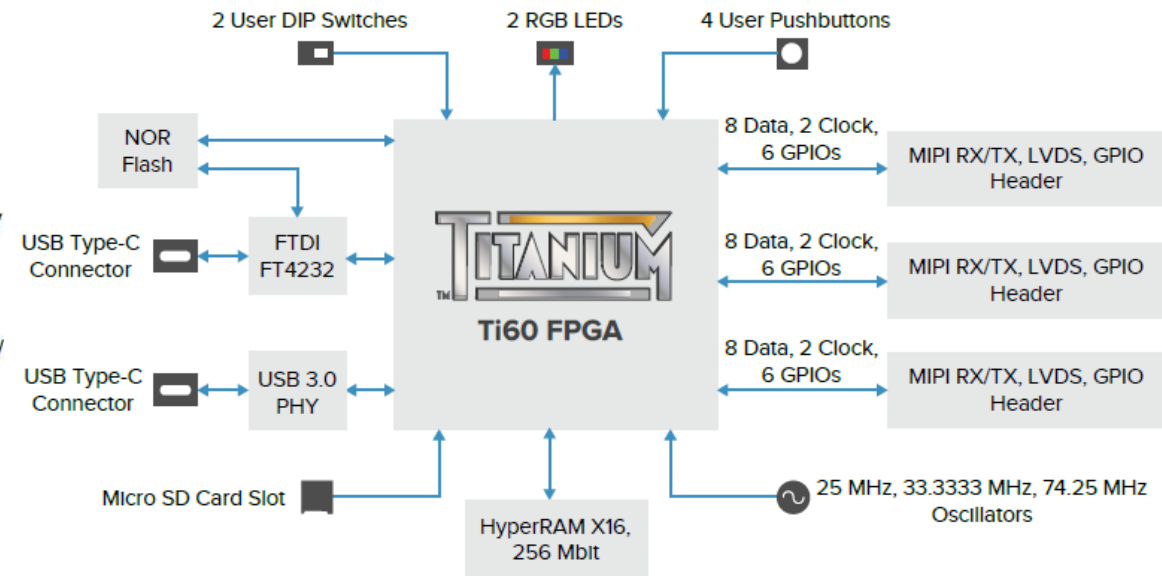**FPGA**

- Software Development Model
- Licensed ARM Core
- Fixed ISA
- Fixed Accelerators
- No Hardware Flexibility

- Software Development Model
- Open-Source RISC-V Core
- Open and Customizable ISA
- Infinitely Configurable Accelerators
- Flexible and Future-Proof Hardware

Eval Boards: Example Ti60F225
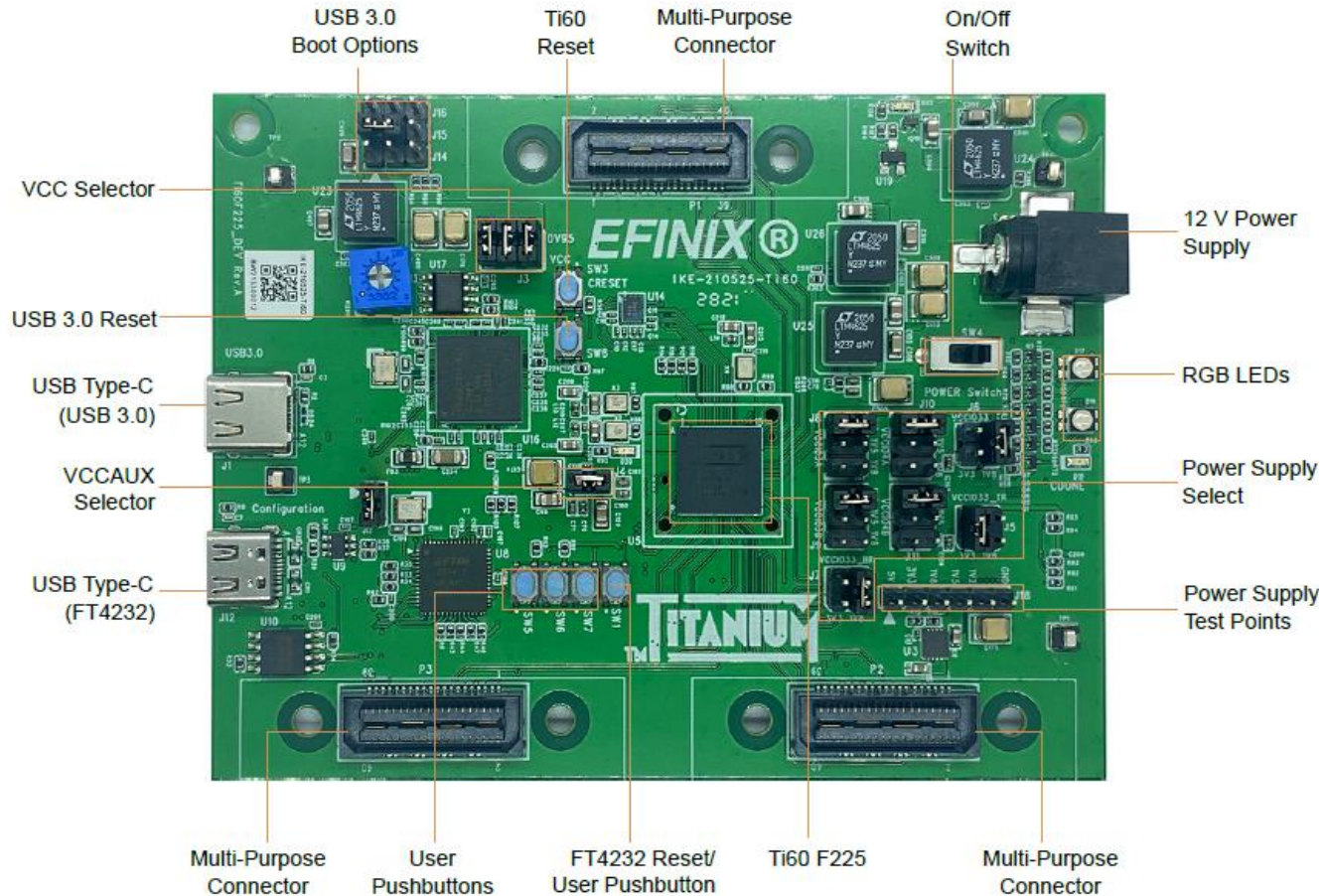
- Ti60F225A
- HyperRAM 256Mbit

Summer Internship (HK$15000+/month)

Software Engineer Intern, EDA Tools (2)

- Electronic Design Automation (EDA) tools development: You will work on user interface, workflow and backend tools with other talented engineers in our global development team.

- Responsibilities:

- * Develop and test EDA tools using C++, Python, etc.

- * Integrate with front and backend tool chain to delivery applications.

Application Engineer Intern (2)

Skill: Basic knowledge in hardware and programming design. Knowledge in RTL and Python will be added as advantage
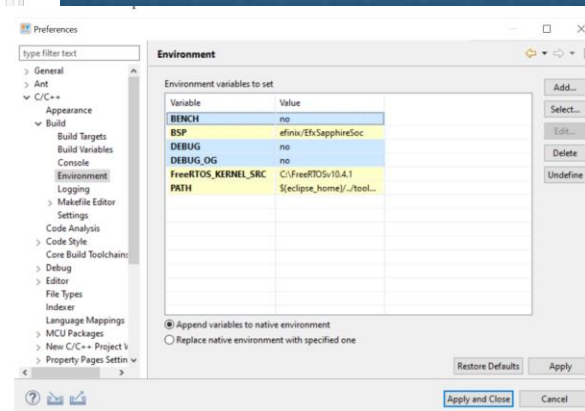- **Responsibilities**

- Support Application team on board and application design
- Assist FPGA chip validation
- Support providing technical information for documentation

Previous projects

Netlist viewer
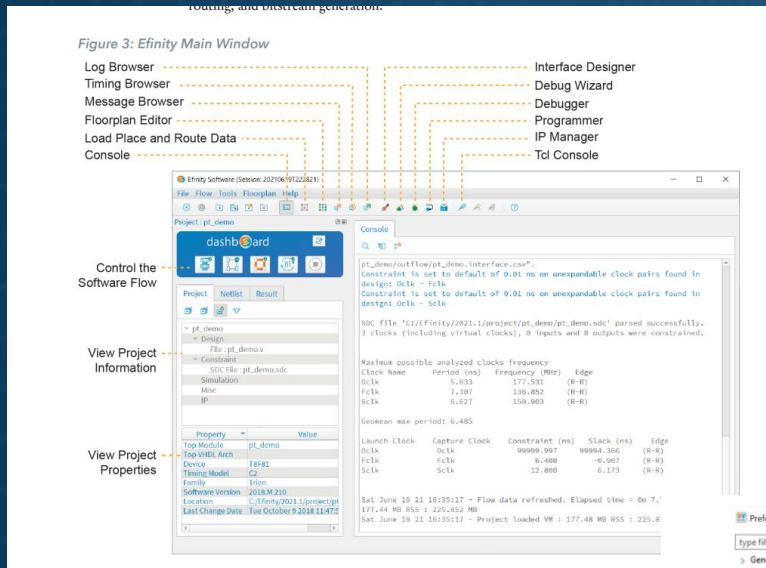
Benchmarking

SoC Builder
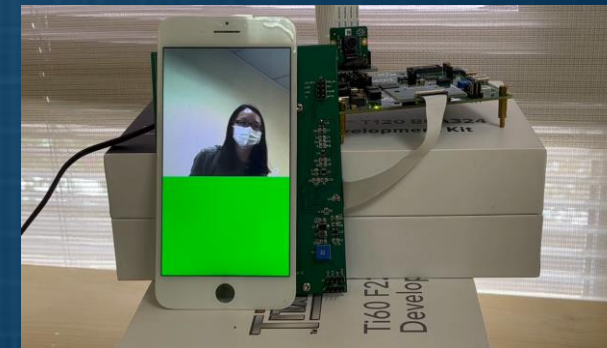
Example design on development board

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Dennis Lee