



**Position: IC Design Engineer – Physical Design**

**Location: Hong Kong**

**Type of Position: Graduate Job**

**Job Responsibilities:**

- Communicate with engineers to provide solutions on verification area;
- Learn for new verification tool and develop related deck;
- Perform layout of custom analog circuit blocks.

**Job Requirements:**

- Bachelor or above in Electronic Engineering or equivalent;
- Knowledge in CMOS physical layout design or various EDA tools is an advantage;
- Good communication skills with English and Mandarin;
- Self-motivated with good working attitude;
- Fresh graduates will also be considered.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: [recruit@solomon-systech.com](mailto:recruit@solomon-systech.com)

All applications will be treated in strict confidence and for recruitment purpose only.