

Identity-Based Encryption from the Diffie-Hellman Assumption*

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Abstract

We provide the first constructions of identity-based encryption and hierarchical identity-based encryption based on the hardness of the (Computational) Diffie-Hellman Problem (without use of groups with pairings) or Factoring. Our construction achieves the standard notion of identity-based encryption as considered by Boneh and Franklin [CRYPTO 2001]. We bypass known impossibility results using garbled circuits that make a non-black-box use of the underlying cryptographic primitives.

1 Introduction

Soon after the invention of public-key encryption [DH76,RSA78], Shamir [Sha84] posed the problem of constructing a public-key encryption scheme where encryption can be performed using just the identity of the recipient. In such an identity-based encryption (IBE) scheme there are four algorithms: (1) **Setup** generates the global public parameters and a master secret key, (2) **KeyGen** uses the master secret key to generate a secret key for the user with a particular identity, (3) **Encrypt** allows for encrypting messages corresponding to an identity, and (4) **Decrypt** can be used to decrypt the generated ciphertext using a secret key for the matching identity.

The ability of IBE to “compress” exponentially many public keys into “small” global public parameters [Coc01,BF01] provides a way for simplifying certificate management in e-mail systems. Specifically, Alice can send an encrypted email to Bob at `bob@iacr.org` by just using the string “`bob@iacr.org`” and the public parameters generated by a setup authority. In this solution, there is no need for Alice to obtain Bob’s public key. Bob could decrypt the email using a secret key corresponding to “`bob@iacr.org`” that he can obtain from the setup authority.¹

The more functional notion of hierarchical IBE (HIBE) [HL02,GS02] additionally allows a user with a secret key for an identity `id` to generate a secret key for any identity `id||id'`. For instance, in the example above, Bob can use the secret key corresponding to identity “`bob@iacr.org`” to obtain

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¹Of course, Bob needs to authenticate his identity to the setup authority in this step. Bob can perform this authentication in the same way as it would do to a Certification Authority while using a public-key encryption scheme.

a secret key corresponding to the identity “bob@iacr.org||2017”. Bob could then give this key to his secretary who could now decrypt all his emails tagged as being sent during the year 2017, while Bob is on vacation.

The first IBE schemes were realized by Boneh and Franklin [BF01] and Cocks [Coc01]. Subsequently, significant research effort has been devoted to realizing IBE and HIBE schemes. By now, several constructions of IBE are known based on (i) various assumptions on groups with a bilinear map, e.g. [BF01, CHK03, BB04a, BB04b, Wat05, OT10], (ii) the quadratic residuosity assumption [Coc01, BGH07] (in the random oracle model [BR93]), or (iii) the learning-with-errors (LWE) assumption [GPV08, CHKP10, AB09]. On the other hand, HIBE schemes are known based on (i) various assumptions on groups with a bilinear map [HL02, GS02, BB04a, BBG05, GH09, Wat09, SW08, LW10], or (ii) LWE [CHKP10, ABB10a, ABB10b].

On the negative side, Boneh, Papakonstantinou, Rackoff, Vahlis, and Waters [BPR⁺08] show that IBE cannot be realized using trapdoor permutations or CCA-secure public-key encryption in a black-box manner. Furthermore, Papakonstantinou, Rackoff and Vahlis [PRV12] show that black-box use of a group over which DDH is assumed to be hard is insufficient for realizing IBE.

1.1 Our Results

In this work, we show a fully-secure construction of IBE and a selectively secure HIBE based just on the Computational Diffie-Hellman (CDH). In the group of quadratic residues this problem is as hard as the Factoring problem [Shm85, McC88, BBR97]. Therefore, this implies a solution based on the hardness of factoring as well.

Our constructions bypass the known impossibility results [BPR⁺08, PRV12] by making a non-black-box use of the underlying cryptographic primitives. However, this non-black-box use of cryptographic primitives also makes our scheme inefficient. In Section 6, we suggest ideas for reducing the non-black-box of the underlying primitives thereby improving the efficiency of our scheme. Even with these optimizations, our IBE scheme is prohibitive when compared with the IBE schemes based on bilinear maps. We leave open the problem of realizing an efficient IBE scheme from the Diffie-Hellman Assumption.

Subsequent work. In a followup paper [DG17] we show how the techniques from this paper can be used to obtain generic constructions of fully-secure IBE and selectively-secure HIBE starting with any selectively-secure IBE scheme.

2 Our Techniques

In this section, we give an intuitive explanation of our construction of IBE from the Decisional Diffie-Hellman (DDH) Assumption. We defer the details on constructing HIBE and obtaining the same results based on Computational Diffie-Hellman to the main body of the paper.

We start by describing a chameleon hash function [KR98] that supports certain encryption and decryption procedures. We refer to this new primitive as a *chameleon encryption scheme*.² Subsequently, we describe how chameleon encryption along with garbled circuits can be used to realize IBE.

²The notion of chameleon hashing is closely related to the notion of chameleon commitment scheme [BCC88] and we refer the reader to [KR98] for more discussion on this.

2.1 Chameleon Encryption

As mentioned above, a chameleon encryption scheme is a chameleon hash function that supports certain encryption and decryption procedures along with. We start by describing the chameleon hash function and then the associated encryption and decryption procedures. Recall that a chameleon hash function is a collision resistant hash function for which the knowledge of a trapdoor enables collision finding.

Our Chameleon Hash. Given a cyclic group \mathbb{G} of prime order p with a generator g consider the following chameleon hash function:

$$H(\mathbf{k}, \mathbf{x}; r) = g^r \prod_{j \in [n]} g_{j, x_j},$$

where $\mathbf{k} = (g, \{g_{j,0}, g_{j,1}\}_{j \in [n]})$, $r \in \mathbb{Z}_p$ and x_j is the j^{th} bit of $\mathbf{x} \in \{0, 1\}^n$. It is not very hard to note that this hash function is (i) collision resistant based on the hardness of the discrete-log problem, and (ii) chameleon given the trapdoor information $\{\text{dlog}_g g_{j,0}, \text{dlog}_g g_{j,1}\}_{j \in [n]}$ — specifically, given any $\mathbf{x}, r, \mathbf{x}'$ and the trapdoor information we can efficiently compute r' such that $H(\mathbf{k}, \mathbf{x}; r) = H(\mathbf{k}, \mathbf{x}'; r')$.

The Associated Encryption — Abstractly. Corresponding to a chameleon hash function, we require encryption and decryption algorithms such that

1. encryption $\text{Enc}(\mathbf{k}, (\mathbf{h}, i, b), \mathbf{m})$ on input a key \mathbf{k} , a hash value \mathbf{h} , a location $i \in [n]$, a bit $b \in \{0, 1\}$, and a message $\mathbf{m} \in \{0, 1\}$ outputs a ciphertext ct , and
2. decryption $\text{Dec}(\mathbf{k}, (\mathbf{x}, r), \text{ct})$ on input a ciphertext ct , \mathbf{x} and coins r yields \mathbf{m} if

$$\mathbf{h} = H(\mathbf{k}, \mathbf{x}; r) \text{ and } x_i = b,$$

where (\mathbf{h}, i, b) are the values used in the generation of the ciphertext ct .

In other words, the decryptor can use the knowledge of the preimage of \mathbf{h} as the key to decrypt \mathbf{m} as long as the i^{th} bit of the preimage it can supply is equal to the value b chosen at the time of encryption. Our security requirement roughly is that

$$\{\mathbf{k}, \mathbf{x}, r, \text{Enc}(\mathbf{k}, (\mathbf{h}, i, 1 - x_i), 0)\} \stackrel{c}{\approx} \{\mathbf{k}, \mathbf{x}, r, \text{Enc}(\mathbf{k}, (\mathbf{h}, i, 1 - x_i), 1)\},$$

where $\stackrel{c}{\approx}$ denotes computational indistinguishability.³

The Associated Encryption — Realization. Corresponding to the chameleon hash defined above our encryption procedure $\text{Enc}(\mathbf{k}, (\mathbf{h}, i, b), \mathbf{m})$ proceeds as follows. Sample a random value $\rho \stackrel{\$}{\leftarrow} \mathbb{Z}_p$ and output the ciphertext $\text{ct} = (e, c, c', \{c_{j,0}, c_{j,1}\}_{j \in [n] \setminus \{i\}})$ and

$$\begin{aligned} c &:= g^\rho & c' &:= \mathbf{h}^\rho, \\ \forall j \in [n] \setminus \{i\}, \quad c_{j,0} &:= g_{j,0}^\rho & c_{j,1} &:= g_{j,1}^\rho, \\ e &:= \mathbf{m} \oplus g_{i,b}^\rho. \end{aligned}$$

³The success of decryption is conditioned on certain requirements placed on (\mathbf{x}, r) . This restricted decryption capability is reminiscent of the concepts of witness encryption [GGSW13] and extractable witness encryption [BCP14, ABG⁺13].

It is easy to see that if $x_i = b$ then decryption $\text{Dec}(\text{ct}, (x, r))$ can just output

$$e \oplus \frac{c'}{c^r \prod_{j \in [n] \setminus \{i\}} c_{j, x_j}}.$$

However, if $x_i \neq b$ then the decryptor has access to the value g_{i, x_i}^{ρ} but not $g_{i, b}^{\rho}$, and this prevents him from learning the message m . Formalizing this intuition, we can argue security of this scheme based on the DDH assumption.⁴ In a bit more detail, we can use an adversary \mathcal{A} breaking the security of the chameleon encryption scheme to distinguish DDH tuples (g, g^u, g^v, g^{uv}) from random tuples (g, g^u, g^v, g^s) . Fix (adversarially chosen) $x \in \{0, 1\}^n$, index $i \in [n]$ and a bit $b \in \{0, 1\}$. Given a tuple (g, U, V, T) , we can simulate public key k , hash value h , coins r and ciphertext ct as follows. Choose uniformly random values $\alpha_{j,0}, \alpha_{j,1} \xleftarrow{\$} \mathbb{Z}_p$ and set $g_{j,0} = g^{\alpha_{j,0}}$ and $g_{j,1} = g^{\alpha_{j,1}}$ for $j \in [n]$. Now *reassign* $g_{i,1-x_i} = U$ and set $k := (g, \{g_{j,0}, g_{j,1}\}_{j \in [n]})$. Choose $r \xleftarrow{\$} \mathbb{Z}_p$ uniformly at random and set $h := H(k, x; r)$. Finally prepare a challenge ciphertext $\text{ct} := (e, c, c', \{c_{j,0}, c_{j,1}\}_{j \in [n] \setminus \{i\}})$ by choosing

$$\begin{aligned} c &:= V & c' &:= V^r \cdot \prod_{j \in [n]} V^{\alpha_{j, x_j}}, \\ \forall j \in [n] \setminus \{i\}, \quad c_{j,0} &:= V^{\alpha_{j,0}} & c_{j,1} &:= V^{\alpha_{j,1}}, \\ e &:= m \oplus T, \end{aligned}$$

where $m \in \{0, 1\}$. Now, if $(g, U, V, T) = (g, g^u, g^v, g^{uv})$, then a routine calculation shows that k , h , r and ct have the same distribution as in the security experiment, thus \mathcal{A} 's advantage in guessing m remains the same. On the other hand, if T is chosen uniformly at random and independent of g, U, V , then \mathcal{A} 's advantage to guess m given k, h, r and ct is obviously 0, which concludes this proof-sketch.

2.2 From Chameleon Encryption to Identity-Based Encryption

The public parameters of an IBE scheme need to encode exponentially many public keys succinctly — one per each identity. Subsequently, corresponding to these public parameters the setup authority should be able to provide the secret key for any of the exponentially many identities. This is in sharp contrast with public-key encryption schemes for which there is only one trapdoor per public key, which if revealed leaves no security. This is the intuition behind the black-box impossibility results for realizing IBE based on trapdoor permutations and CCA secure encryption [BPR⁺08, PRV12]. At a very high level, we overcome this intuitive barrier by actually allowing for exponentially many public keys which are somehow compressed into small public parameters using our chameleon hash function. We start by describing how these keys are sampled and hashed.

Arrangement of the keys. We start by describing the arrangement of the exponentially many keys in our IBE scheme for identities of length n bits. First, imagine a fresh encryption decryption key pair for any public-key encryption scheme for each identity in $\{0, 1\}^n$. We will denote this pair for identity $v \in \{0, 1\}^n$ by (ek_v, dk_v) . Next, in order to setup the hash values, we sample n hash keys — namely, k_0, \dots, k_{n-1} . Now, consider a tree of depth n and for each node $v \in \{0, 1\}^{\leq n-1} \cup \{\epsilon\}$ ⁵

⁴In Section 5, we explain our constructions of chameleon encryption based on the (Computational) Diffie-Hellman Assumption, or the Factoring Assumption.

⁵We use ϵ to denote the empty string.

the hash value h_v is set as:

$$h_v = \begin{cases} H(k_i, ek_{v\|0} \| ek_{v\|1}; r_v) & v \in \{0, 1\}^{n-1} \text{ where } i = |v| \\ H(k_i, h_{v\|0} \| h_{v\|1}; r_v) & v \in \{0, 1\}^{<n-1} \cup \{\epsilon\} \text{ where } i = |v| \end{cases} \quad (1)$$

where r_v for each $v \in \{0, 1\}^{<n} \cup \{\epsilon\}$ are chosen randomly.

Generating the tree on demand. Note that the setup authority cannot generate and hash these exponentially many hash keys at setup time. Instead, it generates them implicitly. More specifically, the setup authority computes each h_v as $H(k_{|v|}, 0^\lambda; \omega_v)$. Then, later on when needed, using the trapdoor $t_{|v|}$ for the hash key $k_{|v|}$ we can obtain coins r_v such that the generated value h_v indeed satisfies Equation 1. Furthermore, in order to maintain consistency (in the tree and across different invocations) the randomness ω_v used for each v is chosen using a pseudorandom function. In summary, with this change the entire can be represented succinctly.

What are the public parameters? Note that the root hash value h_ϵ somehow binds the entire tree of hash values. With this in mind, we sent the public parameters of the scheme to be the n hash keys and the root hash value, i.e.

$$k_0, \dots, k_{n-1}, h_\epsilon.$$

Secret-key for a particular identity id. Given the above tree structure the secret key for some identity id simply consists of the hash values along the path from the root to the leaf corresponding to id and their siblings along with the decryption key dk_{id} .⁶ Specifically, the secret key sk_{id} for identity id consists of $(\{lk_v\}_{v \in V}, dk_{id})$ where $V := \{\epsilon, id[1], \dots, id[1 \dots n-1]\}$ and

$$lk_v = \begin{cases} (h_v, h_{v\|0}, h_{v\|1}, r_v) & \text{for } v \in V \setminus \{id[1 \dots n-1]\} \\ (h_v, ek_{v\|0}, ek_{v\|1}, r_v) & \text{for } v = id[1 \dots n-1] \end{cases}.$$

Encryption and Decryption. Before providing details of encryption and decryption, we will briefly discuss how chameleon encryption can be useful in conjunction with garbled circuits.⁷ Chameleon encryption allows an encryptor knowing a key k and a hash value h to encrypt a set of labels $\{lab_{j,0}, lab_{j,1}\}_j$ such that a decryptor knowing x and r with $H(k, x; r) = h$ can recover $\{lab_{j,x_j}\}_j$. On the other hand, security of chameleon encryption guarantees that the receiver learns nothing about the remaining labels. In summary, using this mechanism, an the generated ciphertexts enable the decryptor to feed x into a garbled circuit to be processed further.

To encrypt a message m to an identity $id \in \{0, 1\}^n$, the encryptor will generate a sequence of $n + 1$ garbled circuits $\{\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}\}$ such that a decryptor in possession of the identity secret key $sk_{id} = (\{lk_v\}_{v \in V}, dk_{id})$ will be able evaluate these garbled circuits one after another. Roughly speaking, circuit \tilde{P}^i for any $i \in \{0 \dots n-1\}$ and $v = id[1 \dots i]$ takes as input a hash value h_v and generates chameleon encryptions of the input labels of the next garbled circuit \tilde{P}^{i+1} using a $k_{|v|}$

⁶We note that our key generation mechanism can be seen as an instantiation of the Naor Yung [NY89] tree-based construction of signature schemes from universal one-way hash functions and one-time signatures. This connection becomes even more apparent in the follow up paper [DG17].

⁷For this part of the intuition, we assume familiarity with garbled circuits.

hardwired inside it and the hash value \mathbf{h} given to it as input (in a manner as described above). The last circuit T will just take as input an encryption key \mathbf{pk}_{id} and output an encryption of the plaintext message \mathbf{m} under \mathbf{ek}_{id} . Finally, the encryptor provides input labels for the first garbled circuit \tilde{P}^0 for the input \mathbf{h}_ε in the ciphertext.

During decryption, for each $i \in \{0 \dots n-1\}$ and $\mathbf{v} = \text{id}[1 \dots i]$ the decryptor will use the local key \mathbf{lk}_v to decrypt the ciphertexts generated by \tilde{P}^i and obtain the input labels for the garbled circuits \tilde{P}^{i+1} (or, T if $i = n-1$). We will now explain the first iteration of this construction in more detail, all further iterations proceed analogously. The encryptor provides garbled input labels corresponding to input \mathbf{h}_ε for the first garbled circuit \tilde{P}^0 in the ciphertext. Thus the decryptor can evaluate \tilde{P}^0 and obtain encryptions of input labels $\{\mathbf{lab}_{j,0}, \mathbf{lab}_{j,1}\}_{j \in [\lambda]}$ for the circuit \tilde{P}^1 , namely:

$$\{\text{Enc}(\mathbf{k}_0, (\mathbf{h}_\varepsilon, \text{id}[1] \cdot \lambda + j, 0), \mathbf{lab}_{j,0}), \quad \text{Enc}(\mathbf{k}_0, (\mathbf{h}_\varepsilon, \text{id}[1] \cdot \lambda + j, 1), \mathbf{lab}_{j,1})\}_{j \in [\lambda]}$$

The garbled circuit has $\text{id}[1]$ and the input labels $\{\mathbf{lab}_{j,0}, \mathbf{lab}_{j,1}\}_{j \in [\lambda]}$ hardwired in it. Given these encryptions the decryptor uses $\mathbf{lk}_\varepsilon = (\mathbf{h}_\varepsilon, \mathbf{h}_0, \mathbf{h}_1, r_\varepsilon)$ to learn the garbled input labels $\{\mathbf{lab}_{j, \mathbf{h}_{\text{id}[1], j}}\}_{j \in [\lambda]}$ where $\mathbf{h}_{\text{id}[1], j}$ is the j^{th} bit of $\mathbf{h}_{\text{id}[1]}$. In other words, the decryptor now possesses input labels for the input $\mathbf{h}_{\text{id}[1]}$ for the garbled circuit \tilde{P}^1 and can therefore evaluate \tilde{P}^1 . Analogous to the previous step, the decryptor uses $\mathbf{lk}_{\text{id}[1]}$ and $r_{\text{id}[1]}$ to obtain input labels to \tilde{P}^2 and so on. The decryptor's ability to provide the local keys \mathbf{lk}_v for $v \in V$ keeps this process going ultimately revealing an encryption of the message \mathbf{m} under the encryption key \mathbf{pk}_{id} . This final ciphertext can be decrypted using the decryption key \mathbf{dk}_{id} . At a high level, our encryption method (and the use of garbled circuits for it) has similarities with garbled RAM schemes [LO13, GH⁺14, GLOS15, GLO15, CDG⁺17]. Full details of the construction are provided in Section 6.

Proof Sketch. The intuition behind the proof of security which follows by a sequence of hybrid changes is as follows. The first (easy) change is to replace the pseudorandom function used to generate the local keys by a truly random function something that should go undetected against a computationally bounded attacker. Next, via a sequence of hybrids we change the $n+1$ garbled circuits $\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}$ to their simulated versions one by one. Once these changes are made the simulated circuit \tilde{T} just outputs an encryption of the message \mathbf{m} under the encryption key $\mathbf{pk}_{\text{id}^*}$ corresponding challenge identity id^* , which hides \mathbf{m} based on semantic security of the encryption scheme.

The only “tricky” part of the proof is the one that involves changing garbled circuits to their simulated versions. In this intuitive description, we explain how the first garbled circuit \tilde{P}^0 is moved to its simulated version. The argument of the rest of the garbled circuits is analogous. This change involves a sequence of four hybrid changes.

1. First, we change how \mathbf{h}_ε is generated. As a quick recap, recall that \mathbf{h}_ε is generated as $\mathbf{H}(\mathbf{k}_0, 0^{2\lambda}; \omega_\varepsilon)$ and r_ε are set to $\mathbf{H}^{-1}(\mathbf{t}_0, (0^{2\lambda}, \omega_\varepsilon), \mathbf{h}_0 \parallel \mathbf{h}_1)$. We instead generate \mathbf{h}_ε directly to be equal to the value r_ε are set to $\mathbf{H}(\mathbf{k}_0, \mathbf{h}_0 \parallel \mathbf{h}_1, r_\varepsilon)$ using fresh coins r_ε . The trapdoor collision and uniformity properties of the chameleon encryption scheme ensure that this change does not affect the distribution of the \mathbf{h}_ε and r_ε , up to a negligible error.
2. The second change we make is that the garbled circuit \tilde{P}^0 is not generated in simulated form instead of honestly. Note that at this point the distribution of this garbled circuit depends only on its output which is $\{\text{Enc}(\mathbf{k}_\varepsilon, (\mathbf{h}_\varepsilon, j, b), \mathbf{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$ where $\{\mathbf{lab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$ are the input labels for the garbled circuit \tilde{P}^1 .

3. Observe that at this point the trapdoor t_ε is not being used at all and \tilde{P}^0 is the simulated form. Therefore, based on the security of the chameleon encryption we have that for all $j \in [\lambda]$, $\text{Enc}(k_\varepsilon, (h_\varepsilon, j, 1 - h_{\text{id}[1],j}), \text{lab}_{j,1-h_{\text{id}[1],j}})$ hides $\text{lab}_{j,1-h_{\text{id}[1],j}}$. Hence, we can change the hardcoded ciphertexts from

$$\{\text{Enc}(k_\varepsilon, (h_\varepsilon, j, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$$

to

$$\{\text{Enc}(k_\varepsilon, (h_\varepsilon, j, b), \text{lab}_{j,h_{\text{id}[1],j}})\}_{j \in [\lambda], b \in \{0,1\}}$$

4. Finally, the fourth change we make is that we reverse the first change. In particular, we generate h_ε as is done in the real execution.

As a consequence, at this point only the labels $\{\text{lab}_{j,h_{\text{id}[1],j}}\}_{j \in [\lambda]}$ are revealed in an information theoretic sense and the same sequence of hybrids can be repeated for the next garbled circuit \tilde{P}^1 . The only change in this step is that now both h_0 and h_1 will be generated (if needed) by first sampling their children. The full proof of security is provided in Section 6.2.

3 Preliminaries

Let λ denote the security parameter. We use the notation $[n]$ to denote the set $\{1, \dots, n\}$. By PPT we mean a probabilistic polynomial time algorithm. For any set S , we use $x \stackrel{\$}{\leftarrow} S$ to mean that x is sampled uniformly at random from the set S .⁸ Alternatively, for any distribution D we use $x \stackrel{\$}{\leftarrow} D$ to mean that x is sampled from the distribution D . We use the operator $:=$ to represent assignment and $=$ to denote an equality check.

3.1 Computational Problems

Definition 3.1 (The Diffie-Hellman (DH) Problem). *Let (\mathbb{G}, \cdot) be a cyclic group of order p with generator g . Let a, b be sampled uniformly at random from \mathbb{Z}_p (i.e., $a, b \stackrel{\$}{\leftarrow} \mathbb{Z}_p$). Given (g, g^a, g^b) , the $\text{DH}(\mathbb{G})$ problem asks to compute g^{ab} .*

Definition 3.2 (The Factoring Problem). *Given a Blum integer $N = pq$ (p and q are large primes with $p = q = 3 \pmod{4}$) the FACT problem asks to compute p and q .*

3.2 Identity-Based Encryption

Below we provide the definition of identity-based encryption (IBE).

Definition 3.3 (Identity-Based Encryption (IBE) [Sha84, BF01]). *An identity-based encryption scheme consists of four PPT algorithms (Setup, KeyGen, Encrypt, Decrypt) defined as follows:*

- **Setup**(1^λ): *given the security parameter, it outputs a master public key mpk and a master secret key msk .*

⁸We use this notion only when the sampling can be done by a PPT algorithm and the sampling algorithm is implicit.

- $\text{KeyGen}(\text{msk}, \text{id})$: given the master secret key msk and an identity $\text{id} \in \{0, 1\}^n$, it outputs a decryption key sk_{id} .
- $\text{Encrypt}(\text{mpk}, \text{id}, \text{m})$: given the master public key mpk , an identity $\text{id} \in \{0, 1\}^n$, and a message m , it outputs a ciphertext ct .
- $\text{Decrypt}(\text{sk}_{\text{id}}, \text{ct})$: given a secret key sk_{id} for identity id and a ciphertext ct , it outputs a string m .

The following completeness and security properties must be satisfied:

- **Completeness:** For all security parameters λ , identities $\text{id} \in \{0, 1\}^n$ and messages m , the following holds:

$$\text{Decrypt}(\text{sk}_{\text{id}}, \text{Encrypt}(\text{mpk}, \text{id}, \text{m})) = \text{m}$$

where $\text{sk}_{\text{id}} \leftarrow \text{KeyGen}(\text{msk}, \text{id})$ and $(\text{mpk}, \text{msk}) \leftarrow \text{Setup}(1^\lambda)$.

- **Security:** For any PPT adversary $\mathcal{A} = (\mathcal{A}_1, \mathcal{A}_2)$, there exists a negligible function $\text{negl}(\cdot)$ such that the following holds:

$$\Pr[\text{IND}_{\mathcal{A}}^{\text{IBE}}(1^\lambda) = 1] \leq \frac{1}{2} + \text{negl}(\lambda)$$

where $\text{IND}_{\mathcal{A}}^{\text{IBE}}$ is shown in Figure 1, and for each key query id that \mathcal{A} sends to the KeyGen oracle, it must hold that $\text{id} \neq \text{id}^*$.

Experiment $\text{IND}_{\mathcal{A}}^{\text{IBE}}(1^\lambda)$:

1. $(\text{mpk}, \text{msk}) \xleftarrow{\$} \text{Setup}(1^\lambda)$.
2. $(\text{id}^*, \text{m}_0, \text{m}_1, \text{st}) \xleftarrow{\$} \mathcal{A}_1^{\text{KeyGen}(\text{msk}, \cdot)}(\text{mpk})$ where $|\text{m}_0| = |\text{m}_1|$ and for each query id by \mathcal{A}_1 to $\text{KeyGen}(\text{msk}, \cdot)$ we have that $\text{id} \neq \text{id}^*$.
3. $b \xleftarrow{\$} \{0, 1\}$.
4. $\text{ct}^* \xleftarrow{\$} \text{Encrypt}(\text{mpk}, \text{id}^*, \text{m}_b)$.
5. $b' \xleftarrow{\$} \mathcal{A}_2^{\text{KeyGen}(\text{msk}, \cdot)}(\text{mpk}, \text{ct}^*, \text{st})$ where for each query id by \mathcal{A}_2 to $\text{KeyGen}(\text{msk}, \cdot)$ we have that $\text{id} \neq \text{id}^*$.
6. Output 1 if $b = b'$ and 0 otherwise.

Figure 1: The $\text{IND}_{\mathcal{A}}^{\text{IBE}}$ Experiment

Hierarchical Identity-Based Encryption (HIBE). A HIBE scheme is an IBE scheme except that we set $\text{sk}_\varepsilon := \text{msk}$ and modify the KeyGen algorithm. In particular, KeyGen takes sk_{id} and a string id' as input and outputs a secret key $\text{sk}_{\text{id} \parallel \text{id}'}$. More formally:

- $\text{KeyGen}(\text{sk}_{\text{id}}, \text{id}')$: given the secret key sk_{id} and an identity $\text{id}' \in \{0, 1\}^*$, it outputs a decryption key $\text{sk}_{\text{id} \parallel \text{id}'}$.

Correctness condition for HIBE is same as it was from IBE. Additionally, the security property is analogous to $\text{IND}_{\mathcal{A}}^{\text{IBE}}(1^\lambda)$ except that now we only consider the notion of *selective security* for HIBE — namely, the adversary \mathcal{A} is required to announce the challenge identity id^* before it has seen the mpk and has made any secret key queries. This experiment $\text{IND}_{\mathcal{A}}^{\text{HIBE}}$ is shown formally in Figure 2.

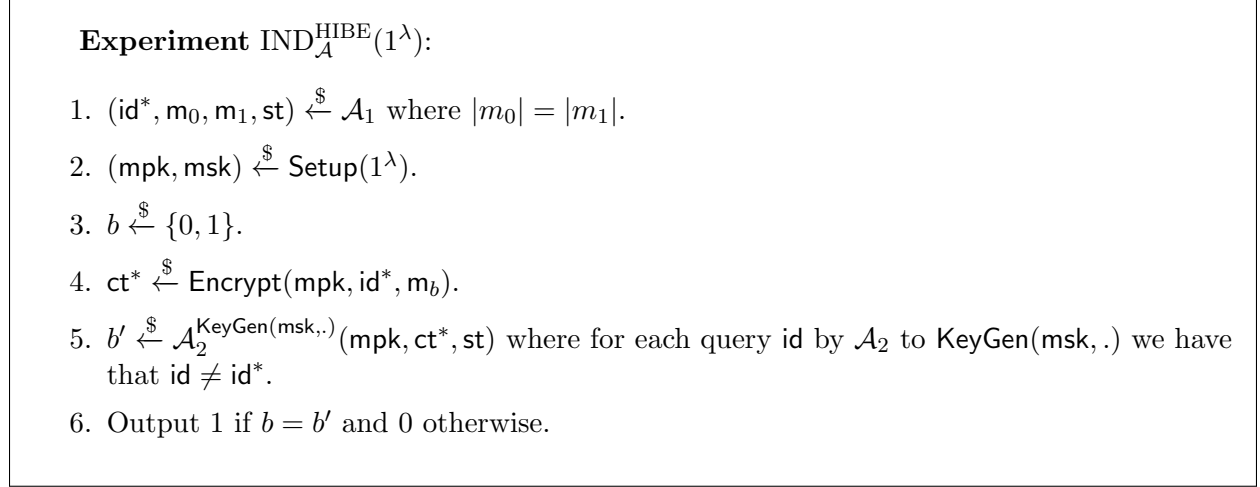


Figure 2: The $\text{IND}_{\mathcal{A}}^{\text{HIBE}}$ Experiment

3.3 Garbled Circuits

Garbled circuits were first introduced by Yao [Yao82] (see Lindell and Pinkas [LP09] and Bellare et al. [BHR12] for a detailed proof and further discussion). A circuit garbling scheme is a tuple of PPT algorithms $(\text{GCircuit}, \text{Eval})$. Very roughly GCircuit is the circuit garbling procedure and Eval the corresponding evaluation procedure. More formally:

- $(\tilde{\mathcal{C}}, \{\text{lab}_{w,b}\}_{w \in \text{inp}(\mathcal{C}), b \in \{0,1\}}) \xleftarrow{\$} \text{GCircuit}(1^\lambda, \mathcal{C})$: GCircuit takes as input a security parameter λ and a circuit \mathcal{C} . This procedure outputs a *garbled circuit* $\tilde{\mathcal{C}}$ and labels $\{\text{lab}_{w,b}\}_{w \in \text{inp}(\mathcal{C}), b \in \{0,1\}}$ where each $\text{lab}_{w,b} \in \{0, 1\}^\lambda$.⁹
- $y := \text{Eval}(\tilde{\mathcal{C}}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(\mathcal{C})})$: Given a garbled circuit $\tilde{\mathcal{C}}$ and a garbled input represented as a sequence of input labels $\{\text{lab}_{w,x_w}\}_{w \in \text{inp}(\mathcal{C})}$, Eval outputs an output y .

Correctness. For correctness, we require that for any circuit \mathcal{C} and input $x \in \{0, 1\}^m$ (here m is the input length to \mathcal{C}) we have that:

$$\Pr \left[\mathcal{C}(x) = \text{Eval} \left(\tilde{\mathcal{C}}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(\mathcal{C})} \right) \right] = 1$$

where $(\tilde{\mathcal{C}}, \{\text{lab}_{w,b}\}_{w \in \text{inp}(\mathcal{C}), b \in \{0,1\}}) \xleftarrow{\$} \text{GCircuit}(1^\lambda, \mathcal{C})$.

⁹Typical definitions of garbled circuits do not require the length of each input label to be λ bits long. This additional requirement is crucial in our constructions as we chain garbled circuits. Note that input labels in any garbled circuit construction can always be shrunk to λ bits using a pseudorandom function.

Security. For security, we require that there is a PPT simulator Sim such that for any C, x , we have that

$$\left(\tilde{C}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(C)}\right) \stackrel{\text{comp}}{\approx} \text{Sim}\left(1^\lambda, C(x)\right)$$

where $(\tilde{C}, \{\text{lab}_{w,b}\}_{w \in \text{inp}(C), b \in \{0,1\}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, C)$.¹⁰

4 Chameleon Encryption

In this section, we give the definition of a chameleon encryption scheme.

Definition 4.1 (Chameleon Encryption). *A chameleon encryption scheme consists of five PPT algorithms Gen , H , H^{-1} , Enc , and Dec with the following syntax.*

- $\text{Gen}(1^\lambda, n)$: Takes the security parameter λ and a message-length n (with $n = \text{poly}(\lambda)$) as input and outputs a key \mathbf{k} and a trapdoor \mathbf{t} .
- $\text{H}(\mathbf{k}, \mathbf{x}; r)$: Takes a key \mathbf{k} , a message $\mathbf{x} \in \{0,1\}^n$, and coins r and outputs a hash value \mathbf{h} , where \mathbf{h} is λ bits.
- $\text{H}^{-1}(\mathbf{t}, (\mathbf{x}, r), \mathbf{x}')$: Takes a trapdoor \mathbf{t} , previously used message $\mathbf{x} \in \{0,1\}^n$ and coins r , and a message $\mathbf{x}' \in \{0,1\}^n$ as input and returns r' .
- $\text{Enc}(\mathbf{k}, (\mathbf{h}, i, b), \mathbf{m})$: Takes a key \mathbf{k} , a hash value \mathbf{h} , an index $i \in [n]$, $b \in \{0,1\}$, and a message $\mathbf{m} \in \{0,1\}^*$ as input and outputs a ciphertext ct .¹¹
- $\text{Dec}(\mathbf{k}, (\mathbf{x}, r), \text{ct})$: Takes a key \mathbf{k} , a message \mathbf{x} , coins r and a ciphertext ct , as input and outputs a value \mathbf{m} (or \perp).

We require the following properties¹²

- **Uniformity:** For $\mathbf{x}, \mathbf{x}' \in \{0,1\}^n$ we have that the two distributions $\text{H}(\mathbf{k}, \mathbf{x}; r)$ and $\text{H}(\mathbf{k}, \mathbf{x}'; r')$ are statistically close (when r, r' are chosen uniformly at random).
- **Trapdoor Collisions:** For every choice of $\mathbf{x}, \mathbf{x}' \in \{0,1\}^n$ and r it holds that if $(\mathbf{k}, \mathbf{t}) \stackrel{\$}{\leftarrow} \text{Gen}(1^\lambda, n)$ and $r' := \text{H}^{-1}(\mathbf{t}, (\mathbf{x}, r), \mathbf{x}')$, then it holds that

$$\text{H}(\mathbf{k}, \mathbf{x}; r) = \text{H}(\mathbf{k}, \mathbf{x}'; r'),$$

i.e. $\text{H}(\mathbf{k}, \mathbf{x}; r)$ and $\text{H}(\mathbf{k}, \mathbf{x}'; r')$ generate the same hash \mathbf{h} . Moreover, if r is chosen uniformly at random, then r' is also statistically close to uniform.

- **Correctness:** For any choice of $\mathbf{x} \in \{0,1\}^n$, coins r , index $i \in [n]$ and message \mathbf{m} it holds that if $(\mathbf{k}, \mathbf{t}) \stackrel{\$}{\leftarrow} \text{Gen}(1^\lambda, n)$, $\mathbf{h} := \text{H}(\mathbf{k}, \mathbf{x}; r)$, and $\text{ct} \stackrel{\$}{\leftarrow} \text{Enc}(\mathbf{k}, (\mathbf{h}, i, x_i), \mathbf{m})$ then $\text{Dec}(\mathbf{k}, \text{ct}, (\mathbf{x}, r)) = \mathbf{m}$.

¹⁰In abuse of notation we assume that Sim knows the (non-private) circuit C . When C has (private) hardwired inputs, we assume that the labels corresponding to these are included in the garbled circuit \tilde{C} .

¹¹ ct is assumed to contain (\mathbf{h}, i, b) .

¹²Typically, Chameleon Hash functions are defined to also have the collision resilience property. This property is implied by the semantic security requirement below. However, we do not need this property directly. Therefore, we do not explicitly define it here.

- **Security:** For any PPT adversary $\mathcal{A} = (\mathcal{A}_1, \mathcal{A}_2)$ there exists a negligible function $\text{negl}(\cdot)$ such that the following holds:

$$\Pr[\text{IND}_{\mathcal{A}}^{\text{CE}}(1^\lambda) = 1] \leq \frac{1}{2} + \text{negl}(\lambda)$$

where $\text{IND}_{\mathcal{A}}^{\text{CE}}$ is shown in Figure 3.

Experiment $\text{IND}_{\mathcal{A}=(\mathcal{A}_1, \mathcal{A}_2)}^{\text{CE}}(1^\lambda)$:

1. $(\mathbf{k}, \mathbf{t}) \xleftarrow{\$} \text{Gen}(1^\lambda, n)$.
2. $(\mathbf{x}, r, i \in [n], \text{st}) \xleftarrow{\$} \mathcal{A}_1(\mathbf{k})$.
3. $b \xleftarrow{\$} \{0, 1\}$.
4. $\text{ct} \xleftarrow{\$} \text{Enc}(\mathbf{k}, (\text{H}(\mathbf{k}, \mathbf{x}; r), i, 1 - x_i), b)$.
5. $b' \xleftarrow{\$} \mathcal{A}_2(\mathbf{k}, \text{ct}, (\mathbf{x}, r), \text{st})$.
6. Output 1 if $b = b'$ and 0 otherwise.

Figure 3: The $\text{IND}_{\mathcal{A}}^{\text{CE}}$ Experiment

5 Constructions of Chameleon Encryption from CDH

Let (\mathbb{G}, \cdot) be a cyclic group of order p (not necessarily prime) with generator g . Let $\text{Sample}(\mathbb{G})$ be a PPT algorithm such that its output is statistically close to a uniform element in \mathbb{Z}_p , where p (not necessarily prime) is the order of \mathbb{G} .¹³ We will now describe a chameleon encryption scheme assuming that the $\text{DH}(\mathbb{G})$ problem is hard.

- $\text{Gen}(1^\lambda, n)$: For each $j \in [n]$, choose uniformly random values $\alpha_{j,0}, \alpha_{j,1} \xleftarrow{\$} \text{Sample}(\mathbb{G})$ and compute $g_{j,0} := g^{\alpha_{j,0}}$ and $g_{j,1} := g^{\alpha_{j,1}}$. Output (\mathbf{k}, \mathbf{t}) where¹⁴

$$\mathbf{k} := \left(g, \left(\begin{array}{c} g_{1,0}, g_{2,0}, \dots, g_{n,0} \\ g_{1,1}, g_{2,1}, \dots, g_{n,1} \end{array} \right) \right) \quad \mathbf{t} := \left(\begin{array}{c} \alpha_{1,0}, \alpha_{2,0}, \dots, \alpha_{n,0} \\ \alpha_{1,1}, \alpha_{2,1}, \dots, \alpha_{n,1} \end{array} \right). \quad (2)$$

- $\text{H}(\mathbf{k}, \mathbf{x}; r)$: Parse \mathbf{k} as in Equation 2, sample $r \xleftarrow{\$} \text{Sample}(\mathbb{G})$, set $\mathbf{h} := g^r \cdot \prod_{j \in [n]} g_{j, x_j}$ and output \mathbf{h}
- $\text{H}^{-1}(\mathbf{t}, (\mathbf{x}, r), \mathbf{x}')$: Parse \mathbf{t} as in Equation 2, compute $r' := r + \sum_{j \in [n]} (\alpha_{j, x_j} - \alpha_{j, x'_j}) \pmod{p}$. Output r' .
- $\text{Enc}(\mathbf{k}, (\mathbf{h}, i, b), \mathbf{m})$: Parse \mathbf{k} as in Equation 2, $\mathbf{h} \in \mathbb{G}$ and $\mathbf{m} \in \{0, 1\}$. Sample $\rho \xleftarrow{\$} \text{Sample}(\mathbb{G})$ and proceed as follows:

¹³We will later provide instantiations of \mathbb{G} which are of prime order and composite order. The use of $\text{Sample}(\mathbb{G})$ procedure is done to unify these two instantiations.

¹⁴We also implicitly include the public and secret parameters for the group \mathbb{G} in \mathbf{k} and \mathbf{t} respectively.

1. Set $c := g^\rho$ and $c' := h^\rho$.
 2. For every $j \in [n] \setminus \{i\}$, set $c_{j,0} := g_{j,0}^\rho$ and $c_{j,1} := g_{j,1}^\rho$.
 3. Set $c_{i,0} := \perp$ and $c_{i,1} := \perp$.
 4. Set $e := m \oplus \text{HardCore}(g_{i,b}^\rho)$.¹⁵
 5. Output $\text{ct} := \left(e, c, c', \begin{pmatrix} c_{1,0}, c_{2,0}, \dots, c_{n,0} \\ c_{1,1}, c_{2,1}, \dots, c_{n,1} \end{pmatrix} \right)$.
- $\text{Dec}(k, (x, r), \text{ct})$: Parse $\text{ct} = \left(e, c, c', \begin{pmatrix} c_{1,0}, c_{2,0}, \dots, c_{n,0} \\ c_{1,1}, c_{2,1}, \dots, c_{n,1} \end{pmatrix} \right)$
- Output $e \oplus \text{HardCore} \left(\frac{c'}{c^r \cdot \prod_{j \in [n] \setminus \{i\}} c_{j,x_j}} \right)$.

Multi-bit Encryption. The encryption procedure described above encrypts single bit messages. Longer messages can be encrypted by encrypting individual bits.

Lemma 5.1. *Assuming that $\text{DH}(\mathbb{G})$ is hard, the construction described above is a chameleon encryption scheme, i.e. it satisfies Definition 4.1.*

Proof. We need to argue the trapdoor collision property, uniformity property, correctness of encryption property and semantic security of the scheme above and we that below.

- **Uniformity:** Observe that for all k and x , we have that $H(k, x; r) = g^r \cdot \prod_{j \in [n]} g_{j,x_j}$ is statistically close to a uniform element in \mathbb{G} . This is because r is sampled statistically close to uniform in \mathbb{Z}_p , where p is the order of \mathbb{G} .
- **Trapdoor Collisions:** For any choice of x, x', r, k, t the value r' is obtained as $r + \sum_{j \in [n]} (\alpha_{j,x_j} - \alpha_{j,x'_j}) \pmod p$. We need to show that $H(k, x'; r')$ is equal to $H(k, x; r)$. This can be established as follows.

$$\begin{aligned}
H(k, x'; r') &= g^{r'} \cdot \prod_{j \in [n]} g_{j,x'_j} \\
&= g^{r + \sum_{j \in [n]} (\alpha_{j,x_j} - \alpha_{j,x'_j})} \cdot \prod_{j \in [n]} g^{\alpha_{j,x'_j}} \\
&= g^{r + \sum_{j \in [n]} (\alpha_{j,x_j} - \alpha_{j,x'_j})} \cdot g^{\sum_{j \in [n]} \alpha_{j,x'_j}} \\
&= g^{r + \sum_{j \in [n]} \alpha_{j,x_j}} \\
&= g^r \cdot \prod_{j \in [n]} g^{\alpha_{j,x_j}} \\
&= g^r \cdot \prod_{j \in [n]} g_{j,x_j} \\
&= H(k, x; r).
\end{aligned}$$

¹⁵We assume that the $\text{HardCore}(g^{ab})$ is a hardcore bit of g^{ab} given g^a and g^b . If a deterministic hard-core bit for the specific function is not known then we can always use the Goldreich-Levin [GL89] construction. We skip the details of that with the goal of keeping exposition simple.

Moreover, as r is statistically close to uniform in \mathbb{Z}_p , $r' := r + \sum_{j \in [n]} (\alpha_{j,x_j} - \alpha_{j,x'_j}) \pmod p$ is also statistically close to uniform in \mathbb{Z}_p .

- **Correctness:** For any choice of $\mathbf{x} \in \{0, 1\}^n$, coins r , index $i \in [n]$ and message $\mathbf{m} \in \{0, 1\}$ if $(\mathbf{k}, \mathbf{t}) \stackrel{\$}{\leftarrow} \text{Gen}(1^\lambda, n)$, $\mathbf{h} := \text{H}(\mathbf{k}, \mathbf{x}; r)$, and $\text{ct} := \text{Enc}(\mathbf{k}, (\mathbf{h}, i, x_i), \mathbf{m})$ then we have:

$$\begin{aligned} \frac{c'}{c^r \cdot \prod_{j \in [n] \setminus \{i\}} c_{j,x_j}} &= \frac{\mathbf{h}^\rho}{g^{\rho \cdot r} \cdot \prod_{j \in [n] \setminus \{i\}} g^{\rho \cdot \alpha_{j,x_j}}} \\ &= \frac{g^{\rho \cdot r} \prod_{j \in [n]} g^{\rho \cdot \alpha_{j,x_j}}}{g^{\rho \cdot r} \cdot \prod_{j \in [n] \setminus \{i\}} g^{\rho \cdot \alpha_{j,x_j}}} \\ &= g^{\rho \cdot \alpha_{i,x_i}} \\ &= g_{i,x_i}^\rho \end{aligned}$$

Using the above calculation and parsing $\text{ct} = \left(e, c, c', \left(\begin{array}{c} c_{1,0}, c_{2,0}, \dots, c_{n,0} \\ c_{1,1}, c_{2,1}, \dots, c_{n,1} \end{array} \right) \right)$ allows us to conclude that

$$\begin{aligned} \text{Dec}(\mathbf{k}, (\mathbf{x}, r), \text{ct}) &= e \oplus \text{HardCore} \left(\frac{c'}{c^r \cdot \prod_{j \in [n] \setminus \{i\}} c_{j,x_j}} \right) \\ &= e \oplus \text{HardCore}(g_{i,x_i}^\rho) \\ &= \mathbf{m} \oplus \text{HardCore}(g_{i,x_i}^\rho) \oplus \text{HardCore}(g_{i,x_i}^\rho) \\ &= \mathbf{m}. \end{aligned}$$

- **Security:** For the sake of contradiction, let us assume that there exists a PPT adversary $\mathcal{A} = (\mathcal{A}_1, \mathcal{A}_2)$ and a non-negligible function $\mu(\cdot)$ such that

$$\Pr[\text{IND}_{\mathcal{A}}^{\text{CE}}(1^\lambda) = 1] \geq \frac{1}{2} + \mu(\lambda).$$

Now we will provide a PPT reduction $\mathcal{R}^{\mathcal{A}}$ which on input $g, U = g^u, V = g^v$ correctly computes the hardcore bit $\text{HardCore}(g^{uv})$ with probability $\frac{1}{2} + \nu(\lambda)$ for some non-negligible function ν . Formally, **Reduction** $\mathcal{R}^{\mathcal{A}=(\mathcal{A}_1, \mathcal{A}_2)}(g, U, V)$ proceeds as follows:

1. For each $j \in [n]$, sample $\alpha_{j,0}, \alpha_{j,1} \stackrel{\$}{\leftarrow} \text{Sample}(\mathbb{G})$ and set $g_{j,0} := g^{\alpha_{j,0}}$ and $g_{j,1} := g^{\alpha_{j,1}}$.
2. Sample $x \stackrel{\$}{\leftarrow} \{0, 1\}$ and $i^* \stackrel{\$}{\leftarrow} [n]$ and reassign $g_{i^*,x} := U$. Finally set

$$\mathbf{k} := \left(g, \left(\begin{array}{c} g_{1,0}, g_{2,0}, \dots, g_{n,0} \\ g_{1,1}, g_{2,1}, \dots, g_{n,1} \end{array} \right) \right).$$

3. $(\mathbf{x}, r, i) \stackrel{\$}{\leftarrow} \mathcal{A}_1(\mathbf{k})$.
4. If $i \neq i^*$ or $x_i = x$ then skip rest of the steps and output a random bit $b \stackrel{\$}{\leftarrow} \{0, 1\}$.

5. Otherwise, set $h := H(k, x; r)$ and $ct := \left(e, c, c', \begin{pmatrix} c_{1,0}, c_{2,0} \cdots, c_{n,0} \\ c_{1,1}, c_{2,1}, \cdots, c_{n,1} \end{pmatrix} \right)$ where:

$$\begin{aligned} c &:= V & c' &:= V^{r + \sum_{j \in [n]} \alpha_{i, x_j}}, \\ \forall j \in [n] \setminus \{i\}, \quad c_{j,0} &:= V^{\alpha_{j,0}} & c_{j,1} &:= V^{\alpha_{j,1}}, \\ e &\stackrel{\$}{\leftarrow} \{0, 1\}. \end{aligned}$$

6. $b \stackrel{\$}{\leftarrow} \mathcal{A}_2(k, (x, r), ct)$.

7. Output $b \oplus e$.

Let E be the event that the $i = i^*$ and $x_i \neq x$. Now observe that the distribution of k in Step 3 is statistically close to distribution resulting from Gen. This implies that (1) the view of the attacker in Step 3 is statistically close to experiment $\text{IND}_{\mathcal{A}}^{\text{CE}}$, and (2) $\Pr[E]$ is close to $\frac{1}{2n}$ up to a negligible additive term. Furthermore, conditioned on the fact that E occurs we have that the view of the attacker in Step 3 is statistically close to experiment $\text{IND}_{\mathcal{A}}^{\text{CE}}$ where ct is an encryption of $e \oplus \text{HardCore}(g^{uv})$ (where $U = g^u$ and $V = g^v$). Now, if \mathcal{A}_2 in Step 6 correctly predicts $e \oplus \text{HardCore}(g^{uv})$ then we have that the output of our reduction \mathcal{R} is a correct prediction of $\text{HardCore}(g^{uv})$. Thus, we conclude that \mathcal{R} predicts $\text{HardCore}(g^{uv})$ correctly with probability at least $\frac{1}{2} \cdot \left(1 - \frac{1}{2n}\right) + \frac{1}{2n} \cdot \left(\frac{1}{2} + \mu\right) = \frac{1}{2} + \frac{\mu}{2n}$ up to a negligible additive term. □

5.1 Instantiations

Instantiating by prime order groups. Our scheme can be directly instantiated in any prime order group \mathbb{G} where $\text{DH}(\mathbb{G})$ is assumed to be hard. Candidates are prime order multiplicative subgroups of finite fields [DH76] and elliptic curve groups [Mil86, Kob87].

Corollary 5.2. *Under the assumption that $\text{DH}(\mathbb{G})$ is hard over some group \mathbb{G} , there exists a chameleon encryption scheme.*

Instantiating by composite order groups and reduction to the Factoring Assumption.

Consider the group of quadratic residues \mathbb{QR}_N over a Blum integer $N = PQ$ (P and Q are large safe primes¹⁶ with $P = Q = 3 \pmod{4}$). Let g be a random generator of \mathbb{G} and $\text{Sample}(\mathbb{G})$ just outputs a uniformly random number from the set $[(N-1)/4]$. Shmueli [Shm85] and McCurley [McC88] proved that the $\text{DH}(\mathbb{QR}_N)$ problem is at least as hard as FACT (also see [BBR97, HK09]).

For this instantiation, we assume that the Gen algorithm generates a fresh Blum integer $N = PQ = (2p+1)(2q+1)$, includes N in the public key k and $|\mathbb{G}| = |\mathbb{QR}_N| = \phi(N)/4 = pq$ in the trapdoor t . Notice that only the trapdoor-collision algorithm H^{-1} needs to know the group-order $|\mathbb{G}| = pq$, while all other algorithms use the public sampling algorithm $\text{Sample}(\mathbb{G})$.

Hence, using the group \mathbb{QR}_N in the above described construction yields a construction of chameleon encryption based on the FACT Assumption.

Corollary 5.3. *Under the assumption that FACT is hard there exists a chameleon encryption scheme.*

¹⁶A prime number $P > 2$ is called safe prime if $(P-1)/2$ is also prime

6 Construction of Identity-Based Encryption

In this section, we describe our construction of IBE from chameleon encryption. Let $\text{PRF} : \{0, 1\}^\lambda \times \{0, 1\}^{\leq n} \cup \{\varepsilon\} \rightarrow \{0, 1\}^\lambda$ be a pseudorandom function, $(\text{Gen}, \text{H}, \text{H}^{-1}, \text{Enc}, \text{Dec})$ be a chameleon encryption scheme and $(\text{G}, \text{E}, \text{D})$ be any semantically secure public-key encryption scheme.¹⁷ We let $\text{id}[i]$ denote the i^{th} -bit of id and let $\text{id}[1 \dots i]$ denote the first i bits of id . Note that $\text{id}[1 \dots 0]$ is the empty string denoted by ε of length 0.

NodeGen and LeafGen functions. As explained in the introduction, we need an exponential sized tree of hash values. The functions **NodeGen** and **LeafGen** provides efficient access to the *hash value* corresponding to any node in this (exponential sized) tree. We will use these function repeatedly in our construction. The **NodeGen** function takes as input the hash keys k_0, \dots, k_{n-1} and corresponding trapdoors t_0, \dots, t_{n-1} , the PRF seed s , and a node $v \in \{0, 1\}^{\leq n-2} \cup \{\varepsilon\}$. On the other hand, the **LeafGen** function takes as input the hash key k_{n-1} and corresponding trapdoor t_{n-1} , the PRF seed s , and a node $v \in \{0, 1\}^{n-1}$. The **NodeGen** and **LeafGen** functions are described in Figure 4.

NodeGen($(k_0, \dots, k_{n-1}), (t_0, \dots, t_{n-1}), s, v$):	LeafGen($k_{n-1}, (t_{n-1}), s, v$):
<ol style="list-style-type: none"> 1. Let $i := v$ (length of v) and generate <ul style="list-style-type: none"> $h_v := \text{H}(k_i, 0^{2\lambda}; \text{PRF}(s, v))$, $h_{v\ 0} := \text{H}(k_{i+1}, 0^{2\lambda}; \text{PRF}(s, v\ 0))$, $h_{v\ 1} := \text{H}(k_{i+1}, 0^{2\lambda}; \text{PRF}(s, v\ 1))$. 2. $r_v := \text{H}^{-1}(t_v, (0^{2\lambda}, \text{PRF}(s, v)), h_{v\ 0}\ h_{v\ 1})$. 3. Output $(h_v, h_{v\ 0}, h_{v\ 1}, r_v)$. 	<ol style="list-style-type: none"> 1. Generate <ul style="list-style-type: none"> $h_v := \text{H}(k_{n-1}, 0^{2\lambda}; \text{PRF}(s, v))$ $(ek_{v\ 0}, dk_{v\ 0}) := \text{G}(1^\lambda; \text{PRF}(s, v\ 0))$, $(ek_{v\ 1}, dk_{v\ 1}) := \text{G}(1^\lambda; \text{PRF}(s, v\ 1))$. 2. $r_v := \text{H}^{-1}(t_{n-1}, (0^{2\lambda}, \text{PRF}(s, v)), ek_{v\ 0}\ ek_{v\ 1})$. 3. Output $((h_v, ek_{v\ 0}, ek_{v\ 1}, r_v), dk_{v\ 0}, dk_{v\ 1})$.

Figure 4: Description of NodeGen and LeafGen.

Construction. We describe our IBE scheme (**Setup**, **KeyGen**, **Encrypt**, **Decrypt**).

- **Setup**($1^\lambda, 1^n$): Proceed as¹⁸ follows:

1. Sample $s \xleftarrow{\$} \{0, 1\}^\lambda$ (seeds for the pseudorandom function PRF).
2. For each $i \in \{0, \dots, n-1\}$ sample $(k_i, t_i) \xleftarrow{\$} \text{Gen}(1^\lambda, 2\lambda)$.
3. Obtain $(h_\varepsilon, h_0, h_1, r_\varepsilon) := \text{NodeGen}((k_0, \dots, k_{n-1}), (t_0, \dots, t_{n-1}), s, \varepsilon)$

¹⁷The algorithm **G** takes as input the security parameter 1^λ and generates encryption key and decryption key pair ek and dk respectively, where the encryption key ek is assumed to be λ bits long. The encryption algorithm $\text{E}(ek, m)$ takes as input an encryption key ek and a message m and outputs a ciphertext ct . Finally, the decryption algorithm $\text{D}(dk, ct)$ takes as input the secret key and the ciphertext and outputs the encrypted message m .

¹⁸The IBE scheme defined in Section 3 does not fix the length of identities that it can be used with. However, in this section we fix the length of identities at setup time and use appropriately changed definitions. Looking ahead, the HIBE construction in Section 7 works for identities of arbitrary length.

4. Output (mpk, msk) where $\text{mpk} := (k_0, \dots, k_{n-1}, h_\varepsilon)$ and $\text{msk} := (\text{mpk}, t_0, \dots, t_{n-1}, s)$

- $\text{KeyGen}(\text{msk} = ((k_0, \dots, k_{n-1}, h_\varepsilon), t_0, \dots, t_{n-1}, s), \text{id} \in \{0, 1\}^n)$:

$V := \{\varepsilon, \text{id}[1], \dots, \text{id}[1 \dots n-1]\}$, where ε is the empty string

For all $v \in V \setminus \{\text{id}[1 \dots n-1]\}$:

$\text{lk}_v := \text{NodeGen}((k_0, \dots, k_{n-1}), (t_0, \dots, t_{n-1}, s), v)$

For $v = \text{id}[1 \dots n-1]$, set $(\text{lk}_v, \text{dk}_{v\|0}, \text{dk}_{v\|1}) := \text{LeafGen}(k_{n-1}, (t_{n-1}, s), v)$

$\text{sk}_{\text{id}} := (\text{id}, \{\text{lk}_v\}_{v \in V}, \text{dk}_{\text{id}})$

- $\text{Encrypt}(\text{mpk} = (k_0, \dots, k_{n-1}, h_\varepsilon), \text{id} \in \{0, 1\}^n, m)$: Before describing the encryption procedure we describe two circuits¹⁹ that will be garbled during the encryption process.

- $\text{T}[m](\text{ek})$: Compute and output $\text{E}(\text{ek}, m)$.
- $\text{P}[\beta \in \{0, 1\}, k, \overline{\text{lab}}](h)$: Compute and output $\{\text{Enc}(k, (h, j + \beta \cdot \lambda, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$, where $\overline{\text{lab}}$ is short for $\{\text{lab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$.

Encryption proceeds as follows:

1. Compute \tilde{T} as:

$$(\tilde{T}, \overline{\text{lab}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{T}[m]).$$

2. For $i = n-1, \dots, 0$ generate $(\tilde{P}^i, \overline{\text{lab}}^i) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{P}[\text{id}[i+1], k_i, \overline{\text{lab}}])$ and set $\overline{\text{lab}} := \overline{\text{lab}}^i$.
3. Output $\text{ct} := (\{\text{lab}_{j, h_{\varepsilon,j}}\}_{j \in [\lambda]}, \{\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}\})$ where $h_{\varepsilon,j}$ is the j^{th} bit of h_ε .

- $\text{Decrypt}(\text{ct}, \text{sk}_{\text{id}} = (\text{id}, \{\text{lk}_v\}_{v \in V}, \text{dk}_{\text{id}}))$: Decryption proceeds as follows:

1. Parse ct as $(\{\text{lab}_{j, h_{\varepsilon,j}}\}_{j \in [\lambda]}, \{\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}\})$.
2. Parse lk_v as $(h_v, h_{v\|0}, h_{v\|1}, r_v)$ for each $v \in V \setminus \{\text{id}[1 \dots n-1]\}$. (Recall $V = \{\varepsilon, \text{id}[1] \dots \text{id}[1 \dots n-1]\}$.)
3. And for $v = \text{id}[1 \dots n-1]$, parse lk_v as $(h_v, \text{ek}_{v\|0}, \text{ek}_{v\|1}, r_v)$.
4. Set $y := h_\varepsilon$.
5. For each $i \in \{0, \dots, n-1\}$, set $v := \text{id}[1 \dots i]$, and proceed as follows:

(a) $\{e_{j, y_j}\}_{j \in [\lambda], b \in \{0,1\}} := \text{Eval}(\tilde{P}^i, \{\text{lab}_{j, y_j}\}_{j \in [\lambda]})$.

(b) If $i = n-1$ then set $y := \text{ek}_{\text{id}}$ and for each $j \in [\lambda]$, compute

$$\text{lab}_{j, y_j} := \text{Dec}(k_v, e_{j, y_j}, (\text{ek}_{v\|0} \parallel \text{ek}_{v\|1}, r_v)).$$

(c) If $i \neq n-1$ then set $y := h_v$ and for each $j \in [\lambda]$, compute

$$\text{lab}_{j, y_j} := \text{Dec}(k_v, e_{j, y_j}, (h_{v\|0} \parallel h_{v\|1}, r_v)).$$

6. Compute $f := \text{Eval}(\tilde{T}, \{\text{lab}_{j, y_j}\}_{j \in [\lambda]})$.
7. Output $m := \text{Dec}(\text{dk}_{\text{id}}, f)$.

¹⁹Random coins used by these circuits are hardwired in them. For simplicity, we do not mention them explicitly.

A note on efficiency. The most computationally intensive part of the construction is the non-black box use of Enc inside garblings of the circuit P and E inside garbling of the circuit T . However, we note that not all of the computation corresponding to Enc and E needs to be performed inside the garbled circuit and it might be possible to push some of it outside of the garbled circuits. In particular, when Enc is instantiated with the DDH based chameleon encryption scheme then we can reduce each Enc to a single modular exponentiation inside the garbled circuit. Similar optimization can be performed for E . In short, this reduces the number of non-black-box modular exponentiations to 2λ for every circuit P and 1 for the circuit T . Finally, we note that additional improvements in efficiency might be possible by increasing the arity of the tree from 2 to a larger value. This would also reduce the depth of the tree and thereby reduce the number of non-black-box modular exponentiations needed.

6.1 Proof of Correctness

We will first show that our scheme is correct. For any identity id , let $V = \{\varepsilon, \text{id}[1], \dots, \text{id}[1 \dots n-1]\}$. Then the secret key sk_{id} consists of $(\text{id}, \{\text{lk}_v\}_{v \in V}, \text{dk}_{\text{id}})$. We will argue that a correctly generated ciphertext on decryption reveals the original message. Note that by construction (and the trapdoor collision property of the chameleon encryption scheme for the first equation below) for all nodes $v \in V \setminus \{\text{id}[1 \dots n-1]\}$ we have that:

$$\text{H}(\text{k}_{|v|}, \text{h}_{v||0} \| \text{h}_{v||1}; r_v) = \text{h}_v.$$

and additionally for $v = \text{id}[1 \dots n-1]$ we have

$$\text{H}(\text{k}_{n-1}, \text{ek}_{v||0} \| \text{ek}_{v||1}; r_v) = \text{h}_v.$$

Next consider a ciphertext $\text{ct} = (\{\text{lab}_{j, \text{h}_{\varepsilon, j}}\}_{j \in [\lambda]}, \{\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}\})$. We argue correctness as each step of decryption is performed. By correctness of garbled circuits, we have that the evaluation of \tilde{P}^0 yields correctly formed ciphertexts $e_{j,b}$ which are encryptions of labels of the next garbled circuit \tilde{P}^1 . Next, by correctness of Dec of the chameleon encryption scheme we have that the decrypting the appropriate ciphertexts yields the correct labels $\{\text{lab}_{j, \text{h}_{\text{id}[1], j}}\}_{j \in [\lambda]}$ for the next garbled circuit, namely \tilde{P}^1 . Following the same argument we can argue that the decryption of the appropriate ciphertexts generated by \tilde{P}^1 yields the correct input labels for \tilde{P}^2 . Repeatedly applying this argument allows us to conclude that the last garbled circuit \tilde{P}^{n-1} outputs labels corresponding to ek_{id} as input for the circuit T which outputs an encryption of m under ek_{id} . Finally, using the correctness of the public-key encryption scheme $(\text{G}, \text{E}, \text{D})$ we have that the recovered message m is the same as the one encrypted.

6.2 Proof of Security

We are now ready to prove the security of the IBE construction above. For the sake of contradiction we proceed by assuming that there exists an adversary \mathcal{A} such that $\Pr[\text{IND}_{\mathcal{A}}^{\text{IBE}}(1^\lambda) = 1] \geq \frac{1}{2} + \epsilon$ for a non-negligible ϵ (in λ), where $\text{IND}_{\mathcal{A}}^{\text{IBE}}$ is shown in Figure 1. Assume further that q is a polynomial upper bound for the running-time of \mathcal{A} , and thus also an upper bound for the number of \mathcal{A} 's key queries. Security follows by a sequence of hybrids. In our hybrids, changes are made in how the secret key queries of the adversary \mathcal{A} are answered and how the challenge ciphertext is generated. Furthermore, these changes are intertwined and need to be done carefully. Our proof consist of a sequence of $n + 2$ hybrids $\mathcal{H}_{-1}, \mathcal{H}_0, \mathcal{H}_1, \dots, \mathcal{H}_{n+1}$. We next describe these hybrids .

- \mathcal{H}_{-1} : This hybrid corresponds to the experiment $\text{IND}_{\mathcal{A}}^{\text{IBE}}$ as shown in Figure 1.
- \mathcal{H}_0 : In this hybrid, we change how the public parameters are generated and how the adversary's requests to the KeyGen oracle are answered. Specifically, we replace all pseudorandom function calls $\text{PRF}(s, \cdot)$ with a random function.

The only change from \mathcal{H}_{-1} to \mathcal{H}_0 is that calls to a pseudorandom are replaced by a random function. Therefore, the indistinguishability between the two hybrids follows directly from the pseudorandomness property of the pseudorandom function.

- \mathcal{H}_τ for $\tau \in \{0 \dots n\}$: For every τ , this hybrid is identical to the experiment \mathcal{H}_0 except in how the ciphertext is generated. Recall that the challenge ciphertext consists of a sequence of $n + 1$ garbled circuits. In hybrid \mathcal{H}_τ , we generate the first τ of these garbled circuits using the simulator provided by the garbled circuit construction. The outputs hard-coded in the simulated circuits are set to be consistent with the output that would have resulted from the execution of honestly generated garbled circuits in their unsimulated versions. More formally, for the challenge identity id^* the challenge ciphertext is generated as follows (modifications with respect to honest ciphertext generation have been highlighted in red). Even though, the adversary never queries sk_{id} , we can generate it locally. In particular, it contains the values $\text{lk}_v = (\mathbf{h}_v, \mathbf{h}_{v\parallel 0}, \mathbf{h}_{v\parallel 1}, r_v)$ for each $v \in \{\varepsilon, \dots, \text{id}[1 \dots n - 2]\}$, $\text{lk}_v = (\mathbf{h}_v, \text{ek}_{v\parallel 0}, \text{ek}_{v\parallel 1}, r_v)$ for each $v = \text{id}[1 \dots n - 1]$, and dk_{id^*} .

1. Compute \tilde{T} as:

If $\tau \neq n$

$$(\tilde{T}, \overline{\text{lab}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \mathbb{T}[m])$$

where $\overline{\text{lab}} = \{\text{lab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$. Else set $y = \text{ek}_{\text{id}^*}$ and generate garbled circuit as,

$$(\tilde{T}, \{\text{lab}_{j,y_j}\}_{j \in [\lambda]}) \stackrel{\$}{\leftarrow} \text{Sim}(1^\lambda, \mathbf{E}(y, m))$$

and set $\overline{\text{lab}} := \{\text{lab}_{j,y_j}, \text{lab}_{j,y_j}\}_{j \in [\lambda]}$.

2. For $i = n - 1, \dots, \tau$ generate $(\tilde{P}^i, \overline{\text{lab}}') \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \mathbb{P}[\text{id}[i + 1], \mathbf{k}_i, \overline{\text{lab}}])$ and set $\overline{\text{lab}} := \overline{\text{lab}}'$.
3. For $i = \tau - 1, \dots, 0$, set $v = \text{id}^*[1 \dots i - 1]$ and generate

$$\tilde{P}^i, \{\text{lab}'_{j, \mathbf{h}_{v,j}}\}_{j \in [\lambda]} := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_v, (\mathbf{h}_v, j, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}})$$

and set $\overline{\text{lab}} := \{\text{lab}'_{j, \mathbf{h}_{v,j}}, \text{lab}'_{j, \mathbf{h}_{v,j}}\}_{j \in [\lambda]}$.

4. Output $\text{ct} := (\{\text{lab}_{j, \mathbf{h}_{\varepsilon,j}}\}_{j \in [\lambda]}, \{\tilde{P}^0, \dots, \tilde{P}^{n-1}, \tilde{T}\})$ where $\mathbf{h}_{\varepsilon,j}$ is the j^{th} bit of \mathbf{h}_ε .

The computational indistinguishability between hybrids $\mathcal{H}_{\tau-1}$ and \mathcal{H}_τ is based on Lemma 6.1 which is proved in Section 6.3.

Lemma 6.1. *For each $\tau \in \{1 \dots n\}$ it is the case that $\mathcal{H}_{\tau-1} \stackrel{c}{\approx} \mathcal{H}_\tau$.*

- \mathcal{H}_{n+1} : This hybrid is same as \mathcal{H}_n except that we change the ciphertext $E(\text{ek}_{\text{id}^*}, m)$ hardwired in the simulated garbling of the circuit T to be $E(\text{ek}_{\text{id}^*}, 0)$.

We can use an adversary distinguishing between \mathcal{H}_n and \mathcal{H}_{n+1} to construct an attacker against the semantic security of the public-key encryption scheme (G, E, D) in the following way. Let q be a polynomial upper bound for the number of queries of the adversary (key-queries and the challenge query). The reduction gets as input a public key ek . There are two cases that might happen. In the first one, the challenge identity will be the *sibling* of an identity for which the adversary makes a key-query, i.e. the adversary gets to see $\text{ek}_{v\|0}$ and $\text{ek}_{v\|1}$ such that *wlog* $\text{ek}_{v\|0}$ is the leaf key for a key-query of the adversary and $\text{ek}_{v\|1}$ is the key for the challenge identity $v\|1$. In the second one, this is not the case. To deal with both cases, the reduction first guesses an index $j^* \in \{0, \dots, q\}$. We interpret the guess $j^* = 0$ as the second case, i.e. the challenge identity id^* will not be the sibling of an identity for which the adversary makes a key query. All other guesses are interpreted as the challenge identity will be the sibling of the identity of the j^* -th key query. Therefore, if $j^* = 0$, the reduction will not use ek before the adversary provides the challenge identity and then set $\text{ek}_{\text{id}^*} = \text{ek}$. In the second case the reduction will use ek as the leaf key of the sibling of the identity of the j^* -th key query. Once the adversary provides the challenge identity id^* , the reduction first checks if its guess was correct. If not, it aborts and outputs a random bit. Otherwise, if its guess turns out to be correct, which happens with probability at least $1/q$, it forwards the challenge messages of \mathcal{A} to the IND-CPA experiment, uses the challenge ciphertext in its own experiment and outputs whatever the adversary outputs. Note that the adversary \mathcal{A} never queries for sk_{id^*} . Therefore, it is never provided the value dk_{id^*} .

It follows routinely that the advantage of the reduction is at least $1/q$ times the advantage of the adversary \mathcal{A} . This allows us to conclude that $\mathcal{H}_n \stackrel{c}{\approx} \mathcal{H}_{n+1}$.

Finally, note that the hybrid \mathcal{H}_{n+1} is information theoretically independent of the plaintext message m .

6.3 Proof of Lemma 6.1

The proof follows by a sequence of sub-hybrids $\mathcal{H}_{\tau,0}$ to $\mathcal{H}_{\tau,6}$ where $\mathcal{H}_{\tau,0}$ is same as $\mathcal{H}_{\tau-1}$ and $\mathcal{H}_{\tau,6}$ is same as \mathcal{H}_τ .

- $\mathcal{H}_{\tau,0}$: This hybrid is same as $\mathcal{H}_{\tau-1}$.
- $\mathcal{H}_{\tau,1}$: Skip this hybrid if $\tau = n$. Otherwise, this hybrid is identical to $\mathcal{H}_{\tau,0}$, except that we change how the values \mathbf{h}_v and r_v for $v \in \{0, 1\}^\tau$ (if needed to answer a **KeyGen** query of the adversary) are generated.

Recall that in hybrid $\mathcal{H}_{\tau,0}$, \mathbf{h}_v is generated as $H(k_\tau, 0^{2\lambda}; \omega_v)$ and then

$$r_v := \begin{cases} H^{-1}(k_\tau, (0^{2\lambda}, \omega_v), \mathbf{h}_{v\|0} \parallel \mathbf{h}_{v\|1}) & \text{if } \tau < n - 1 \\ H^{-1}(k_\tau, (0^{2\lambda}, \omega_v), \text{ek}_{v\|0} \parallel \text{ek}_{v\|1}) & \text{otherwise} \end{cases}.$$

In this hybrid, we generate r_v first as being chosen uniformly. Next,

$$\mathbf{h}_v := \begin{cases} H(k_\tau, \mathbf{h}_{v\|0} \parallel \mathbf{h}_{v\|1}; r_v) & \text{if } \tau < n - 1 \\ H(k_\tau, \text{ek}_{v\|0} \parallel \text{ek}_{v\|1}; r_v) & \text{otherwise} \end{cases}.$$

Statistical indistinguishability of hybrids $\mathcal{H}_{\tau,0}$ and $\mathcal{H}_{\tau,1}$ follows from the trapdoor collision and uniformity properties of the chameleon encryption scheme.

- $\mathcal{H}_{\tau,2}$: We start with the case when $\tau < n$. For this case, in this hybrid, we change how the garbled circuit \tilde{P}^τ is generated. Let $\mathbf{v} = \text{id}^*[1 \dots \tau]$ and recall that

$$\text{lk}_{\mathbf{v}} = \begin{cases} (\mathbf{h}_{\mathbf{v}}, \text{ek}_{\mathbf{v}||0}, \mathbf{h}_{\mathbf{v}||1}, r_{\mathbf{v}}) & \text{if } \tau < n - 1 \\ (\mathbf{h}_{\mathbf{v}}, \text{ek}_{\mathbf{v}||0}, \text{ek}_{\mathbf{v}||1}, r_{\mathbf{v}}) & \text{if } \tau = n - 1 \end{cases}.$$

In this hybrid, we change the generation process of the garbled circuit \tilde{P}^τ from

$$(\tilde{P}^\tau, \overline{\text{lab}}') \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{P}[\text{id}[\tau + 1], k_\tau, \overline{\text{lab}}])$$

and setting $\overline{\text{lab}} := \overline{\text{lab}}'$ to

$$\tilde{P}^i, \{\text{lab}'_{j, \mathbf{h}_{\mathbf{v}, j}}\}_{j \in [\lambda]} := \text{Sim}(1^\lambda, \{\text{Enc}(k_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}})$$

and set $\overline{\text{lab}} := \{\text{lab}'_{j, \mathbf{h}_{\mathbf{v}, j}}, \text{lab}'_{j, \mathbf{h}_{\mathbf{v}, j}}\}_{j \in [\lambda]}$.

For the case when $\tau = n$, then we change computation of \tilde{T} from

$$(\tilde{T}, \overline{\text{lab}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{T}[\mathbf{m}])$$

where $\overline{\text{lab}} = \{\text{lab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$ to setting $y = \text{ek}_{\text{id}^*}$ and generating garbled circuit as,

$$(\tilde{T}, \{\text{lab}_{j,y_j}\}_{j \in [\lambda]}) \stackrel{\$}{\leftarrow} \text{Sim}(1^\lambda, \text{E}(y, \mathbf{m}))$$

and setting $\overline{\text{lab}} := \{\text{lab}_{j,y_j}, \text{lab}_{j,y_j}\}_{j \in [\lambda]}$.

For the case when $\tau < n$, computational indistinguishability of hybrids $\mathcal{H}_{\tau,1}$ and $\mathcal{H}_{\tau,2}$ follows by the security of the garbling scheme and the fact that $\{\text{Enc}(k_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$ is exactly the output of the circuit $\text{P}[\text{id}[\tau + 1], k_\tau, \overline{\text{lab}}]$ on input $\mathbf{h}_{\mathbf{v}}$. On the other hand, for the case when $\tau = n$, then again indistinguishability of hybrids $\mathcal{H}_{n,1}$ and $\mathcal{H}_{n,2}$ follows by the security of the garbling scheme and the fact that $\text{E}(\text{ek}_{\text{id}^*}, \mathbf{m})$ is the output of the circuit $\text{T}[\mathbf{m}]$ on input ek_{id^*} .

- $\mathcal{H}_{\tau,3}$: Skip this hybrid if $\tau = n$. This hybrid is identical to $\mathcal{H}_{\tau,2}$, except that using $\mathbf{v} := \text{id}[1 \dots \tau]$ we change

$$\tilde{P}^i, \{\text{lab}'_{j, \mathbf{h}_{\mathbf{v}, j}}\}_{j \in [\lambda]} := \text{Sim}(1^\lambda, \{\text{Enc}(k_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{lab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}})$$

to

$$\tilde{P}^i, \{\text{lab}'_{j, \mathbf{h}_{\mathbf{v}, j}}\}_{j \in [\lambda]} := \text{Sim}(1^\lambda, \{\text{Enc}(k_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{lab}_{j, \mathbf{h}_{\text{id}[1 \dots \tau + 1], j}})\}_{j \in [\lambda], b \in \{0,1\}})$$

Notice that $\mathbf{t}_{\mathbf{v}}$ is not used in this experiment. Therefore computational indistinguishability of hybrids $\mathcal{H}_{\tau,2}$ and $\mathcal{H}_{\tau,3}$ follows by λ^2 invocations (one invocation for each bit of the λ labels) of the security of the chameleon encryption scheme. We now provide the reduction for one change below.

We will now outline a reduction to the security of the chameleon hash function. Specifically, the challenger provides a hash key k^* and the reduction needs to submit x^*, r^* . Recall that q is an upper bound for the number of queries by the adversary (including key and challenge queries). The reduction first guesses an index $j^* \in \{0, \dots, q\}$, such that the node v^* on level τ of the j^* -th query is also on the root-to-leaf path of the challenge identity. We interpret $j^* = 0$ as the challenge identity not sharing a prefix of length τ with any keys queried by the adversary.

The reduction now sets $k_\tau := k^*$ and submits $x^* := h_{v^*||0} || h_{v^*||1}$ and randomly chosen coins $r_{v^*} := r^*$ to the experiment once the label $h_{v^*} := H(k_\tau, x^*; r^*)$ for the node v^* is needed in its simulation.

Once the adversary announces the challenge identity id^* , the reduction checks if v^* is on the root-to-leaf path of id^* , if not it aborts and outputs a random bit. Clearly, it holds that v^* is on the root-to-leaf path of id^* with probability $1/q$. Now we can use the attacker's ability to distinguish the encryptions of the provided labels to break the security of the chameleon encryption scheme, incurring a polynomial loss of $1/q$.

Remark: We note that the ciphertexts hardwired inside the garbled circuit only provide the labels $\{\text{lab}_{j, h_{id[1 \dots \tau+1], j}}\}_{j \in [\lambda]}$ (in an information theoretical sense).

- $\mathcal{H}_{\tau,4}$: Skip this hybrid if $\tau = n$. In this hybrid, we undo the change made in going from hybrid $\mathcal{H}_{\tau,0}$ to hybrid $\mathcal{H}_{\tau,1}$, i.e. we go back to generating all h_v values using `NodeGen` and `LeafGen`.

Computational indistinguishability of hybrids $\mathcal{H}_{\tau,3}$ and $\mathcal{H}_{\tau,4}$ follows from the trapdoor collision and uniformity properties of the chameleon encryption scheme. Observe that the hybrid $\mathcal{H}_{\tau,4}$ is the same as hybrid \mathcal{H}_τ .

7 Construction of Hierarchical Identity-Based Encryption

In this section, we describe our construction of HIBE from chameleon encryption. Let $(\text{Gen}, H, H^{-1}, \text{Enc}, \text{Dec})$ be a chameleon encryption scheme and (G, E, D) be any semantically secure public-key encryption scheme. We let $id[i]$ denote the i^{th} -bit of id and $id[1 \dots i]$ denote the first i bits of id (and $id[1 \dots 0] = \varepsilon$).

Notation for the pseudorandom function F . Let $\text{PRG} : \{0, 1\}^\lambda \rightarrow \{0, 1\}^{3\lambda}$ be a length tripling pseudorandom generator and $\text{PRG}_0, \text{PRG}_1$ and PRG_2 be the $1 \dots \lambda, \lambda + 1 \dots 2\lambda$ and $2\lambda + 1 \dots 3\lambda$ bits of the output of PRG , respectively. Now define a GGM-type [GGM84] pseudo-random function $F : \{0, 1\}^\lambda \times \{0, 1, 2\}^* \rightarrow \{0, 1\}^\lambda$ such that $F(s, x) := \text{PRG}_{x_n}(\text{PRG}_{x_{n-1}}(\dots(\text{PRG}_{x_1}(s))\dots))$, where $n = |x|$ and for each $i \in [n]$ x_i is the i^{th} element (from 0, 1 or 2) of string x .²⁰

NodeGen and NodeGen' functions. As explained in the introduction, we need an exponential sized tree of local-keys. The function `NodeGen` provides efficient access to *local-keys* corresponding to any node in this (exponential sized) tree. We will use this function repeatedly in our construction.

²⁰ $F(s, \varepsilon)$ is set to output s .

The function takes as input the hash key k_G (a key of the chameleon hash function from $2\ell + 2\lambda$ bits to λ bits, where ℓ is specified later), a node $v \in \{0, 1\}^* \cup \{\varepsilon\}$ (ε denotes the empty string), and $s = (s_1, s_2, s_3)$ seeds for the pseudo-random function PRF. This function is explained in the Figure 5.

NodeGen($k_G, v, (s_1, s_2, s_3)$):

1. Obtain ω_1, ω_2 , and ω_3 be the first, second and third $\lambda/3$ bits of s_1 , respectively.
2. Generate $(k_v, t_v) := \text{Gen}(1^\lambda; \omega_1)$ and $h_v := H(k_v, 0^\lambda; \omega_2)$.
3. Analogous to the previous two steps generate $k_{v\parallel 0}, h_{v\parallel 0}$ using seed s_2 and $k_{v\parallel 1}, h_{v\parallel 1}$ using seed s_3 .
4. Sample r'_v and generate $(ek_{v\parallel 0}, dk_{v\parallel 0}) \xleftarrow{\$} G(1^\lambda)$ and $(ek_{v\parallel 1}, dk_{v\parallel 1}) \xleftarrow{\$} G(1^\lambda)$ using ω_3 as random coins.
5. $h'_v := H(k_G, k_{v\parallel 0} \parallel h_{v\parallel 0} \parallel k_{v\parallel 1} \parallel h_{v\parallel 1} \parallel ek_{v\parallel 0} \parallel ek_{v\parallel 1}; r'_v)$.
6. $r_v := H^{-1}(t_v, (0^\lambda, \omega_2), h'_v)$.
7. $lk_v := (k_v, h_v, r_v, h'_v, r'_v, k_{v\parallel 0}, h_{v\parallel 0}, k_{v\parallel 1}, h_{v\parallel 1}, ek_{v\parallel 0}, ek_{v\parallel 1})$.
8. Output lk_v

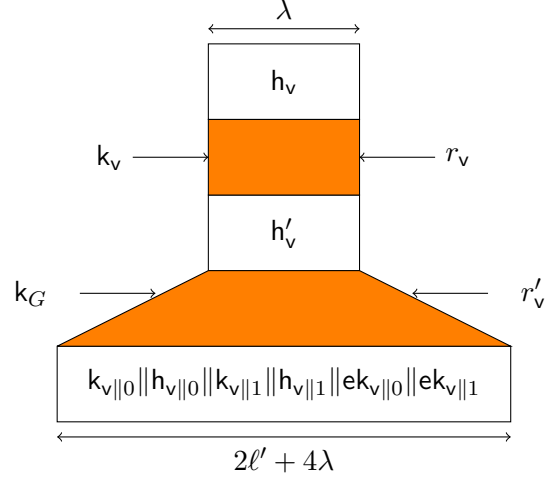


Figure 5: Explanation on how **NodeGen** works. Strings ω_1, ω_2 and ω_3 are used as randomness for cryptographic functions and can be sufficiently expanded using a PRG.

We also define a function **NodeGen'**, which is identical to **NodeGen** except that it additionally takes a bit β as input and outputs $dk_{v\parallel\beta}$. More formally, **NodeGen'**($k_G, v, (s_1, s_2, s_3), \beta$) executes just like **NodeGen** but in Step 8 it outputs $dk_{v\parallel\beta}$.

Construction. We describe our HIBE scheme (**Setup**, **KeyGen**, **Encrypt**, **Decrypt**).

- **Setup**(1^λ): Proceed as follows:

1. Sample $s \xleftarrow{\$} \{0, 1\}^\lambda$ (seeds for the pseudorandom function PRF).
2. Setup a global hash function $(k_G, \cdot) := \text{Gen}(1^\lambda, 2\ell + 2\lambda)$ ²¹ where $\ell = \ell' + \lambda$ and ℓ' is the length of k generated from **Gen**($1^\lambda, \lambda$).
3. Obtain $(k_\varepsilon, h_\varepsilon, r_\varepsilon, h'_\varepsilon, r'_\varepsilon, k_0, h_0, k_1, h_1) := \text{NodeGen}(k_G, \varepsilon, s)$
4. Output (mpk, msk) where $\text{mpk} := (k_G, k_\varepsilon, h_\varepsilon)$ and $\text{msk} = \text{sk}_\varepsilon := (\varepsilon, \emptyset, s, \perp)$

- **KeyGen**($\text{sk}_{\text{id}} = (\text{id}, \{lk_v\}_{v \in V}, s, dk_{\text{id}}), \text{id}' \in \{0, 1\}^*$):²²

²¹The trapdoor for the global hash function is not needed in the construction or the proof and is therefore dropped.

²²HIBE is often defined to have separate **KeyGen** and **Delegate** algorithms. For simplicity, we describe our scheme with just one **KeyGen** algorithm that enables both the tasks of decryption and delegation. Secret-keys without delegation capabilities can be obtained by dropping the third entry (the PRG seed) from sk_{id} .

Let $n := |\text{id}'|$ and set $V' := \{\text{id} \parallel \text{id}'[1 \dots j - 1]\}_{j \in [n]}$
For all $\mathbf{v} \in V'$:
 $\text{lk}_{\mathbf{v}} := \text{NodeGen}(k_G, \mathbf{v}, (\text{F}(s, \mathbf{v} \parallel 2), \text{F}(s, \mathbf{v} \parallel 0 \parallel 2), \text{F}(s, \mathbf{v} \parallel 1 \parallel 2)))$
Let $\mathbf{v} := \text{id} \parallel \text{id}'[1 \dots n - 1]$
 $\text{dk}_{\text{id} \parallel \text{id}'} := \text{NodeGen}'(k_G, \mathbf{v}, (\text{F}(s, \mathbf{v} \parallel 2), \text{F}(s, \mathbf{v} \parallel 0 \parallel 2), \text{F}(s, \mathbf{v} \parallel 1 \parallel 2)), \text{id}'[n])$
Output $\text{sk}_{\text{id} \parallel \text{id}'} := (\text{id}, \{\text{lk}_{\mathbf{v}}\}_{\mathbf{v} \in V \cup V'}, \text{F}(s, \text{id}'), \text{dk}_{\text{id} \parallel \text{id}'})$

Remark: We note that in our construction the secret key for any identity is unique regardless of many iterations of KeyGen operations were performed to obtain it.

- **Encrypt**($\text{mpk} = (k_G, k_\varepsilon, h_\varepsilon), \text{id} \in \{0, 1\}^n, \mathbf{m}$): Before describing the encryption procedure we describe four circuits that will be garbled during the encryption process.
 - $\text{T}[\mathbf{m}](\text{ek})$: Compute and output $\text{E}(\text{ek}, \mathbf{m})$.
 - $\text{Q}_{\text{last}}[\beta \in \{0, 1\}, k_G, \overline{\text{tlab}}](\mathbf{h})$: Compute and output $\{\text{Enc}(k_G, (\mathbf{h}, j + \beta \cdot \lambda + 2\ell, b), \text{tlab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$, where $\overline{\text{tlab}}$ is short for $\{\text{tlab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$.
 - $\text{Q}[\beta \in \{0, 1\}, k_G, \overline{\text{plab}}](\mathbf{h})$: Compute and output $\{\text{Enc}(k_G, (\mathbf{h}, j + \beta \cdot \ell, b), \text{plab}_{j,b})\}_{j \in [\ell], b \in \{0,1\}}$, where $\overline{\text{plab}}$ is short for $\{\text{plab}_{j,b}\}_{j \in [\ell], b \in \{0,1\}}$.
 - $\text{P}[\overline{\text{qlab}}](k, \mathbf{h})$: Compute and output $\{\text{Enc}(k, (\mathbf{h}, j, b), \text{qlab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$, where $\overline{\text{qlab}}$ is short for $\{\text{qlab}_{j,b}\}_{j \in [\lambda], b \in \{0,1\}}$.

Encryption proceeds as follows:

1. Compute \tilde{T} as:

$$(\tilde{T}, \overline{\text{tlab}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{Q}_{\text{out}}[k_G, \mathbf{m}])$$

2. For $i = n, \dots, 1$ generate

- (a) If $i = n$ then

$$(\tilde{Q}^n, \overline{\text{qlab}}^n) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{Q}_{\text{last}}[\text{id}[n], k_G, \overline{\text{tlab}}]),$$

else

$$(\tilde{Q}^i, \overline{\text{qlab}}^i) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{Q}[\text{id}[i], k_G, \overline{\text{plab}}^{i+1}]).$$

- (b) $(\tilde{P}^i, \overline{\text{plab}}^i) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{P}[\overline{\text{qlab}}^i]).$

3. Set $x_\varepsilon := k_\varepsilon \parallel h_\varepsilon$.

4. Output $\text{ct} := (\{\text{plab}_{j,x_{\varepsilon,j}}^1\}_{j \in [\ell]}, \{\tilde{P}^i, \tilde{Q}^i\}_{i \in [n]}, \tilde{T})$ where $x_{\varepsilon,j}$ is the j^{th} bit of x_ε .

- **Decrypt**($\text{ct}, \text{sk}_{\text{id}} = (\text{id}, \{\text{lk}_{\mathbf{v}}\}_{\mathbf{v} \in V}), s, \text{dk}_{\text{id}}$): Decryption proceeds as follows:

1. Parse ct as $(\{\text{plab}_{j,x_{\varepsilon,j}}^1\}_{j \in [\ell]}, \{\tilde{P}^i, \tilde{Q}^i\}_{i \in [n]}, \tilde{T})$ where $x_\varepsilon := k_\varepsilon \parallel h_\varepsilon$ and $x_{\varepsilon,j}$ is its j^{th} bit.
2. Parse $\text{lk}_{\mathbf{v}}$ as $(h_{\mathbf{v}}, r_{\mathbf{v}}, h'_{\mathbf{v}}, r'_{\mathbf{v}}, k_{\mathbf{v} \parallel 0}, h_{\mathbf{v} \parallel 0}, k_{\mathbf{v} \parallel 1}, h_{\mathbf{v} \parallel 1}, \text{ek}_{\mathbf{v} \parallel 0}, \text{ek}_{\mathbf{v} \parallel 1})$ for each $\mathbf{v} \in V$. (Recall $V = \{\text{id}[1 \dots j - 1]\}_{j \in [n]}$.)
3. For each $i \in [n]$, proceed as follows:

- (a) Set $v := \text{id}[1 \dots i - 1]$, $x_v := k_v \| h_v$, $y_v := h'_v$, and if $i < n$ then set $z_v := k_v \| \text{id}[i] \| h_v \| \text{id}[i]$ else set $z_v := \text{ek}_{\text{id}}$.²³
- (b) $\{e_{j,b}^i\}_{j \in [\lambda], b \in \{0,1\}} := \text{Eval}(\tilde{P}^i, \{\text{plab}_{j,x_v,j}^i\}_{j \in [\ell]})$.
- (c) For each $j \in [\lambda]$, compute $\text{qlab}_{j,y_v,j}^i := \text{Dec}(k_v, e_{j,y_v,j}^i, (h'_v, r_v))$.
- (d) If $i < n$ then,

$$\{f_{j,b}^i\}_{j \in [\ell], b \in \{0,1\}} := \text{Eval}(\tilde{Q}^i, \{\text{qlab}_{j,y_v,j}^i\})$$

and for each $j \in [\ell]$

$$\text{plab}_{j,z_v,j}^{i+1} := \text{Dec}(k_G, f_{j,z_v,j}^i, (k_v \| 0 \| h_v \| 0 \| k_v \| 1 \| h_v \| 1 \| \text{ek}_{v \| 0} \| \text{ek}_{v \| 1}, r'_v))$$

- (e) else,

$$\{g_{j,b}\}_{j \in [\lambda], b \in \{0,1\}} := \text{Eval}(\tilde{Q}^n, \{\text{qlab}_{j,y_v,j}^n\})$$

and for each $j \in [\lambda]$

$$\text{tlab}_{j,z_v,j} := \text{Dec}(k_G, g_{j,z_v,j}, (k_v \| 0 \| h_v \| 0 \| k_v \| 1 \| h_v \| 1 \| \text{ek}_{v \| 0} \| \text{ek}_{v \| 1}, r'_v)).$$

4. Output $D(\text{dk}_{\text{id}}, \text{Eval}(\tilde{T}, \{\text{tlab}_{j,\text{ek}_{\text{id}},j}\}_{j \in [\lambda]}))$.

7.1 Proof of Correctness

For any identity id , let $V = \{\text{id}[1 \dots j - 1]\}_{j \in [n]}$ be the set of nodes on the root-to-leaf path corresponding to identity id . Then the secret key sk_{id} consists of $\{k_v\}_{v \in V}$, dk_{id} and a seed of the pseudorandom function F . $\{k_v\}_{v \in V}$, dk_{id} and will be used for decryption and s is used for delegating keys. Note that by construction (and the trapdoor collision property of the chameleon encryption scheme for the first equation below) for all nodes $v \in V$ we have that:

$$\begin{aligned} H(k_G, k_v \| 0 \| h_v \| 0 \| k_v \| 1 \| h_v \| 1 \| \text{ek}_{v \| 0} \| \text{ek}_{v \| 1}; r'_v) &= h'_v, \\ H(k_v, h'_v; r_v) &= h_v. \end{aligned}$$

By correctness of garbled circuits, we have that the evaluation of \tilde{P}^1 yields correctly formed ciphertexts $f_{j,b}^1$. Next, by correctness of Dec of the chameleon encryption scheme we have that the decrypted values $\text{qlab}_{j,y_{\varepsilon,j}}^1$ are the correct input labels for the next garbled circuit \tilde{Q}^1 . Following the same argument we can argue that the decryption of ciphertexts generated by \tilde{Q}^1 yields the correct input labels for \tilde{P}^2 . Repeatedly applying this argument allows us to conclude that the last garbled circuit \tilde{Q}^n outputs correct encryptions of input labels of \tilde{T} . The decryption of appropriate ciphertexts among these and the execution of the garbled circuit \tilde{T} using the obtained labels yields the ciphertext $E(\text{ek}_{\text{id}}, m)$ which can be decrypted using the decryption key dk_{id} . Correctness of the last steps depends on the correctness of the public-key encryption scheme.

Next, the correctness of delegation follows from the fact that that for every id and id'

$$\text{KeyGen}(\text{sk}_{\varepsilon}, \text{id} \| \text{id}') = \text{KeyGen}(\text{KeyGen}(\text{sk}_{\varepsilon}, \text{id}), \text{id}').$$

This fact follows directly from the the following property of the GGM PRF. Specifically, for every x we have that $F(s, \text{id} \| x) = F(F(s, \text{id}), x)$.

²³For $i < n$, z_v will become the x_v in next iteration.

7.2 Proof of Security

We are now ready to prove the selective security of the HIBE construction above. For the sake of contradiction we proceed by assuming that there exists an adversary \mathcal{A} such that $\Pr[\text{IND}_{\mathcal{A}}^{\text{HIBE}}(1^\lambda) = 1] \geq \frac{1}{2} + \epsilon$ for a non-negligible ϵ (in λ), where $\text{IND}_{\mathcal{A}}^{\text{HIBE}}$ is shown in Figure 2. Assume further that q is a polynomial upper bound for the running-time of \mathcal{A} , and thus also an upper bound for the number of \mathcal{A} 's key queries. Security follows by a sequence of hybrids. In our hybrids, changes are made in how the secret key queries of the adversary \mathcal{A} are answered and how the challenge ciphertext is generated. However, unlike the IBE case these changes are not intertwined with each other. In particular, we will make changes to the secret keys first and then the ciphertext. We describe our hybrids next. Our proof consist of a sequence of hybrids $\mathcal{H}_{-3}, \mathcal{H}_{-2}, \mathcal{H}_{-1}, \mathcal{H}_0, \mathcal{H}_1, \dots, \mathcal{H}_{n+2}$. We describe these below. Since we are in the selective the case the adversary declares the challenge identity id^* before the public parameters mpk are provided to it. Also, we let V^* be the set $\{\varepsilon, \text{id}^*[1] \dots \text{id}^*[1 \dots n - 1]\}$.

- \mathcal{H}_{-3} : This hybrid corresponds to the experiment $\text{IND}_{\mathcal{A}}^{\text{HIBE}}$ as shown in Figure 2.
- \mathcal{H}_{-2} : In this hybrid, we change how the seed s of generated in Step 1 of Setup is used. Specifically, we sample $s \xleftarrow{\$} \{0, 1\}^\lambda$ and generate
 1. For each $i \in [n]$, let $a_i := \text{F}(s, \text{id}^*[1 \dots i - 1] || (1 - \text{id}^*[i]))$.
 2. $b := \text{F}(s, \text{id}^*)$.
 3. For each $i \in \{0 \dots n - 1\}$, let $c_i := \text{F}(s, \text{id}^*[1 \dots i] || 2)$.

Now, through out the execution of the experiment we replace the use of s with the values $(\{a_i\}, b, \{c_i\})$. First, observe that (by standard properties of the GGM pseudorandom function) given these values we can generate $\text{F}(s, v || 2)$ for all $v \in \{0, 1\}^* \cup \{\varepsilon\}$. Also, note that for the execution of the functions NodeGen and $\text{NodeGen}'$ only $\text{F}(s, v || 2)$ needs to be generated. Therefore, all executions of NodeGen and $\text{NodeGen}'$ remain unaffected.

Secondly, note that the \mathcal{A} is only allowed to make KeyGen queries for identities $\text{id} \notin V^* \cup \{\text{id}^*\}$. Therefore, in order to answer these queries the experiment needs to generate $\text{F}(s, v)$ for $v \notin V^* \cup \{\text{id}^*\}$. Observe that using $(\{a_i\}, b)$ by standard properties of the GGM pseudorandom function the experiment can compute $\text{F}(s, v)$ for any $v \notin V^*$. Therefore, all of \mathcal{A} 's KeyGen queries can be answered.²⁴

The hybrids \mathcal{H}_{-3} and \mathcal{H}_{-2} are the same distribution and the only change we have made is syntactic.

- \mathcal{H}_{-1} : In this hybrids, we change how each c_i is generated. In particular, we sample each c_i uniformly and independently instead of using F .

The indistinguishability between hybrids \mathcal{H}_{-2} and \mathcal{H}_{-1} follows based on the pseudorandomness of the pseudorandom function F .

- \mathcal{H}_0 : In this hybrid, we change how NodeGen and $\text{NodeGen}'$ behave when computed with an input $v \in V^*$.²⁵ For all $v \notin V^*$ the behavior of NodeGen and $\text{NodeGen}'$ remains unchanged.

²⁴The experiment can provide $\text{F}(s, \text{id}^*)$ even though it does not appear in any of the \mathcal{A} 's secret key queries. The reason is that $\text{F}(s, \text{id}^*)$ allows the capabilities of delegation but not decryption for ciphertexts to identity id^* .

²⁵Observe that these are specifically the cases in which one or two of the values s_1, s_2 and s_3 given as input to NodeGen and $\text{NodeGen}'$ depend on the $\{c_i\}$ values.

At a high level, the goal is to change the generating of $\{\text{lk}_v\}_{v \in V^*}$ such that the trapdoor values $\text{t}_v \in V^*$ are unused and so that the encryption key ek_{id^*} is sampled independent of everything else. The execution of `NodeGen` and `NodeGen'` for every $v \notin V^*$ remain unaffected. In particular, at `Setup` time we proceed as follows and fix the values $\{\text{lk}_v\}_{v \in V^*}$ and $\{\text{dk}_{v\|0}, \text{dk}_{v\|1}\}_{v \in V^*}$.²⁶

1. For every $v \in V^*$:
 - (a) Generate $(\text{k}_v, \text{t}_v) \xleftarrow{\$} \text{Gen}(1^\lambda)$.
 - (b) Generate $(\text{ek}_{v\|0}, \text{dk}_{v\|0}) \xleftarrow{\$} \text{G}(1^\lambda)$ and $(\text{ek}_{v\|1}, \text{dk}_{v\|1}) \xleftarrow{\$} \text{G}(1^\lambda)$.
 - (c) Sample r'_v, r_v .
2. Let $S^* := \{\text{id}^*[1 \dots i - 1] \parallel (1 - \text{id}^*[i])\}_{i \in [n]} \cup \{\text{id}^*\}$. (Note that $S^* \cap V^* = \emptyset$.)
3. For all $v \in S^*$ set k_v, h_v as first two outputs of `NodeGen`($\text{k}_G, v, (\text{F}(s, v\|2), \text{F}(s, v\|0\|2), \text{F}(s, v\|1\|2))$).
4. For each $i \in \{n - 1 \dots 0\}$:
 - (a) Set $v := \text{id}^*[1 \dots i]$
 - (b) Generate $\text{h}'_v := \text{H}(\text{k}_G, \text{k}_{v\|0} \parallel \text{h}_{v\|0} \parallel \text{k}_{v\|1} \parallel \text{h}_{v\|1} \parallel \text{ek}_{v\|0} \parallel \text{ek}_{v\|1}; r'_v)$.
 - (c) $\text{h}_v := \text{H}(\text{k}_v, \text{h}'_v; r_v)$.
 - (d) $\text{lk}_v := (\text{k}_v, \text{h}_v, r_v, \text{h}'_v, r'_v, \text{k}_{v\|0}, \text{h}_{v\|0}, \text{k}_{v\|1}, \text{h}_{v\|1}, \text{ek}_{v\|0}, \text{ek}_{v\|1})$.
5. Output $\{\text{lk}_v\}_{v \in V^*}$ and $\{\text{dk}_{v\|0}, \text{dk}_{v\|1}\}_{v \in V^*}$.

Statistical indistinguishability of hybrids $\mathcal{H}_{\tau, -1}$ and $\mathcal{H}_{\tau, 0}$ follows from the trapdoor collision and uniformity properties of the chameleon encryption scheme. Note that in this hybrid the trapdoor t_v for any node $v \in V^*$ is no longer being used.

- \mathcal{H}_τ for $\tau \in \{1 \dots n\}$: This hybrid is identical to \mathcal{H}_0 except we change how the ciphertext is generated. Recall that the challenge ciphertext consists of a sequence of $2n + 1$ garbled circuits. In hybrid \mathcal{H}_τ , we generate the first 2τ of these garbled circuits (namely, $\tilde{P}^1, \tilde{Q}^1 \dots \tilde{P}^\tau, \tilde{Q}^\tau$) using the simulator provided by the garbled circuit construction. The outputs hard-coded in the simulated circuits are set to be consistent with the output that would have resulted from the execution of honestly generated garbled circuits using keys obtained from invocations of `NodeGen`. More formally, for the challenge identity id^* the challenge ciphertext is generated as follows (modifications with respect to honest ciphertext generation have been highlighted in red):

1. Compute \tilde{T} as:

$$(\tilde{T}, \overline{\text{tlab}}) \xleftarrow{\$} \text{GCircuit}(1^\lambda, \text{Q}_{out}[\text{k}_G, \text{m}])$$

2. For $i = n, \dots, \tau + 1$ generate

- (a) If $i = n$ then

$$(\tilde{Q}^n, \overline{\text{qlab}}^n) \xleftarrow{\$} \text{GCircuit}(1^\lambda, \text{Q}_{last}[\text{id}[n], \text{k}_G, \overline{\text{tlab}}]),$$

else

$$(\tilde{Q}^i, \overline{\text{qlab}}^i) \xleftarrow{\$} \text{GCircuit}(1^\lambda, \text{Q}[\text{id}[i], \text{k}_G, \overline{\text{plab}}^{i+1}]).$$

²⁶Note that since the adversary never makes a `KeyGen` query for an identity id that is a prefix of id^* . Therefore, we have that dk_v for $v \in V^* \cup \{\text{id}^*\}$ will not be provided to \mathcal{A} .

(b) $(\tilde{P}^i, \overline{\text{plab}}^i) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, P[\overline{\text{qlab}}^i])$.

3. For $i = \tau, \dots, 1$:

(a) Set $\mathbf{v} = \text{id}^*[1 \dots i - 1]$, $x_{\mathbf{v}} := \mathbf{k}_{\mathbf{v}} \parallel \mathbf{h}_{\mathbf{v}}$, $y_{\mathbf{v}} := \mathbf{h}'_{\mathbf{v}}$, and if $i < n$ then $z_{\mathbf{v}} := \mathbf{k}_{\mathbf{v} \parallel \text{id}^*[i]} \parallel \mathbf{h}_{\mathbf{v} \parallel \text{id}^*[i]}$ else $z_{\mathbf{v}} := \mathbf{ek}_{\text{id}^*}$.

(b) If $i = n$ then

$$(\tilde{Q}^n, \{\text{qlab}_{j, y_{\mathbf{v}, j}}^n\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_G, (\mathbf{h}'_{\mathbf{v}}, j + \text{id}^*[n] \cdot \lambda + 2\ell, b), \text{tlab}_{j, z_{\mathbf{v}, j}})\}_{j \in [\lambda], b \in \{0, 1\}})$$

else

$$(\tilde{Q}^i, \{\text{qlab}_{j, y_{\mathbf{v}, j}}^i\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_G, (\mathbf{h}'_{\mathbf{v}}, j + \text{id}^*[i] \cdot \ell, b), \text{plab}_{j, z_{\mathbf{v}, j}}^{i+1})\}_{j \in [\lambda], b \in \{0, 1\}}).$$

(c) $\overline{\text{qlab}}^i := \{\text{qlab}_{j, y_{\mathbf{v}, j}}^i, \text{qlab}_{j, y_{\mathbf{v}, j}}^i\}_{j \in [\lambda]}$.

(d) $(\tilde{P}^i, \{\text{plab}_{j, x_{\mathbf{v}, j}}^i\}_{j \in [\ell]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{qlab}_{j, y_{\mathbf{v}, j}}^i)\}_{j \in [\lambda], b \in \{0, 1\}})$.

(e) $\overline{\text{plab}}^i := \{\text{plab}_{j, x_{\mathbf{v}, j}}^i, \text{plab}_{j, x_{\mathbf{v}, j}}^i\}_{j \in [\ell]}$.

4. Set $x_\varepsilon := \mathbf{k}_\varepsilon \parallel \mathbf{h}_\varepsilon$.

5. Output $\text{ct} := (\{\text{plab}_{j, x_{\varepsilon, j}}^1\}_{j \in [\lambda]}, \{\tilde{P}^i, \tilde{Q}^i\}_{i \in [n]}, \tilde{T})$ where $x_{\varepsilon, j}$ is the j^{th} bit of x_ε .

The computational indistinguishability between hybrids $\mathcal{H}_{\tau-1}$ and \mathcal{H}_τ is based on Lemma 7.1 which is proved in Section 7.3.

Lemma 7.1. *For each $\tau \in \{1 \dots n\}$ it is the case that $\mathcal{H}_{\tau-1} \stackrel{c}{\approx} \mathcal{H}_\tau$.*

- \mathcal{H}_{n+1} : This hybrid is same as hybrid \mathcal{H}_n except that we generate the garbled circuit \tilde{T} to using the garbling simulator. More specifically, instead of generating \tilde{T} as

$$(\tilde{T}, \overline{\text{tlab}}) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, \text{Q}_{out}[\mathbf{k}_G, \mathbf{m}])$$

we set $y = \mathbf{ek}_{\text{id}^*}$ and generate garbled circuit as,

$$(\tilde{T}, \{\text{lab}_{j, y_j}\}_{j \in [\lambda]}) \stackrel{\$}{\leftarrow} \text{Sim}(1^\lambda, \text{E}(y, \mathbf{m}))$$

and set $\overline{\text{lab}} := \{\text{lab}_{j, y_j}, \text{lab}_{j, y_j}\}_{j \in [\lambda]}$.

Computational indistinguishability between hybrids \mathcal{H}_n and \mathcal{H}_{n+1} follows directly from the security of the gabled circuits.

- \mathcal{H}_{n+2} : This hybrid is same as \mathcal{H}_n except that we change the ciphertext $\text{E}(\mathbf{ek}_{\text{id}^*}, \mathbf{m})$ hardwired in the simulated garbling of the circuit T to be $\text{E}(\mathbf{ek}_{\text{id}^*}, 0)$.

Note that the adversary \mathcal{A} never queries for $\mathbf{sk}_{\text{id}^*}$. Therefore, it is never provided the value $\mathbf{dk}_{\text{id}^*}$. Therefore, we can use an adversary distinguishing between \mathcal{H}_{n+1} and \mathcal{H}_{n+2} to construct an attacker against the semantic security of the public-key encryption scheme (G, E, D) . This allows us to conclude that $\mathcal{H}_{n+1} \stackrel{c}{\approx} \mathcal{H}_{n+2}$.

Finally, note that the hybrid \mathcal{H}_{n+2} is information theoretically independent of the plaintext message \mathbf{m} .

7.3 Proof of Lemma 7.1

The proof follows by a sequence of sub-hybrids $\mathcal{H}_{\tau,0}$ to $\mathcal{H}_{\tau,4}$ where $\mathcal{H}_{\tau,0}$ is same as $\mathcal{H}_{\tau-1}$ and $\mathcal{H}_{\tau,4}$ is same as \mathcal{H}_{τ} .

- $\mathcal{H}_{\tau,0}$: This hybrid is same as $\mathcal{H}_{\tau-1}$.
- $\mathcal{H}_{\tau,1}$: In this hybrid, we change how the garbled circuit \tilde{P}^τ is generated. Let $\mathbf{v} = \text{id}^*[1 \dots \tau - 1]$ and $\text{lk}_{\mathbf{v}} = (\mathbf{k}_{\mathbf{v}}, \mathbf{h}_{\mathbf{v}}, r_{\mathbf{v}}, \mathbf{h}'_{\mathbf{v}}, r'_{\mathbf{v}}, \mathbf{k}_{\mathbf{v}||0}, \mathbf{h}_{\mathbf{v}||0}, \mathbf{k}_{\mathbf{v}||1}, \mathbf{h}_{\mathbf{v}||1}, \mathbf{ek}_{\mathbf{v}||0}, \mathbf{ek}_{\mathbf{v}||1})$ and define $x_{\mathbf{v}} := \mathbf{k}_{\mathbf{v}} || \mathbf{h}_{\mathbf{v}}$. The change we make is the following. We generate

$$(\tilde{P}^\tau, \overline{\text{plab}}^\tau) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, P[\overline{\text{qlab}}^\tau])$$

now as

$$(\tilde{P}^\tau, \{\text{plab}_{j,x_{\mathbf{v}},j}^\tau\}_{j \in [\ell]}) \stackrel{\$}{\leftarrow} \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{qlab}_{j,b}^\tau)\}_{j \in [\lambda], b \in \{0,1\}})$$

where $x_{\mathbf{v},j}$ is the j^{th} bit of $x_{\mathbf{v}}$. Next, we set $\overline{\text{plab}}^i := \{\text{plab}_{j,x_{\mathbf{v}},j}^i, \text{plab}_{j,y_{\mathbf{v}},j}^i\}_{j \in [\ell]}$.

Computational indistinguishability of hybrids $\mathcal{H}_{\tau,0}$ and $\mathcal{H}_{\tau,1}$ follows by the security of the garbling scheme GCircuit and the fact that $\{\text{Enc}(\mathbf{k}_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{qlab}_{j,b}^\tau)\}_{j \in [\lambda], b \in \{0,1\}}$ is exactly the output of the circuit $P[\overline{\text{qlab}}^\tau]$ on input $x_{\mathbf{v}}$.

- $\mathcal{H}_{\tau,2}$: This hybrid is identical to $\mathcal{H}_{\tau,1}$, except that for $\mathbf{v} = \text{id}^*[1 \dots \tau - 1]$ we change

$$(\tilde{P}^\tau, \{\text{plab}_{j,x_{\mathbf{v}},j}^\tau\}_{j \in [\ell]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{qlab}_{j,b}^\tau)\}_{j \in [\lambda], b \in \{0,1\}})$$

to

$$(\tilde{P}^\tau, \{\text{plab}_{j,x_{\mathbf{v}},j}^\tau\}_{j \in [\ell]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_{\mathbf{v}}, (\mathbf{h}_{\mathbf{v}}, j, b), \text{qlab}_{j,y_{\mathbf{v}},j}^\tau)\}_{j \in [\lambda], b \in \{0,1\}}),$$

where $y_{\mathbf{v}} := \mathbf{h}'_{\mathbf{v}}$.

Notice that node \mathbf{v} is generated so that the trapdoor value $\mathbf{t}_{\mathbf{v}}$ is not used in the execution of the experiment. Therefore, computational indistinguishability of hybrids $\mathcal{H}_{\tau,1}$ and $\mathcal{H}_{\tau,2}$ follows by λ^2 invocations (one invocation for each bit of the λ labels) of the security of the chameleon encryption scheme. The reduction is analogous to the reduction proving indistinguishability of hybrids $\mathcal{H}_{\tau,2}$ and $\mathcal{H}_{\tau,3}$ in the proof of Lemma 6.1.

Remark: We note that the ciphertexts hardwired inside the garbled circuit only provide the labels $\{\text{qlab}_{j,y_{\mathbf{v}},j}^\tau\}_{j \in [\lambda]}$ (in an information theoretical sense).

- $\mathcal{H}_{\tau,3}$ This hybrid is identical to $\mathcal{H}_{\tau,2}$, except that for $\mathbf{v} = \text{id}^*[1 \dots \tau - 1]$ we change how \tilde{Q}^τ is generated. If $\tau = n$ then

$$(\tilde{Q}^n, \overline{\text{qlab}}^n) \stackrel{\$}{\leftarrow} \text{GCircuit}(1^\lambda, Q_{\text{last}}[\text{id}^*[n], \mathbf{k}_G, \overline{\text{tlab}}]),$$

is changed to

$$(\tilde{Q}^n, \{\text{qlab}_{j,y_{\mathbf{v}},j}^n\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(\mathbf{k}_G, (\mathbf{h}'_{\mathbf{v}}, j + \text{id}^*[n] \cdot \lambda + 2\ell, b), \text{tlab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}),$$

and $\overline{\text{qlab}}^n := \{\text{qlab}_{j,y_v,j}^n, \text{qlab}_{j,y_v,j}^n\}_{j \in [\lambda]}$ where $y_v := h'_v$. Otherwise, if $\tau \neq n$ then

$$(\tilde{Q}^\tau, \overline{\text{qlab}}^\tau) \stackrel{\S}{\leftarrow} \text{GCircuit}(1^\lambda, \text{Q}[\text{id}^*[\tau], k_G, \overline{\text{plab}}^{\tau+1}])$$

is changed to

$$(\tilde{Q}^\tau, \{\text{qlab}_{j,y_v,j}^\tau\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(k_G, (h'_v, j + \text{id}^*[\tau] \cdot \ell, b), \text{plab}_{j,b}^{\tau+1})\}_{j \in [\ell], b \in \{0,1\}}),$$

and $\overline{\text{qlab}}^\tau := \{\text{qlab}_{j,y_v,j}^\tau, \text{qlab}_{j,y_v,j}^\tau\}_{j \in [\lambda]}$ where $y_v := h'_v$.

Computational indistinguishability between hybrids $\mathcal{H}_{\tau,2}$ and $\mathcal{H}_{\tau,3}$ follows by the security of the garbling scheme and the fact that is the output of the circuit $\text{Q}_{\text{last}}[\text{id}^*[n], k_G, \overline{\text{tlab}}]$ is $\{\text{Enc}(k_G, (h'_v, j + \text{id}^*[n] \cdot \lambda + 2\ell, b), \text{tlab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}}$ and the output of the circuit $\text{Q}[\text{id}^*[\tau], k_G, \overline{\text{plab}}^{\tau+1}]$ is $\{\text{Enc}(k_G, (h'_v, j + \text{id}^*[\tau] \cdot \ell, b), \text{plab}_{j,b}^{\tau+1})\}_{j \in [\ell], b \in \{0,1\}}$.

- $\mathcal{H}_{\tau,4}$: This hybrid is identical to $\mathcal{H}_{\tau,4}$, except that we change generation of \tilde{Q}^τ . Specifically, in the case $\tau = n$ then we change the generation process of \tilde{Q}^n from

$$(\tilde{Q}^n, \{\text{qlab}_{j,y_v,j}^n\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(k_G, (h'_v, j + \text{id}^*[n] \cdot \lambda + 2\ell, b), \text{tlab}_{j,b})\}_{j \in [\lambda], b \in \{0,1\}})$$

to

$$(\tilde{Q}^n, \{\text{qlab}_{j,y_v,j}^n\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(k_G, (h'_v, j + \text{id}^*[n] \cdot \lambda + 2\ell, b), \text{tlab}_{j,z_v,j})\}_{j \in [\lambda], b \in \{0,1\}}),$$

where $z_v := \text{ek}_{\text{id}^*}$. On the other hand, when $\tau \neq n$ then it is changed from

$$(\tilde{Q}^\tau, \{\text{qlab}_{j,y_v,j}^\tau\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(k_G, (h'_v, j + \text{id}^*[\tau] \cdot \ell, b), \text{plab}_{j,b}^{\tau+1})\}_{j \in [\ell], b \in \{0,1\}})$$

to

$$(\tilde{Q}^\tau, \{\text{qlab}_{j,y_v,j}^\tau\}_{j \in [\lambda]}) := \text{Sim}(1^\lambda, \{\text{Enc}(k_G, (h'_v, j + \text{id}^*[\tau] \cdot \ell, b), \text{plab}_{j,z_v,j}^{\tau+1})\}_{j \in [\ell], b \in \{0,1\}}),$$

where $z_v := h_{v \parallel \text{id}^*[\tau]} \parallel k_{v \parallel \text{id}^*[\tau]}$.

Notice that since the trapdoor for k_G is unavailable (never generated or used), computational indistinguishability of hybrids $\mathcal{H}_{\tau,3}$ and $\mathcal{H}_{\tau,4}$ follows by λ^2 invocations (one invocation per bit of the λ labels) if $\tau = n$ and by $\ell\lambda$ invocations (one invocation per bit of the ℓ labels) otherwise of the security of the chameleon encryption scheme. And the reduction to the security of the chameleon encryption scheme is analogous to the reduction described for indistinguishability between hybrids $\mathcal{H}_{\tau,1}$ and $\mathcal{H}_{\tau,2}$.

Observe that the hybrid $\mathcal{H}_{\tau,4}$ is the same as hybrid \mathcal{H}_τ .

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