

COMPARATIVE STUDY OF ZETA AND SINGLE STAGE HIGH VOLTAGE GAIN DC-DC CONVERTERS

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ABSTRACT

Solar energy will be the important source of energy for the future. Solar cells transform energy from an essentially unlimited source sun. PV system virtually zero running cost energy is the input source of power. It is environment friendly. The DC-DC converters are used to increase the efficiency of PV system. This paper envisages the comparison of the two topologies of two converters; Zeta and high voltage gain DC-DC Converter.

Keywords: Zeta Converter, Dickson Charge Pump, high voltage gain DC-DC Converter

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1. INTRODUCTION

Renewable energy often provides generation of electricity, transport fuels, air cooling and water heating. It provides electric generation world-wide. Solar and wind energy provides the significant share of electricity. [1-2] The deployment of renewable energy efficiency and resources due to climate change results in renewable energy products in large scale, which is suited to remote and rural areas, and developing countries, where energy is often crucial in human development. Due to higher efficiency and reliability at an improved power quality, DC distribution systems was better than the AC distribution systems. [3] They offer a simpler integration of renewable energy and energy storage systems. Currently, telecom centres, data centres, commercial buildings, residential buildings, and micro grids are among the emerging examples of dc distribution systems. The Zeta converter provides a positive output voltage above or below the output voltage. This converter needs two inductor and capacitor. This will regulate the voltage. [4]

A single stage high-voltage-gain dc–dc converter is proposed which resembles Dickson charge-pump based voltage multiplier on output side and a two-phase interleaved boost converter on its input side. This converter offers continuous input current, which makes it more appealing for the integration of renewable sources like solar panels. This converter is capable of drawing power from either a single source or two independent sources. Depending upon the output voltage stages can be added. Furthermore, the VM used offers low voltage ratings for capacitors that potentially leads to size reduction. [5]

2. ZETA CONVERTER

The circuit of ZETA converter is shown below. It has Capacitors C_C and C_{OUT} , inductors L_1 and L_2 , MOSFET acting as switch and a diode. Consider the Zeta converter acting in continuous conduction mode. The modes of operation is given for Q is ON and Q is OFF.[6-9]

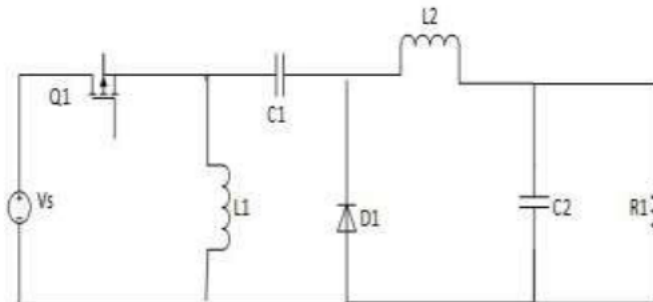


Figure 1 :Zeta Converter

Mode 1

When Q ON; Capacitor C_C charged to V_{OUT} and it is connected in series with L_2 , so the voltage across L_1 is V_{IN} and Diode D sees V_{IN} and V_{OUT} .

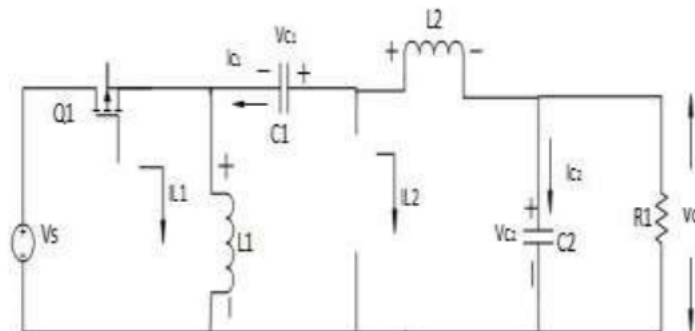


Figure 2 Mode 1 of Zeta Converter

$$L_1 * \frac{DiL_1}{dt} = V_s \quad \dots \quad (1)$$

$$Di \frac{L_2}{dt} = \frac{V_s}{L_2} + \frac{VC_1}{L_2} - \frac{VC_2}{L_2} \quad \dots \quad (2)$$

$$C_2 * \frac{DV_{C2}}{dt} = i_{L1} \quad \dots \quad (3)$$

Mode 2

When Q OFF; the voltage across L1 must be V_{OUT} , the voltage across Q is $V_{IN} + V_{OUT}$, therefore the voltage across L1 is $-V_{OUT}$ relative to the drain of Q1.

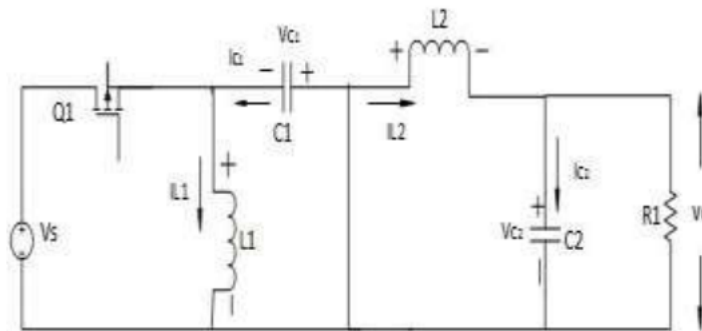


Figure 3 Mode 1 of Zeta Converter

By kirchoff's voltage law, voltage across inductor (L_1) is given by,

$$L_1 \frac{di_{L1}}{dt} = -V_1 \quad \dots \quad (4)$$

Voltage across inductor (L_2) is given by,

$$L_2 \frac{di_{L2}}{dt} = -V_{L2} \quad \dots \quad (5)$$

By applying kirchoff's current law, current through the capacitor C_1 is,

$$i_{L1} = C_1 * \frac{dV_{C1}}{dt} \quad \dots \quad (6)$$

$$D = \frac{V_o}{V_o + V_s} \quad \dots \quad (7)$$

By volt second balance,

$$V_s * T_{ON} + (V_s - V_{C1}) * T_{OFF} = 0 \quad \dots \quad (9)$$

3. BASIC DICKSON CHARGE PUMP DESIGN

The basic configuration of a Dickson charge pump is shown in Figure. It consists of a large number of identical stages, each containing a diode and a capacitor, where the bottom plates of the capacitors in consecutive stages. Charge is transferred from one capacitor yielding an output voltage that can be much higher than the input voltage. In a real application, a feedback control loop is added to the circuit in order to maintain the output voltage very stable and independent of system parameters or load characteristics. ^[10]

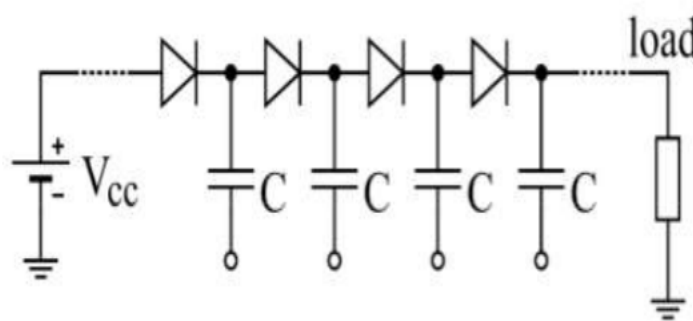


Figure 4 Dickson Charge Pump

4. HIGH VOLTAGE GAIN DC-DC CONVERTER

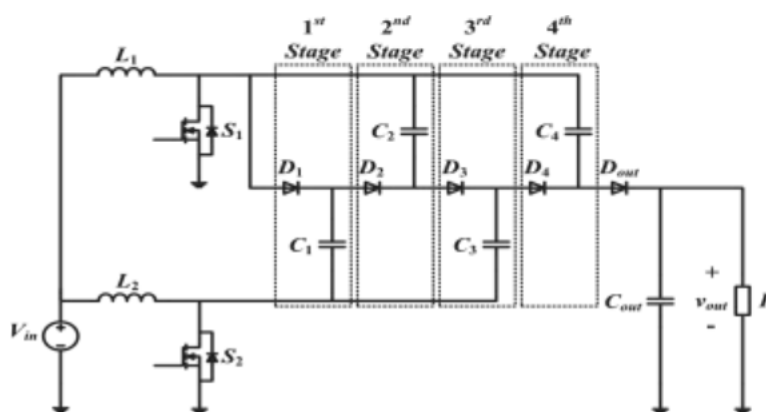


Figure 5 High Voltage gain DC-DC Converter

The energy storage cell – based converters offers low voltage . The switched capacitor based network converter has a discontinuous input ripple due to series – parallel connection of the inductors. [11]

High-voltage-gain dc–dc converters using coupled inductors have been proposed for the integration of solar panels to 100-V dc using single stage. For higher voltage, the stages can be added.

The converter makes use of Voltage multiplier cells derived from Dickson Charge pump. The voltage ratings of each capacitor is twice that of its previous VM cell. [12-14] Also, the inductors (L1,L2) and switches (S1,S2) experience different current stresses whenever even number of VM cells are used.

5. SIMULATION RESULTS

5.1. Zeta converter



Figure 6 Simulation model of Zeta Converter

The zeta converter with a single input is shown in figure 6. The output obtained from the rectifier is boosted using the Zeta Converter whose parameters are calculated using formulas for zeta converter.

Table 1 Zeta converter design

ATTRIBUTES	VALUES
INPUT VOLTAGE(V_s)	20 V
OUTPUT VOLTAGE(V_o)	59 V
SWITCHING FREQUENCY (F_s)	100 KHz
DUTY RATIO (D)	75%
INDUCTOR $L_1=L_2$	60mH
INPUT CAPACITOR	1 μ F
OUTPUT CAPACITOR	2.5 μ F

The table 1 shows the input, output switching frequency, duty ratio, inductors and capacitor values designed for the zeta converter.

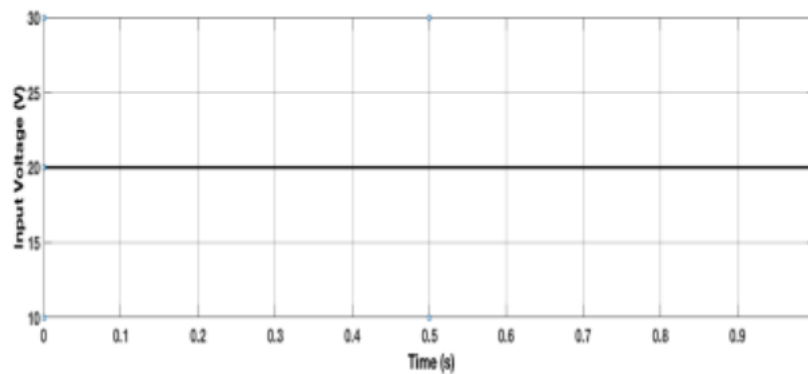


Figure 7 Input Voltage of Zeta Converter

The figure 7 shows the input voltage of 20V DC given to the Zeta converter.

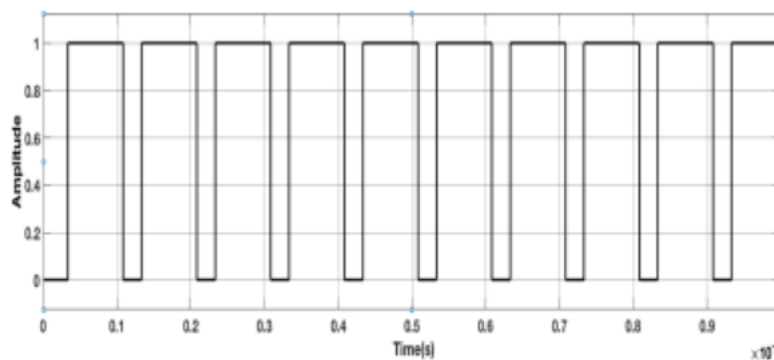


Figure 8 Pulses of Zeta Converter

The figure 8 shows the pulse given to the Zeta converter with switching frequency of 100KHz and 75% of duty ratio.

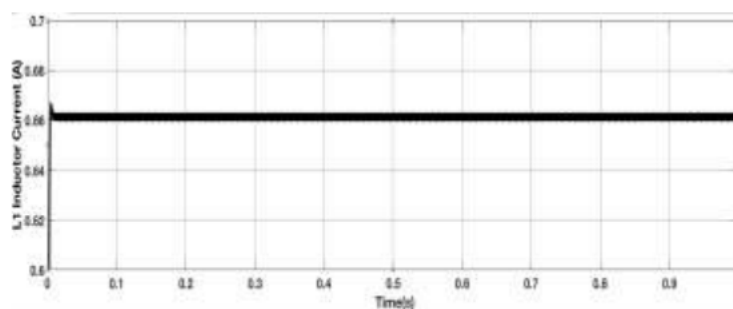


Figure 9 Inductor current L1

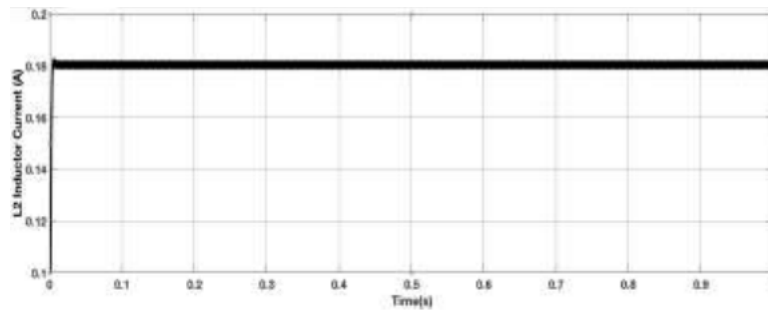


Figure 10 Inductor Current L2

The figure 9 and figure 10 shows the inductor current of the Zeta converter

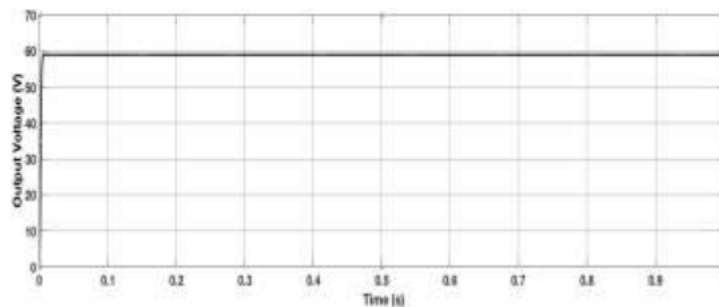


Figure 11 Output Voltage of Zeta Converter

The figure 11 show the output of the zeta converter which is boosted to 60 V from 20V input.

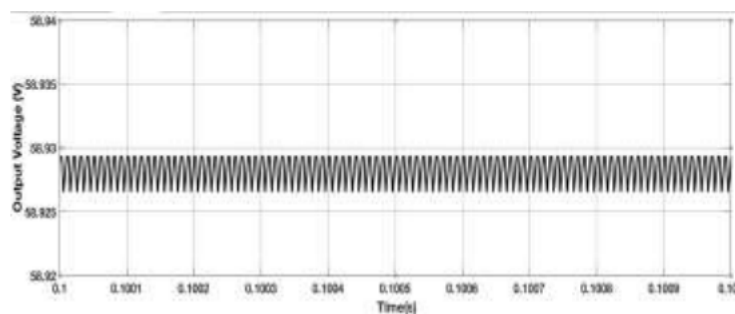


Figure 12:Output ripple of ZETA converter

The figure 12 shows the output voltage ripple which is around 0.01 V.

The input source is connected to zeta converter. The inductor currents for both are similar. The pulse width for the zeta converter is given at 75% switching duty cycle with switching frequency 100 KHz input 20V and the output voltage of 60V was obtained. The output voltage ripple was 0.01V.

5.2. Single stage high voltage DC-DC converter

The high voltage DC-DC converter can be supplied from two inputs as well as using only one input source. When a single input is used for the proposed converter, switches S_1 and S_2 have the same switching duty cycle d and are 180° out of phase from each other.[10-14] The proposed converter with single source is shown in figure13.

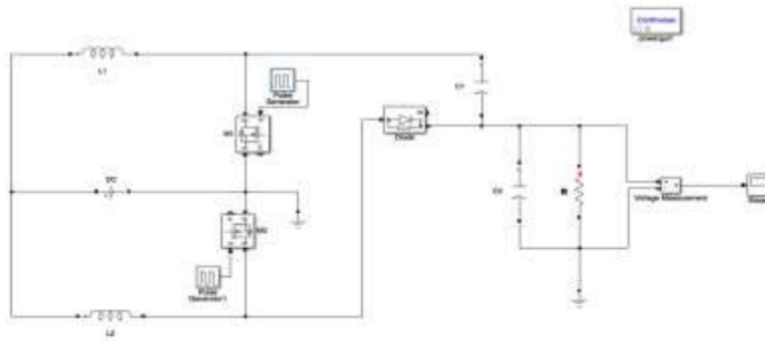


Figure 13 Simulation model of single stage High voltage gain converter

Table 2 High Voltage DC-DC Converter Design

ATTRIBUTES	VALUES
INPUT VOLTAGE(V_s)	20 V
OUTPUT VOLTAGE(V_o)	80 V
SWITCHING FREQUENCY (F_s)	100 KHz
DUTY RATIO (D)	75%
INDUCTOR L1=L2	100mH
INPUT CAPACITOR	60 μ F
OUTPUT CAPACITOR	60 μ F

The table 2 shows the input, output switching frequency, duty ratio, inductors and capacitor values designed for the high voltage DC-DC converter.

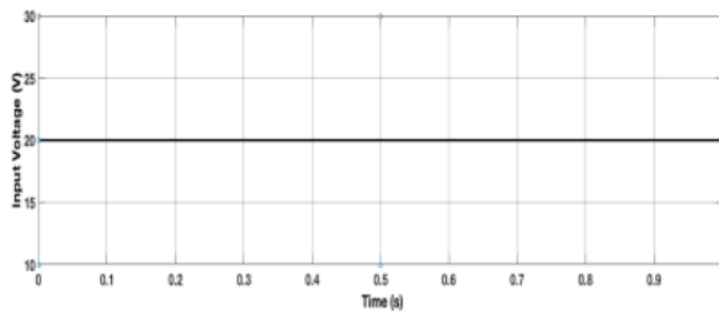


Figure 14 Input Voltage of Single stage High voltage gain converter

The figure 10 shows the input voltage of single stage high voltage gain converter which is 20V DC.

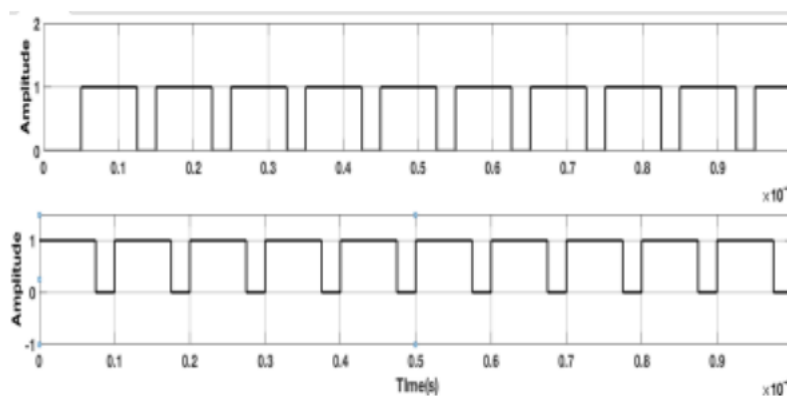


Figure 15 Pulses of Single stage High voltage gain converter

The pulses of the switches in the proposed converter are shown in figure 15.

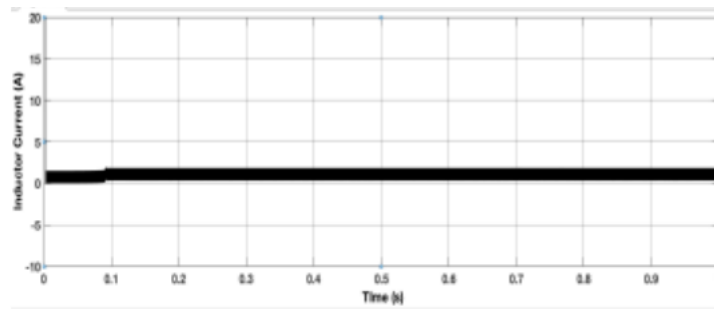


Figure 16 Inductor Current of Single stage High voltage gain converter

The figure 16 shows the inductor current of the Single stage High voltage gain converter

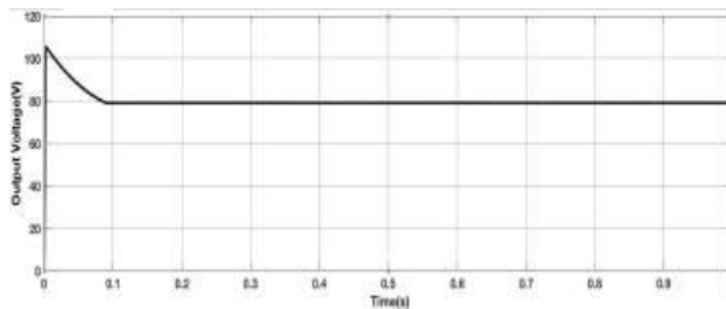


Figure 17 Output Voltage of Single stage High voltage gain converter

The figure 17 shows the output voltage of 80V which is boosted from the input of 20V.

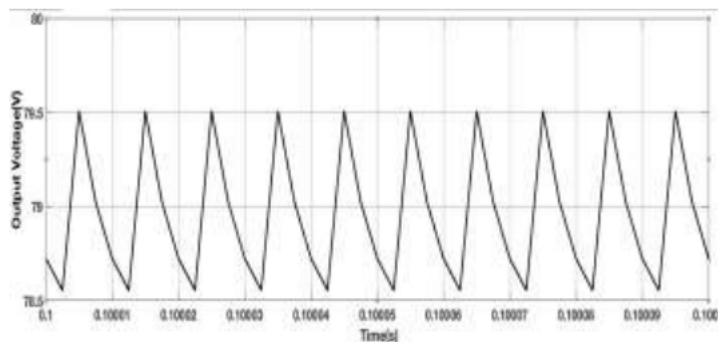


Figure 18 Output ripple of Single stage High voltage gain converter

The output voltage ripple of 0.08V was shown in the figure18.

A high-voltage-gain dc–dc converter step up a 20 V input to 80 V output. This converter is based on a two-phase interleaved boost and the modified Dickson charge pump VM circuit. This interleaved structure reduces the input current ripple and distributes the current through each component. It can draw power from a single source as well as from two independent sources while offering continuous input current in both cases, making the converter well suited for renewable applications like solar. The converter is small in size and symmetric, i.e., the semiconductor components experience an efficient high step-up conversion through the voltage multiplier module and current stresses.

6. HARDWARE RESULT

6.1. Zeta Converter

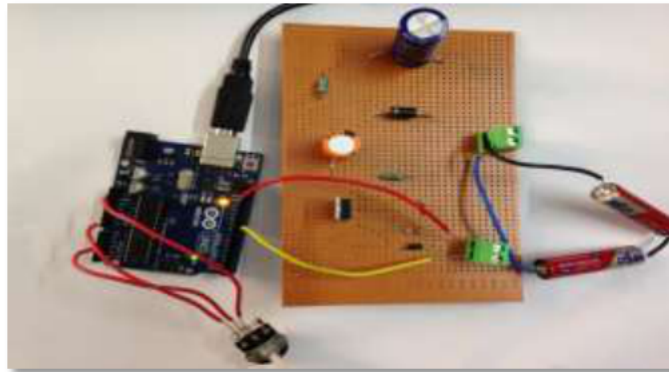


Figure 19 Zeta Converter

The figure 19 shows the zeta converter hardware. The IRF540N is used for zeta converter an advanced HEXFET N-channel power MOSFET, from International Rectifier. The device is extremely versatile with its performance capabilities and thus becomes ideal for numerous electronic applications.

The switching pulse to the MOSFET was provided by an Arduino board. Since the switching frequency required by our circuit was high, we preferred Arduino programming to a manual switch. The value of our Inductance was calculated to be 2.2mH. The output capacitor 220 μ F/160volts Electrolytic Non-Polarized Crossover Capacitor was used. The input capacitor 47 μ F/60volts Electrolytic Non-Polarized Crossover Capacitor was used. In the prototype, the 3 volts is indicated by a 3 volt battery that is connected to the zeta converter. The output voltage of 30V was obtained

6.2. Single stage high voltage DC-DC converter



Figure 20 High Voltage DC-DC Converter

The IRF540N is used for high voltage DC-DC converter an advanced HEXFET N-channel power MOSFET, from International Rectifier. The device is extremely versatile with its performance capabilities and thus becomes ideal for numerous electronic applications. The switching pulse to the MOSFET was provided by PIC16F877A. It perfectly fits many uses, from automotive industries and controlling home appliances to industrial instruments, remote sensors, electrical door locks and safety devices. It is also ideal for smart cards as well as for battery supplied devices because of its low consumption. Multiplier cells of the Dickson's charge pump are fed from the drain of MOSFET1 and source of MOSFET2. This Dickson's

charge pump consists of 4 sets of multiplier cells which have parallel connected diodes and capacitors. Diode IN4007 with a resistor[1Kohm] and a capacitor connected in series with the boosted output from the Dickson’s charge pump to reduce the noise. Which are further connected to the inverter circuit. Monolithic gate driver circuits [FAN7392] are used which are fed by the buffer circuit. In the prototype 20V input is connected to the high voltage DC-DC converter. The output voltage of 80V was obtained. Driver circuit needs 12V and 5V. Microcontroller need 5V supply, so 230V AC supply is first step down in to 15V by using step down transformer. Then this 15V AC is converted in to DC by using Full bridge rectifier which has high efficiency than all other methods. This 15V DC is converting into 12V DC and 5v DC by using 7812 and 7805 regulator respectively. The capacitor is used to provide smooth variation in voltage.

7. CONCLUSION

In this paper was presented a comparative study of DC-DC converters in Zeta and single stage high voltage topologies. The operation equations of main parameters were presented. The simulation results of both the converters are presented here. The input voltage is boosted in both the converters. The analysis of Zeta and single stage high voltage DC-DC converter is performed using the given design values for inductance and capacitance. The ripple free output voltage is obtained. The output obtained is ripple free with fewer losses. The steady state is obtained very early during the operation which increases the efficiency of this model. The Zeta converter thus provides a substantial boost of the input voltage which increases the overall efficiency of operation of this circuit. The high-voltage-gain dc–dc converter is based on a two-phase interleaved boost and the modified Dickson charge pump VM circuit, which reduces the input current ripple and distributes the current through each component. The input, output and output voltage ripple values of the both the converters were given in the table 3.

Table 3 Comparison Values

Attributes	Zeta Converter	Single Stage High Voltage DC-DC Converter
Input Voltage (V_s)	20V	20 V
Output Voltage (V_o)	59V	79.5 V
Output Ripple Voltage (ΔV_o)	0.01V	0.08V

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