

# Introduction to the Issue on Hybrid Analog–Digital Signal Processing for Hardware-Efficient Large Scale Antenna Arrays (Part II)

**H**YBRID analog-digital (HAD) processing provides a key technology for the coming generations of wireless networks, as a means of obtaining hardware-efficient transceivers. The principle behind HAD is that the transceiver processing is divided into the analog and digital domain, where networks of analog components implement large-dimensional processing at the transceiver front end, allowing for a low-dimensional digital processing which necessitates only a few RF chains. This technology has recently been brought at the forefront of research motivated by the proliferation of millimeter-wave (mmWave) communications, as a solution to circumvent the use of large numbers of expensive mmWave RF components. Its scope however is not limited solely to mmWave, as hardware-efficient transmission is key for small cell deployments in the microwave frequencies and also in emerging applications such as the internet of things (IoT) involving massive connectivity. All these applications still rely on transceivers capable of beamforming, using cheap, low-power, and physically small devices.

Accordingly, the aim of this Special Issue (SI) has been to gather the relevant contributions focusing on the practical challenges of hybrid analog-digital transmission. We received a total of 59 submissions of particularly high quality, spanning a broad range of topics. After a strict review process, we decided to accept 23 papers, which will be published in two issues. The previous first issue contained 12 papers which provided significant solutions with an explicit mmWave component. This second issue contains 11 papers, covering a wide selection of topics as follows.

In the first paper of this issue, X. Jiang *et al.* focus on the issue of channel state information at the transmitter (CSIT) in time division duplex (TDD) systems with hybrid beamforming. They propose a reciprocity calibration scheme that allows to acquire full CSIT. Different to existing CSIT acquisition methods, the approach studied circumvents beam training or selection and acquires CSIT without any assumption on the channel. They illustrate that, by use of full CSIT, TDD hybrid beamforming systems can fully release their beamforming potential.

Liu *et al.* investigate a two-timescale hybrid precoding (THP) where the RF beamformer is set according to the second order statistics of the channel. The authors use stochastic successive convex approximation to design an online algorithm for THP

design. The advantage of the proposed method is that it requires outdated perfect CSI samples it is more robust to signalling latency. The proposed solution is applicable to different THP optimization problems such as throughput maximization, fairness and power minimization.

Abdelaziz *et al.* study the problem of digital predistortion (DPD) for hybrid MIMO transmitters. They propose a novel DPD approach per antenna sub-array that involves a combined signal from multiple power amplifier outputs and a decorrelation-based learning rule in order to minimize the non-linear distortions in the direction of the intended receiver. The technique requires only a single observation receiver and is shown to outperform conventional techniques that rely on a single power amplifier. It is also shown to be robust in terms of amplitude and phase imbalances, and to have low out-of-band emissions in all signal directions.

In the paper titled “Spectral Efficiency Optimization For mmWave Multiuser MIMO Systems”, the authors Qingjiang Shi and Mingyi Hong take an optimization approach and analyze the use of penalty dual decomposition method, together with the weighted minimum mean-square error approach, for solving the nonconvex hybrid beamformer optimization problem. The paper also treats the case where the phase shifter is low resolution. The results illustrate the theoretical performance of the optimized hybrid structure as compared to fully digital beamforming.

H. He *et al.* consider a HAD MIMO-OFDM receiver and address the strong non-linearity introduced by low-resolution ADCs. They provide a low-complexity Bayesian optimal detector. they also present an analytical framework to study the theoretical performance of the detector in the large-system limit, which can precisely evaluate the performance expressions such as mean-square error and symbol error rate. Based on this optimal detector, the potential of adding a few low-resolution RF chains and high-resolution ADCs for mixed-ADC architecture is investigated.

Roth *et al.* compare the spectral and energy efficiency of hybrid beamforming and digital beam forming with low resolution ADCs under the effects of channel estimation, transmitter impairments and a wideband multipath model. Their result shows that as the Signal to Noise Ratio (SNR) gets larger the ADC resolution achieving the optimal energy efficiency gets also larger. They also show that in the multiuser scenario digital beamforming is more energy efficient than hybrid beamforming.

Their results also demonstrate that if mixed ADC resolutions are used we can achieve any desired trade-off between power consumption and rate close to those achieved with only one ADC resolution.

Ding *et al.* consider multiuser massive MIMO channel estimation problem for a system that consists of both hybrid analog-to-digital beamformer and lower resolution ADCs. This structure benefits from low cost and power consumption, however, the hardware constraints impose sever design challenges. By exploiting the sparse nature of mmWave channels in the angular domain, the authors formulate the channel estimation problem in the form of sparse Bayesian learning (SBL) framework. Then, an efficient algorithm is proposed based on variational Bayesian (VB) method. Then, using data-aided channel estimation, they can remove the signalling overhead. The proposed methods can effectively overcome the reduced number of effective and coarser channel measurements due to the use of hybrid beamformer and low resolution ADCs.

In the paper titled “DFT-based Hybrid Beamforming Multiuser Systems: Rate Analysis and Beam Selection”, the authors Yu Han, Shi Jin, Jun Zhang, Jiayi Zhang, and Kai-Kit Wong propose an analog Discrete Fourier Transform (DFT) based beam selection algorithm for hybrid beamforming. The algorithm relies on an accurate approximation of the achievable uplink and downlink rates. The main contributions of the paper are an analog beam selection strategy and an achievable rate characterization based on the Rician channel statistics.

The work by Gmez-Tornero *et al.* treats the problem of direction-of-arrival (DoA) estimation by a combination of analog amplitude monopulse radar techniques and digital signal processing of conventional WiFi received signals. The reader architecture comprises two panel antennas that are tilted between them to form the monopulse tilted-array configuration required for the radar-type DoA estimation. A received signal strength indicator (RSSI)-based monopulse function is used for the DoA estimation in the digital domain. The DoA performance is tested over typical indoor scattering environments, whereas, by combining two readers, 2D localization is experimentally demonstrated in an outdoor environment.

C. Zhang *et al.* study the beam-based training and the transmission design design jointly for hybrid massive antenna single-user (SU) and multiple-user (MU) systems based on outage probability. They propose an interleaved training design to concatenate the feedback and training procedures for the SU case, thus making the training length adaptive to the channel realization. They also derive exact analytical expressions for the average training length and the outage probability of the proposed interleaved training. For MU systems, two solutions for the beam assignment are provided with different complexity-performance tradeoff. All proposed schemes are shown to provide significant savings in the training overhead.

In this paper Sohrabi *et al.* consider downlink of massive MIMO system with 1-bit resolution DACs. Firstly, a tight upper-bound for symbol error rate (SER) with low-resolution DACs

is derived. Then, in order to minimize the SER, the optimal QAM constellation range of QAM is calculated. Finally, a low-complexity heuristic one-bit symbol level precoder is proposed for both single-user and multiuser scenarios. It is shown that 1-bit precoding requires larger number of antennas to achieve the same performance compared to infinite resolution phase shifters.

This completes the second part of our two-part Special Issue. Our guest editorial team would like to thank the Authors of all published and unpublished papers who contributed their works towards this Special Issue. Our sincere gratitude also goes to our expert Reviewers for their timely and thorough reviews that have significantly improved the quality of the Special Issue.

#### C. MASOUROS, *Lead Guest Editor*

Department of Electronic and Electrical Engineering  
University College London  
London WC1E 6BT, U.K.  
e-mail: C.Masouros@ucl.ac.uk

#### M. SELLATHURAI, *Guest Editor*

School of Engineering and Physical Sciences  
Heriot-Watt University  
Edinburgh EH14 4AS, U.K.  
e-mail: m.sellathurai@hw.ac.uk

#### C. B. PAPADIAS, *Guest Editor*

Athens Information Technology  
Athens 15125, Greece  
e-mail: papadias@ait.edu.gr

#### L. DAI, *Guest Editor*

Department of Electronics Engineering  
Tsinghua University  
Beijing 100084, China  
e-mail: daill@tsinghua.edu.cn

#### W. YU, *Guest Editor*

Department of Electrical and Computer Engineering  
University of Toronto  
Toronto, ON M5S 3H7, Canada  
e-mail: weiyu@ece.utoronto.ca

#### T. SIZER, *Guest Editor*

Network Fabric Research  
Nokia Bell Labs  
Murray Hill, NJ 07974 USA  
e-mail: theodore.sizer@nokia-bell-labs.com



**Christos Masouros** (M'06–SM'14) is currently an Associate Professor with the Department of Electrical and Electronic Engineering, University College London, London, U.K. He has held a prestigious Royal Academy of Engineering Research Fellowship between 2011 and 2016. His research interests include in the field of wireless communications and signal processing. He was the recipient of the Best Paper Award in the IEEE GlobeCom 2015 conference, and has been recognized as an Exemplary Editor for the IEEE COMMUNICATIONS LETTERS. He is an Editor for the IEEE TRANSACTIONS ON COMMUNICATIONS, and an Associate Editor for the IEEE COMMUNICATIONS LETTERS.



**Mathini Sellathurai** is currently a Professor in signal processing for communications and intelligent systems with Heriot-Watt University, Edinburgh, U.K. She was the recipient of the IEEE Communication Society Fred W. Ellersick Best Paper Award in 2005, Industry Canada Public Service Awards in 2005, and Best Ph.D. thesis award (Silver Medal) from NSERC, Canada, in 2002. She is also a member of the IEEE SPCOM Technical Strategy Committee, an Editor for the IEEE TRANSACTIONS ON SIGNAL PROCESSING (since 2009) and the General Co-Chair for the IEEE SPAWC2016, Edinburgh, U.K.



**Constantinos Papadias** is the Dean of Athens Information Technology, Athens, Greece, where he is also a Professor and the Head of its Broadband Wireless and Sensor Networks Research Group. He is also an Adjunct Professor with Aalborg University, Aalborg, Denmark. He was the recipient of the Bell Labs Presidents Award in 2002, the IEEE Signal Processing Society's Young Author Best Paper Award in 2003, a Bell Labs Teamwork Award in 2004, his recognition as a Highly Cited Greek Scientist in 2011, two IEEE conference paper awards in 2013 and 2014, and a Best Booth Award at EUCNC, in 2016. He was a Distinguished Lecturer of the IEEE Communications Society for 2012–2013.



**Linglong Dai** is an Assistant Professor with the Department of Electronic Engineering, Tsinghua University, Beijing, China. He was the recipient of the Outstanding Ph.D. graduate of Tsinghua University Award in 2011, the Excellent Doctoral Dissertation of Beijing Award in 2012, the IEEE ICC Best Paper Awards in 2013 and 2014, the National Excellent Doctoral Dissertation Nomination Award in 2013, the URSI Young Scientist Award in 2014 and 2016, the IEEE Transactions on Broadcasting Best Paper Award in 2015, the IEEE RADIO Young Scientist Award in 2015, the WCSP Best Paper Award in 2016, the Exemplary Reviewer of IEEE Communications Letters in 2016, and the second prize of Science and Technology Award of China Institute of Communications, in 2016.



**Wei Yu** received the Ph.D. degree from Stanford University, Stanford, CA, USA, in 2002. He is a Professor and the Canada Research Chair in Information Theory and Wireless Communications with the University of Toronto, Toronto, ON, Canada. He is on the IEEE Information Theory Society Board of Governors and Chairs the Signal Processing for Communications and Networking Technical Committee of the IEEE Signal Processing Society. He was the recipient of the Steacie Memorial Fellowship, an IEEE Communications Society Best Tutorial Paper Award in 2015, and the IEEE Signal Processing Society Best Paper Award, in 2008 and in 2017. He is a Fellow of the Canadian Academy of Engineering, and is recognized as a Highly Cited Researcher.



**Theodore (Tod) Sizer** received the Graduate degree from Amherst College, Amherst, MA, USA, and the Master's and Doctorate degrees in optics from the Institute of Optics, University of Rochester, Rochester, NY, USA. He is the Vice-President of Network Fabric Research, Nokia Bell Labs, Murray Hill, NJ, USA, leading teams innovating in all aspects of IP and optical solutions for core, metro, submarine, and data center communications. Prior to his current role, he led Wireless Research, Nokia Bell Labs for many years driving the vision and research on 5G. He is the author of 54 U.S. patents. In 2012, he was the recipient of the Popular Science Breakthrough Innovation Award for the light Radio invention. He is a Fellow of Bell Labs and WWRF, and is a member of OSA.