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This index covers all technical items—papers, correspondence, reviews, etc.—that appeared in this periodical during 2021, and items from previous years that were commented upon or corrected in 2021. Departments and other items may also be covered if they have been judged to have archival value.

The Author Index contains the primary entry for each item, listed under the first author's name. The primary entry includes the coauthors' names, the title of the paper or other item, and its location, specified by the publication abbreviation, year, month, and inclusive pagination. The Subject Index contains entries describing the item under all appropriate subject headings, plus the first author's name, the publication abbreviation, month, and year, and inclusive pages. Note that the item title is found only under the primary entry in the Author Index.

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- Chen, L.**, *see* Zhang, B., *TCSI April 2021* 1532-1542
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- Chen, L.**, *see* Chen, L., *TCSI Nov. 2021* 4616-4625
- Chen, M.**, Xu, S., Huang, L., Sun, W., and Shi, L., A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique; *TCSI Feb. 2021* 950-962
- Chen, M.**, Wang, H., and Liu, X., Adaptive Practical Fixed-Time Tracking Control With Prescribed Boundary Constraints; *TCSI April 2021* 1716-1726
- Chen, M.**, *see* Bao, H., *TCSI Nov. 2021* 4534-4544
- Chen, P.**, Yu, L., and Zhang, D., Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults; *TCSI Feb. 2021* 797-807
- Chen, P.**, *see* Chen, W., *TCSI April 2021* 1493-1506
- Chen, P.**, *see* Zhang, F., *TCSI May 2021* 1855-1868
- Chen, P.**, *see* Hu, S., *TCSI June 2021* 2317-2328
- Chen, P.**, *see* Ye, L., *TCSI Dec. 2021* 4821-4834
- Chen, Q.**, *see* Tao, T., *TCSI May 2021* 1906-1916
- Chen, R.**, *see* Chen, W., *TCSI April 2021* 1493-1506
- Chen, W.**, Chen, R., Chen, P., and Hsiao, Y., A High-Performance Bidirectional Architecture for the Quasi-Comparison-Free Sorting Algorithm; *TCSI April 2021* 1493-1506
- Chen, W.**, *see* Wang, J., *TCSI May 2021* 2107-2120
- Chen, W.**, *see* Liu, X., *TCSI July 2021* 2998-3011
- Chen, W.**, Yao, Y., and Liu, S., A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD; *TCSI Nov. 2021* 4566-4575
- Chen, X.**, *see* Yin, N., *TCSI Jan. 2021* 311-321
- Chen, X.**, *see* Wang, C., *TCSI Jan. 2021* 387-395
- Chen, X.**, *see* Zhu, Q., *TCSI Sept. 2021* 3951-3964
- Chen, Y.**, *see* Liu, X., *TCSI Jan. 2021* 238-249
- Chen, Y.**, *see* Zhao, X., *TCSI Jan. 2021* 89-102
- Chen, Y.**, *see* Zhang, Q., *TCSI Feb. 2021* 842-855
- Chen, Y.**, *see* Cao, Y., *TCSI Feb. 2021* 641-654
- Chen, Y.**, *see* Yang, Z., *TCSI June 2021* 2307-2316
- Chen, Y.**, *see* Yin, H., *TCSI Sept. 2021* 3965-3974
- Chen, Y.**, *see* Shi, B., *TCSI Nov. 2021* 4746-4759
- Chen, Y.**, *see* Huang, T., *TCSI Nov. 2021* 4417-4418
- Chen, Y.**, *see* Wu, A., *TCSI Nov. 2021* 4508-4519
- Chen, Y.**, *see* Huang, T., *TCSI Dec. 2021* 4835-4836
- Chen, Z.**, Yu, X., Xu, W., and Wen, G., Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm; *TCSI Jan. 2021* 376-386
- Chen, Z.**, *see* Yin, H., *TCSI Sept. 2021* 3965-3974
- Chen, Z.**, *see* Zhao, D., *TCSI Oct. 2021* 3977-3990
- Chen, Z.**, *see* Zhao, D., *TCSI Oct. 2021* 4413
- Cheng, C.**, *see* Wang, C., *TCSI Oct. 2021* 4182-4193

- Cheng, J.**, Liang, L., Park, J.H., Yan, H., and Li, K., A Dynamic Event-Trigged Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities; *TCSI Dec. 2021* 4924-4934
- Cheng, K.**, *see* Song, J., *TCSI Aug. 2021* 3377-3387
- Cheng, L.**, *see* Pan, D., *TCSI March 2021* 1091-1101
- Cheng, L.**, *see* Ge, X., *TCSI June 2021* 2736-2748
- Cheng, Q.**, *see* Xie, Z., *TCSI March 2021* 1366-1376
- Cheng, W.**, Zhang, K., Jiang, B., and Ding, S.X., Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances; *TCSI May 2021* 2121-2133
- Cheng, X.**, *see* Han, Y., *TCSI July 2021* 2962-2975
- Cheng, X.**, *see* Tan, C., *TCSI July 2021* 3044-3057
- Cheng, X.**, *see* Lu, J., *TCSI July 2021* 2976-2985
- Cheng, Y.**, *see* Wang, C., *TCSI Oct. 2021* 4182-4193
- Cheng, Z.**, *see* Zhang, Z., *TCSI Sept. 2021* 3598-3610
- Cheung, R.C.C.**, *see* Liu, Y., *TCSI Oct. 2021* 4194-4206
- Chiang, C.**, *see* Tsai, K., *TCSI Aug. 2021* 3328-3336
- Chiou, C.**, *see* Wang, C., *TCSI Oct. 2021* 4182-4193
- Choi, D.**, *see* Kang, S.M., *TCSI Dec. 2021* 4837-4850
- Choi, E.**, *see* Namgoong, G., *TCSI June 2021* 2702-2713
- Choi, J.**, Kwon, D., Woo, J., Park, E., and Kwon, K., Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device; *TCSI July 2021* 2863-2875
- Choi, P.**, Lee, M., and Kim, D.K., ECC Coprocessor Over a NIST Prime Field Using Fast Partial Montgomery Reduction; *TCSI March 2021* 1206-1216
- Choi, P.**, *see* Liu, B., *TCSI June 2021* 2404-2417
- Choi, S.**, Ahn, H.K., Song, B., Kang, S.H., and Jung, S., Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction; *TCSI June 2021* 2481-2493
- Choi, S.**, Park, C.S., Chae, H.Y., Oh, B., Lee, J., Kwon, Y.M., Baik, J.M., Shin, H., and Kim, J.J., A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes; *TCSI June 2021* 2432-2443
- Choi, Y.**, *see* Lim, C., *TCSI Aug. 2021* 3242-3253
- Chu, H.**, *see* Huang, B., *TCSI Nov. 2021* 4672-4685
- Chu, J.**, *see* Liu, X., *TCSI July 2021* 2998-3011
- Chu, P.**, *see* Guo, L., *TCSI Dec. 2021* 5194-5205
- Chu, Y.**, *see* Liu, T., *TCSI Feb. 2021* 904-917
- Chua, L.**, *see* Marco, M.D., *TCSI Jan. 2021* 14-24
- Chua, L.**, *see* Huang, T., *TCSI Nov. 2021* 4417-4418
- Chua, L.**, *see* Huang, T., *TCSI Dec. 2021* 4835-4836
- Chua, L.O.**, *see* Jin, P., *TCSI Nov. 2021* 4419-4432
- Chua, L.O.**, *see* James, A.P., *TCSI Nov. 2021* 4470-4481
- Chua, L.O.**, *see* Messaris, I., *TCSI Dec. 2021* 4979-4992
- Chua, L.O.**, *see* Kang, S.M., *TCSI Dec. 2021* 4837-4850
- Chuan, K.C.T.**, *see* Yu, C., *TCSI Feb. 2021* 667-679
- Ciampa, M.**, Active Circuits With Diodes: Topological Conditions Sufficient to Determine the State of a Diode; *TCSI Jan. 2021* 35-44
- Ciccognani, W.**, Colangeli, S., Longhi, P.E., Serino, A., Giofre, R., Pace, L., and Limiti, E., Broadband Amplifier Design Technique by Dissipative Matching Networks; *TCSI Jan. 2021* 148-160
- Cid-Pastor, A.**, *see* Martinez-Trevino, B.A., *TCSI Jan. 2021* 524-535
- Ciftci, B.**, Chamanian, S., Koyuncuoglu, A., Muhtaroglu, A., and Kulah, H., A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators; *TCSI April 2021* 1458-1471
- Ciofani, M.**, *see* Meucci, R., *TCSI July 2021* 3023-3030
- Ciubotaru, F.**, *see* Mahmoud, A.N., *TCSI Jan. 2021* 536-549
- Cohen, E.**, *see* Regev, D., *TCSI Dec. 2021* 5168-5181
- Colangeli, S.**, *see* Ciccognani, W., *TCSI Jan. 2021* 148-160
- Colter, Z.**, *see* Fayazi, M., *TCSI June 2021* 2418-2431
- Connell, L.**, *see* Jiang, H., *TCSI April 2021* 1432-1443
- Constantinou, T.G.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Constantinou, T.G.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Corinto, F.**, *see* Marco, M.D., *TCSI Jan. 2021* 14-24
- Corinto, F.**, *see* Zoppo, G., *TCSI Dec. 2021* 4910-4923
- Coromina, J.**, *see* Ebrahimi, A., *TCSI July 2021* 2787-2799
- Costa-Rauschert, M.C.**, *see* Cabrera, C., *TCSI Aug. 2021* 3232-3241
- Cotofana, S.**, *see* Mahmoud, A.N., *TCSI Jan. 2021* 536-549
- Crafton, B.**, *see* Karimzadeh, F., *TCSI Feb. 2021* 751-764
- Craninckx, J.**, *see* Hershberg, B., *TCSI July 2021* 2813-2826
- Crovetti, P.**, *see* Toledo, P., *TCSI Sept. 2021* 3693-3706
- Crovetti, P.S.**, *see* Rubino, R., *TCSI June 2021* 2494-2507
- Cruz-Blas, C.A.d.l.**, *see* Beloso-Legarra, J., *TCSI Sept. 2021* 3562-3573
- Cui, H.**, Ghaffari, F., Le, K., Declercq, D., Lin, J., and Wang, Z., Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes; *TCSI Feb. 2021* 879-891
- Cui, Y.**, *see* Liu, L., *TCSI Sept. 2021* 3901-3912
- Cui, Y.**, Yang, Y., Zhu, Y., Qiao, J., and Guo, L., Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances; *TCSI Oct. 2021* 4360-4370

D

- D'Amico, S.**, *see* Radogna, A.V., *TCSI March 2021* 1123-1133
- D'Amico, S.**, *see* Biccario, G.E., *TCSI Nov. 2021* 4626-4635
- D'Arco, M.**, *see* Napoli, E., *TCSI Oct. 2021* 4142-4155
- da Costa, E.A.C.**, *see* Paim, G., *TCSI April 2021* 1481-1492
- da Costa, E.A.C.**, *see* Seidel, H.B., *TCSI May 2021* 1814-1826
- da Rosa, M.M.A.**, *see* Seidel, H.B., *TCSI May 2021* 1814-1826
- Dai, W.**, *see* Liu, Y., *TCSI Oct. 2021* 4194-4206
- Darabi, H.**, *see* Mirzaei, A., *TCSI Jan. 2021* 553
- Datta, G.**, Lin, Y., Zhang, B., and Beerel, P.A., Metastability in Superconducting Single Flux Quantum (SFQ) Logic; *TCSI May 2021* 1990-2002
- David, J.**, *see* Perodou, A., *TCSI Jan. 2021* 161-174
- David, J.**, and Barragan, M.J., Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020; *TCSI Aug. 2021* 3131-3132
- de Lima, A.A.**, *see* Passos, W.L., *TCSI Nov. 2021* 4761-4771
- De Matteis, M.**, *see* Vallicelli, E.A., *TCSI Jan. 2021* 3-13
- de Moraes Amory, A.**, *see* Juracy, L.R., *TCSI Nov. 2021* 4783-4795
- DeBrunner, L.S.**, *see* Xue, D., *TCSI May 2021* 2051-2059
- DeBrunner, V.**, *see* Xue, D., *TCSI May 2021* 2051-2059
- Declercq, D.**, *see* Cui, H., *TCSI Feb. 2021* 879-891
- Demirkol, A.S.**, *see* Messaris, I., *TCSI Dec. 2021* 4979-4992
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- Deng, C.**, *see* Zhu, Y., *TCSI March 2021* 1146-1159
- Deng, W.**, *see* Sun, Z., *TCSI Jan. 2021* 196-209
- Deng, W.**, *see* Liu, B., *TCSI Feb. 2021* 603-616
- Dermit, D.**, *see* Hershberg, B., *TCSI July 2021* 2813-2826
- Derudder, V.**, *see* Li, M., *TCSI May 2021* 2224-2233
- Dessel, C.**, *see* Li, M., *TCSI May 2021* 2224-2233
- Dessouky, M.**, *see* Mostafa, M., *TCSI May 2021* 2003-2016
- Diguet, J.**, *see* Arakawa, R., *TCSI Jan. 2021* 67-76
- Ding, L.**, *see* Huang, M., *TCSI April 2021* 1659-1670
- Ding, S.**, *see* Fang, L., *TCSI June 2021* 2626-2638
- Ding, S.X.**, *see* Cheng, W., *TCSI May 2021* 2121-2133
- Ding, X.**, *see* Chen, J., *TCSI Feb. 2021* 918-928
- Ding, Y.**, *see* Wang, C., *TCSI Oct. 2021* 4182-4193
- Do, A.T.**, *see* Pu, J., *TCSI Dec. 2021* 5081-5094
- Dogan, H.**, *see* Kara, I., *TCSI Jan. 2021* 210-223
- Dogiamis, G.C.**, *see* Yi, X., *TCSI Sept. 2021* 3537-3550
- Dong, J.**, *see* Liu, J., *TCSI May 2021* 2060-2068
- Dong, J.**, *see* Li, X., *TCSI Oct. 2021* 4268-4277
- Dong, L.**, *see* Lin, D., *TCSI March 2021* 1034-1044
- Dong, P.**, *see* Li, Q., *TCSI May 2021* 1892-1905
- Dong, W.**, Li, S., Fu, X., Li, Z., Fairbank, M., and Gao, Y., Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks; *TCSI April 2021* 1760-1768
- Dong, X.**, *see* Zhang, D., *TCSI May 2021* 2158-2170
- Dong, X.**, *see* Wang, Q., *TCSI Aug. 2021* 3436-3448
- Donnelly, Y.**, and Kennedy, M.P., An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion; *TCSI Oct. 2021* 4259-4267
- Dou, C.**, *see* Chen, J., *TCSI Feb. 2021* 918-928

- Dounavis, A.**, *see* Menkad, T., *TCSI March 2021* 1283-1296
Dounavis, A., *see* Menkad, T., *TCSI Dec. 2021* 5120-5133
Dreslinski, R., *see* Fayazi, M., *TCSI June 2021* 2418-2431
Du, E., *see* Wang, J., *TCSI May 2021* 2107-2120
Du, K., *see* Wang, Z., *TCSI March 2021* 1160-1170
Du, Y., Liu, F., Qiu, J., and Buss, M., Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning ; *TCSI Oct. 2021* 4324-4336
Duan, N., Wu, Y., Sun, X., and Zhong, C., Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink; *TCSI April 2021* 1610-1623
Duan, S., *see* Zhou, Y., *TCSI Dec. 2021* 4851-4861
Duan, Z., *see* Pan, D., *TCSI March 2021* 1091-1101
Duran, J., *see* Fragasse, R., *TCSI May 2021* 1827-1840

E

- Ebrahimi, A.**, Coromina, J., Munoz-Enano, J., Velez, P., Scott, J., Ghorbani, K., and Martin, F., Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line; *TCSI July 2021* 2787-2799
Edfors, O., *see* Sarajlic, M., *TCSI May 2021* 2183-2195
Eggimann, M., Rahimi, A., and Benini, L., A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing ; *TCSI Oct. 2021* 4116-4128
Eissa, A.I., *see* Alvarez-Fontecilla, E., *TCSI March 2021* 965-974
El-Kharashi, M.W., *see* Mostafa, M., *TCSI May 2021* 2003-2016
El-Sankary, K., *see* Truhachev, D., *TCSI Jan. 2021* 496-509
Elkoori Ghantala Karnam, V., *see* Tannirkulam Chandrasekaran, S., *TCSI March 2021* 1023-1033
Ellinger, F., *see* Ferschischi, A., *TCSI June 2021* 2368-2381
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Eshraghian, J.K., *see* Wang, X., *TCSI Jan. 2021* 264-274
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Estrada-Lopez, J.J., *see* Zeng, Z., *TCSI Sept. 2021* 3587-3597
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F

- Faehn, E.**, *see* Pavlidis, A., *TCSI June 2021* 2580-2593
Fahimi, Z., Mahmoodi, M.R., Klachko, M., Nili, H., and Strukov, D.B., The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits ; *TCSI Oct. 2021* 4090-4101
Fairbank, M., *see* Dong, W., *TCSI April 2021* 1760-1768
Fan, D., *see* Jiang, H., *TCSI March 2021* 1217-1230
Fan, S., Yan, H., Zhang, H., Shen, H., and Shi, K., Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application; *TCSI Feb. 2021* 856-867
Fan, X., Babic, M., Zhang, S., Grass, E., and Krstic, M., Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction; *TCSI July 2021* 3031-3043
Fang, L., Ding, S., Park, J.H., and Ma, L., Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints; *TCSI June 2021* 2626-2638
Farjah, E., *see* Naseri, F., *TCSI March 2021* 1308-1318
Farzam, M., Bayat-Sarmadi, S., Mosanaei-Boorani, H., and Alivand, A., Hardware Architecture for Supersingular Isogeny Diffie-Hellman and Key Encapsulation Using a Fast Montgomery Multiplier; *TCSI May 2021* 2042-2050
Fayazi, M., Colter, Z., Afshari, E., and Dreslinski, R., Applications of Artificial Intelligence on the Modeling and Optimization for Analog and Mixed-Signal Circuits: A Review; *TCSI June 2021* 2418-2431
Fei, Y., Shi, P., and Lim, C., Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach; *TCSI June 2021* 2616-2625
Feng, Q., *see* Liao, Z., *TCSI Feb. 2021* 929-939
Feng, Q., *see* Liao, Z., *TCSI July 2021* 3103-3113
Feng, Z., *see* Liu, X., *TCSI July 2021* 2998-3011
Feng, Z., *see* Bi, X., *TCSI Nov. 2021* 4589-4602

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- Fernando, T.**, *see* Lin, D., *TCSI March 2021* 1034-1044
Ferrari, P., *see* Gomes, L., *TCSI Aug. 2021* 3158-3169
Ferrari, P., *see* Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
Ferschischi, A., Ghaleb, H., Schulz, M., Jorges, U., Carta, C., and Ellinger, F., Nonlinear Analysis of Cross-Coupled Super-Regenerative Oscillators; *TCSI June 2021* 2368-2381
Forrester, M., *see* Jiang, H., *TCSI April 2021* 1432-1443
Forte, D., *see* Park, B., *TCSI Nov. 2021* 4700-4709
Forti, M., *see* Marco, M.D., *TCSI Jan. 2021* 14-24
Fougstedt, C., *see* Jain, V., *TCSI Jan. 2021* 25-34
Fragasse, R., Tantawy, R., Smith, D., Specht, T., Taghipour, Z., Hooser, P.V., Taylor, C., Ronningen, T.J., Fuller, E., Reyner, C., Duran, J., Ariyawansa, G., Krishna, S., and Khalil, W., Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors; *TCSI May 2021* 1827-1840

Franciosi, L., *see* Radogna, A.V., *TCSI March 2021* 1123-1133**Friedman, J.S.**, *see* Hu, X., *TCSI Jan. 2021* 301-310**Fu, C.**, *see* Ji, L., *TCSI July 2021* 2841-2849**Fu, D.**, *see* Yin, P., *TCSI July 2021* 2925-2935**Fu, H.**, Hong, Q., Wang, C., Sun, J., and Li, Y., Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit; *TCSI Nov. 2021* 4495-4507**Fu, X.**, *see* Dong, W., *TCSI April 2021* 1760-1768**Fu, Y.**, *see* Chen, H., *TCSI March 2021* 1231-1244**Fu, Y.**, *see* Chen, H., *TCSI Aug. 2021* 3293-3304**Fuller, E.**, *see* Fragasse, R., *TCSI May 2021* 1827-1840**Fusco, V.**, *see* Zhang, Z., *TCSI Sept. 2021* 3598-3610**G**

- Galayko, D.**, *see* Koskin, E., *TCSI Jan. 2021* 406-415
Galias, Z., Continuation-Based Method to Find Periodic Windows in Bifurcation Diagrams With Applications to the Chua's Circuit With a Cubic Nonlinearity ; *TCSI Sept. 2021* 3784-3793
Galton, I., *see* Alvarez-Fontecilla, E., *TCSI March 2021* 965-974
Gao, H., *see* Chang, X., *TCSI April 2021* 1706-1715
Gao, P., *see* Wu, J., *TCSI May 2021* 2271-2279
Gao, Y., *see* Dong, W., *TCSI April 2021* 1760-1768
Gao, Y., *see* Lin, X., *TCSI Sept. 2021* 3890-3900
Gaquiere, C., *see* Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
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Garibotti, R., *see* Abich, G., *TCSI Nov. 2021* 4772-4782
Garrett, J., *see* Chen, K., *TCSI Oct. 2021* 4062-4075
Garrido, M., and Malagon, P., The Constant Multiplier FFT; *TCSI Jan. 2021* 322-335
Gava, J., *see* Abich, G., *TCSI Nov. 2021* 4772-4782
Ge, M., *see* Wang, L., *TCSI Dec. 2021* 4957-4969
Ge, X., Cheng, L., Yao, Y., and Ki, W., A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA; *TCSI June 2021* 2736-2748
Ge, Z., *see* Chen, L., *TCSI Nov. 2021* 4616-4625
Gebregiorgis, A., *see* Singh, A., *TCSI May 2021* 1917-1930
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Ghaleb, H., *see* Ferschischi, A., *TCSI June 2021* 2368-2381
Ghanbari, B., *see* Haghiri, S., *TCSI Jan. 2021* 275-287
Ghanbarpour, M., Naderi, A., Haghiri, S., and Ahmadi, A., An Efficient Digital Realization of Retinal Light Adaptation in Cone Photoreceptors; *TCSI Dec. 2021* 5072-5080
Ghannouchi, F.M., *see* Liu, X., *TCSI July 2021* 2998-3011
Gheisarnejad, M., *see* Yildirim, B., *TCSI April 2021* 1693-1705
Ghorbani, K., *see* Ebrahimi, A., *TCSI July 2021* 2787-2799
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- Gielen, G.G.E.,** see Callens, N., *TCSI Aug. 2021 3117-3130*
- Gines, A.,** Leger, G., and Peralias, E., Digital Non-Linearity Calibration for ADCs With Redundancy Using a New LUT Approach; *TCSI Aug. 2021 3197-3210*
- Ginzberg, N.,** see Regev, D., *TCSI Dec. 2021 5168-5181*
- Giofre, R.,** see Cicognani, W., *TCSI Jan. 2021 148-160*
- Giustolisi, G.,** and Palumbo, G., Design of Three-Stage OTA Based on Set-timing-Time Requirements Including Large and Small Signal Behavior; *TCSI March 2021 998-1011*
- Goh, W.L.,** see Pu, J., *TCSI Dec. 2021 5081-5094*
- Gois, J.N.,** see Passos, W.L., *TCSI Nov. 2021 4761-4771*
- Gomes, L.,** Sharma, E., Souza, A.A.L., Serrano, A.L.C., Rheder, G.P., Pistono, E., Ferrari, P., and Bourdel, S., 77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Coplanar Stripline Resonator ; *TCSI Aug. 2021 3158-3169*
- Gomes, W.,** see Nasrin, S., *TCSI May 2021 1966-1978*
- Gomez-Estern, F.,** see Albea, C., *TCSI Aug. 2021 3485-3494*
- Gomez-Garcia, R.,** Munoz-Ferreras, J., and Psychogiou, D., Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection; *TCSI May 2021 2196-2209*
- Gomis-Bellmunt, O.,** see Orellana, L., *TCSI Sept. 2021 3758-3771*
- Gong, C.,** Zhu, G., Shi, P., and Agarwal, R.K., Distributed Fault Detection and Control for Markov Jump Systems Over Sensor Networks With Round-Robin Protocol; *TCSI Aug. 2021 3422-3435*
- Gong, J.,** see Guo, B., *TCSI Jan. 2021 224-237*
- Gopalakrishnan, L.,** see Asadikouhanjani, M., *TCSI May 2021 2030-2041*
- Gordillo, F.,** see Albea, C., *TCSI Aug. 2021 3485-3494*
- Grass, E.,** see Fan, X., *TCSI July 2021 3031-3043*
- Grebogi, C.,** see Tian, K., *TCSI July 2021 3012-3022*
- Gu, C.,** see Zhang, Z., *TCSI Sept. 2021 3598-3610*
- Gu, H.,** see Zhang, Y., *TCSI April 2021 1553-1566*
- Gu, P.,** Zhao, D., and You, X., Analysis and Design of a CMOS Bidirectional Passive Vector-Modulated Phase Shifter ; *TCSI April 2021 1398-1408*
- Gu, P.,** see Zhao, D., *TCSI Oct. 2021 3977-3990*
- Gu, P.,** see Zhao, D., *TCSI Oct. 2021 4413*
- Gu, Z.,** see Tao, T., *TCSI May 2021 1906-1916*
- Guan, D.,** see Yang, Z., *TCSI April 2021 1472-1480*
- Guan, X.,** see Yin, P., *TCSI July 2021 2925-2935*
- Guan, Z.,** see Zhu, Z., *TCSI Jan. 2021 444-457*
- Guan, Z.,** see Han, T., *TCSI Oct. 2021 4393-4402*
- Guerrero, J.M.,** see Chen, J., *TCSI Feb. 2021 918-928*
- Gui, P.,** see Pan, D., *TCSI March 2021 1091-1101*
- Guo, B.,** Gong, J., and Wang, Y., A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations; *TCSI Jan. 2021 224-237*
- Guo, L.,** see Cui, Y., *TCSI Oct. 2021 4360-4370*
- Guo, L.,** Li, X., Chu, P., and Wu, K., Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power; *TCSI Dec. 2021 5194-5205*
- Guo, M.,** see Song, J., *TCSI Aug. 2021 3377-3387*
- Guo, T.,** see Liu, B., *TCSI June 2021 2404-2417*
- Guo, Y.,** see Wang, X., *TCSI Feb. 2021 741-750*
- Gupta, S.,** and Calhoun, B.H., Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs ; *TCSI March 2021 1171-1182*
- Gupta, S.,** see Boljanovic, V., *TCSI April 2021 1727-1739*
- Gupta, S.,** see Hu, H., *TCSI June 2021 2444-2456*
- Gupta, S.,** and Calhoun, B.H., Dynamic Write V_{MIN} and Yield Estimation for Nanoscale SRAMs; *TCSI Dec. 2021 5038-5048*
- Gural, A.,** see Rekhi, A.S., *TCSI June 2021 2555-2568*
- Guyeux, C.,** see Wang, Q., *TCSI Sept. 2021 3794-3807*
- H**
- Ha, Y.,** see Chen, J., *TCSI April 2021 1520-1531*
- Hadidian, B.,** see Khoeini, F., *TCSI Sept. 2021 3642-3655*
- Haghiri, S.,** Naderi, A., Ghanbari, B., and Ahmadi, A., High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions; *TCSI Jan. 2021 275-287*
- Haghiri, S.,** see Ghanbarpour, M., *TCSI Dec. 2021 5072-5080*
- Halder, T.,** see Pavan, S., *TCSI Aug. 2021 3222-3231*
- Hamad, G.B.,** see Alshalalfah, A., *TCSI Aug. 2021 3147-3157*
- Hamada, M.,** see Shiba, K., *TCSI Feb. 2021 692-703*
- Hamdioui, S.,** see Mahmoud, A.N., *TCSI Jan. 2021 536-549*
- Hamdioui, S.,** see Singh, A., *TCSI May 2021 1917-1930*
- Hamilton, T.J.,** see Chen, L., *TCSI Nov. 2021 4616-4625*
- Hampel, A.F.,** see Juracy, L.R., *TCSI Nov. 2021 4783-4795*
- Han, G.,** and Kinget, P.R., Comments on “Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters” *TCSI Jan. 2021 550-552*
- Han, G.,** and Kinget, P.R., Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface; *TCSI Sept. 2021 3913-3926*
- Han, J.,** see Yuan, T., *TCSI Jan. 2021 250-263*
- Han, J.,** Bae, W., Chang, E., Wang, Z., Nikolic, B., and Alon, E., LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies; *TCSI March 2021 1012-1022*
- Han, J.,** see Jiang, H., *TCSI March 2021 1217-1230*
- Han, J.,** see Han, Y., *TCSI July 2021 2962-2975*
- Han, J.,** see Tan, C., *TCSI July 2021 3044-3057*
- Han, J.,** see Xie, R., *TCSI Dec. 2021 5095-5107*
- Han, Q.,** see Yang, Y., *TCSI May 2021 2148-2157*
- Han, R.,** see Yi, X., *TCSI Sept. 2021 3537-3550*
- Han, S.,** see Kwon, E., *TCSI Oct. 2021 4156-4169*
- Han, T.,** see Lee, E., *TCSI Aug. 2021 3305-3316*
- Han, T.,** Guan, Z., Xiao, B., and Yan, H., Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence; *TCSI Oct. 2021 4393-4402*
- Han, Y.,** Li, T., Cheng, X., Wang, L., Han, J., Zhao, Y., and Zeng, X., Radiation Hardened 12T SRAM With Crossbar-Based Peripheral Circuit in 28nm CMOS Technology; *TCSI July 2021 2962-2975*
- Han, Y.,** see Min, F., *TCSI Oct. 2021 4207-4220*
- Han, Y.,** see Liu, P., *TCSI Nov. 2021 4444-4455*
- Han, Z.,** see Wang, W., *TCSI Sept. 2021 3822-3835*
- Hancioğlu, E.,** see Shi, L., *TCSI Oct. 2021 4001-4012*
- Hancke, G.P.,** see Chen, J., *TCSI Feb. 2021 918-928*
- Hanyu, T.,** see Arakawa, R., *TCSI Jan. 2021 67-76*
- Hao, L.,** and Shi, G., High-Dimensional Extension of the TICER Algorithm; *TCSI Nov. 2021 4722-4734*
- Hariharan, S.I.,** see Malavipathirana, H., *TCSI Aug. 2021 3363-3376*
- Harpe, P.J.A.,** see Berkol, G., *TCSI April 2021 1409-1420*
- Hasler, J.,** and Natarajan, A., Continuous-Time, Configurable Analog Linear System Solutions With Transconductance Amplifiers; *TCSI Feb. 2021 765-775*
- Hasler, J.,** and Shah, S., An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter; *TCSI Feb. 2021 592-602*
- Hassan, A.,** see Mirfakhraei, S.S., *TCSI April 2021 1388-1397*
- Hazra, J.,** see Rafiq, S., *TCSI July 2021 2900-2910*
- He, P.,** see Zhao, D., *TCSI Oct. 2021 3977-3990*
- He, P.,** see Zhao, D., *TCSI Oct. 2021 4413*
- He, W.,** see Yang, Y., *TCSI May 2021 2148-2157*
- He, W.,** see Ye, L., *TCSI Dec. 2021 4821-4834*
- He, Z.,** see Zhang, Y., *TCSI April 2021 1553-1566*
- Helal, E.,** see Alvarez-Fontecilla, E., *TCSI March 2021 965-974*
- Henkel, J.,** see Paim, G., *TCSI April 2021 1481-1492*
- Henkel, J.,** see Salamin, S., *TCSI Oct. 2021 4299-4309*
- Henkel, J.,** see Balaskas, K., *TCSI Nov. 2021 4710-4721*
- Heo, D.,** see Agarwal, P., *TCSI March 2021 1080-1090*
- Heo, D.,** see Boljanovic, V., *TCSI April 2021 1727-1739*
- Hershberg, B.,** van Liempd, B., Markulic, N., Lagos, J., Martens, E., Dermitt, D., and Craninckx, J., Asynchronous Event-Driven Clocking and Control in Pipelined ADCs; *TCSI July 2021 2813-2828*
- Herzig, M.,** see Weiher, M., *TCSI May 2021 2082-2095*

- Hikawa, H.,** Hardware Self-Organizing Map Based on Digital Frequency-Locked Loop and Triangular Neighborhood Function; *TCSI March 2021* 1245-1258
- Hikihara, T.,** see Murakawa, Y., *TCSI April 2021* 1750-1759
- Hirtzlin, T.,** see Laborieux, A., *TCSI Jan. 2021* 138-147
- Hofmann, K.,** see Reuter, M., *TCSI Jan. 2021* 114-125
- Holloway, J.W.,** see Yi, X., *TCSI Sept. 2021* 3537-3550
- Honarpasvar, M.,** see Akbari, M., *TCSI Aug. 2021* 3133-3146
- Hong, J.,** see Zhu, Y., *TCSI Dec. 2021* 5007-5017
- Hong, Q.,** see Lin, H., *TCSI Aug. 2021* 3397-3410
- Hong, Q.,** see Fu, H., *TCSI Nov. 2021* 4495-4507
- Hong, R.,** see Yang, Z., *TCSI April 2021* 1472-1480
- Hong, S.,** and Sun, N., Portable CMOS NMR System With 50-kHz IF, 10- μ s Dead Time, and Frequency Tracking; *TCSI Nov. 2021* 4576-4588
- Hooser, P.V.,** see Fragasse, R., *TCSI May 2021* 1827-1840
- Hou, Y.,** see Qi, W., *TCSI June 2021* 2665-2674
- Hsiao, Y.,** see Chen, W., *TCSI April 2021* 1493-1506
- Hsu, C.,** see Kuo, S., *TCSI July 2021* 2890-2899
- Hu, B.,** see Zhu, Z., *TCSI Jan. 2021* 444-457
- Hu, B.,** see Wu, D., *TCSI Aug. 2021* 3211-3221
- Hu, C.,** see Wang, L., *TCSI Dec. 2021* 4957-4969
- Hu, H.,** Lin, C., and Gupta, S., A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX ; *TCSI June 2021* 2444-2456
- Hu, J.,** see Xia, Z., *TCSI Feb. 2021* 728-740
- Hu, J.,** and Ma, K., Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application; *TCSI June 2021* 2393-2403
- Hu, J.,** see Wang, L., *TCSI Dec. 2021* 4957-4969
- Hu, S.,** Chen, P., Quinlan, P., and Staszewski, R.B., A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS; *TCSI June 2021* 2317-2328
- Hu, X.,** Abraham, A.S., Incorvia, J.A.C., and Friedman, J.S., Hybrid Pass Transistor Logic With Ambipolar Transistors; *TCSI Jan. 2021* 301-310
- Hu, X.,** see Zhou, Y., *TCSI Dec. 2021* 4851-4861
- Hu, Y.,** see Song, J., *TCSI Aug. 2021* 3377-3387
- Hu, Z.,** Shi, P., and Wu, L., Polytopic Event-Triggered Robust Model Predictive Control for Constrained Linear Systems; *TCSI June 2021* 2594-2603
- Hu, Z.,** see Yi, X., *TCSI Sept. 2021* 3537-3550
- Hu, Z.,** Liu, S., Luo, W., and Wu, L., Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks; *TCSI Sept. 2021* 3857-3868
- Hua, C.,** see Li, K., *TCSI July 2021* 3069-3078
- Hua, L.,** Zhu, H., Shi, K., Zhong, S., Tang, Y., and Liu, Y., Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays; *TCSI April 2021* 1599-1609
- Hua, Z.,** see Bao, H., *TCSI Nov. 2021* 4534-4544
- Huan, Y.,** see Huang, B., *TCSI Nov. 2021* 4672-4685
- Huang, B.,** see Yin, N., *TCSI Jan. 2021* 311-321
- Huang, B.,** Huan, Y., Chu, H., Xu, J., Liu, L., Zheng, L., and Zou, Z., IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency; *TCSI Nov. 2021* 4672-4685
- Huang, C.,** see Wang, C., *TCSI Oct. 2021* 4182-4193
- Huang, D.,** see Pan, D., *TCSI March 2021* 1091-1101
- Huang, H.,** see Sun, Z., *TCSI Jan. 2021* 196-209
- Huang, J.,** see Zhang, W., *TCSI Feb. 2021* 776-785
- Huang, J.,** see Kuo, S., *TCSI July 2021* 2890-2899
- Huang, L.,** see Chen, M., *TCSI Feb. 2021* 950-962
- Huang, M.,** Ding, L., Li, W., Chen, C., and Liu, Z., Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault; *TCSI April 2021* 1659-1670
- Huang, Q.,** see Xia, Z., *TCSI Feb. 2021* 728-740
- Huang, Q.,** see Wang, Z., *TCSI March 2021* 1160-1170
- Huang, Q.,** see Wang, Z., *TCSI April 2021* 1624-1635
- Huang, R.,** see Wang, Z., *TCSI March 2021* 1160-1170
- Huang, R.,** see Wang, Z., *TCSI April 2021* 1624-1635
- Huang, R.,** see Song, J., *TCSI Aug. 2021* 3377-3387
- Huang, R.,** see Tang, A., *TCSI Sept. 2021* 3941-3950
- Huang, R.,** see Wang, S., *TCSI Dec. 2021* 4900-4909
- Huang, R.,** see Ye, L., *TCSI Dec. 2021* 4821-4834
- Huang, T.,** see Li, C., *TCSI May 2021* 1881-1891
- Huang, T.,** Chen, Y., Zeng, Z., and Chua, L., Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor—Part I; *TCSI Nov. 2021* 4417-4418
- Huang, T.,** Chen, Y., Zeng, Z., and Chua, L., Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor - Part II; *TCSI Dec. 2021* 4835-4836
- Huang, W.,** see Zhang, X., *TCSI March 2021* 1297-1307
- Huang, X.,** see Bai, L., *TCSI Feb. 2021* 704-714
- Huang, Y.,** see Kuo, S., *TCSI July 2021* 2890-2899
- Huang, Y.,** and Zhao, J., Cyber-Physical Systems With Multiple Denial-of-Service Attackers: A Game-Theoretic Framework ; *TCSI Oct. 2021* 4349-4359
- Huang, Z.,** see Wang, X., *TCSI Feb. 2021* 741-750
- Huemer, M.,** see Preissl, C., *TCSI May 2021* 2234-2245
- Hulfachor, R.,** see Chen, K., *TCSI Oct. 2021* 4062-4075
- Hung, J.,** see Jhang, C., *TCSI May 2021* 1773-1786
- Hussain, M.A.,** and Tsai, T., Memory Access Optimization for On-Chip Transfer Learning; *TCSI April 2021* 1507-1519
- Hwang, Y.,** see Liu, T., *TCSI Feb. 2021* 904-917

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- Ibrahim, A.,** see Younes, H., *TCSI Oct. 2021* 4232-4244
- Ibrahim, M.I.,** see Yi, X., *TCSI Sept. 2021* 3537-3550
- Iehl, B.,** see Jiang, H., *TCSI April 2021* 1432-1443
- Ielmini, D.,** see Mannocci, P., *TCSI Dec. 2021* 4889-4899
- Ielmini, D.,** see Wang, S., *TCSI Dec. 2021* 4900-4909
- Imura, R.,** Kitamura, S., and Kawahara, T., Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins ; *TCSI Dec. 2021* 5061-5071
- Im, D.,** see Bae, S., *TCSI Feb. 2021* 892-903
- Incorvia, J.A.C.,** see Hu, X., *TCSI Jan. 2021* 301-310
- Indiveri, G.,** see Rubino, A., *TCSI Jan. 2021* 45-56
- Irvine, R.,** see Jiang, H., *TCSI April 2021* 1432-1443
- Islam, A.,** see Pal, S., *TCSI June 2021* 2470-2480
- Islam, A.,** see Pal, S., *TCSI Aug. 2021* 3317-3327
- Iu, H.H.,** see Wang, X., *TCSI Jan. 2021* 264-274
- Iu, H.H.,** see Lin, D., *TCSI March 2021* 1034-1044
- Iu, H.H.,** see Jin, P., *TCSI Nov. 2021* 4419-4432

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- Jain, V.,** Fougstedt, C., and Larsson-Edefors, P., Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder; *TCSI Jan. 2021* 25-34
- James, A.P.,** and Chua, L.O., Analog Neural Computing With Super-Resolution Memristor Crossbars; *TCSI Nov. 2021* 4470-4481
- Jayaraj, A.,** see Tannirkulam Chandrasekaran, S., *TCSI March 2021* 1023-1033
- Jenderny, S.,** see Ochs, K., *TCSI Sept. 2021* 3656-3667
- Jeong, D.S.,** see Kim, J., *TCSI Jan. 2021* 350-362
- Jeong, S.,** see Lee, E., *TCSI Aug. 2021* 3305-3316
- Jeong, Y.,** Chaudhary, G., and Kim, P., Frequency Selective Impedance Transformer With High-Impedance Transforming Ratio and Extremely High/Low Termination Impedances; *TCSI June 2021* 2382-2392
- Jha, C.K.,** Singh, S., Thakker, R., Awasthi, M., and Mekie, J., Zero Aware Configurable Data Encoding by Skipping Transfer for Error Resilient Applications; *TCSI Aug. 2021* 3337-3350
- Jha, S.K.,** see Rafiq, S., *TCSI July 2021* 2900-2910
- Jhang, C.,** Xue, C., Hung, J., Chang, F., and Chang, M., Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices; *TCSI May 2021* 1773-1786
- Ji, H.,** Shen, Y., Song, W., Zhang, Z., You, X., and Zhang, C., Hardware Implementation for Belief Propagation Flip Decoding of Polar Codes; *TCSI March 2021* 1330-1341
- Ji, L.,** Fu, C., and Sun, W., Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map; *TCSI July 2021* 2841-2849

- Ji, X.,** see Wu, J., *TCSI June 2021* 2522-2534
Ji, X., see Song, J., *TCSI Aug. 2021* 3377-3387
Jiang, B., see Liu, C., *TCSI April 2021* 1646-1658
Jiang, B., see Cheng, W., *TCSI May 2021* 2121-2133
Jiang, C., see Liao, Z., *TCSI July 2021* 3103-3113
Jiang, G., see Wang, X., *TCSI Dec. 2021* 5145-5155
Jiang, H., Angizi, S., Fan, D., Han, J., and Liu, L., Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates; *TCSI March 2021* 1217-1230
Jiang, H., Al-Qaq, W., Forrester, M., Zhang, Z., McHugh, T., Iehl, B., Connell, L., Sung, E., Chadalawada, R., and Irvine, R., A 660 MHz-5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection; *TCSI April 2021* 1432-1443
Jiang, N., see Wang, J., *TCSI May 2021* 2107-2120
Jiang, S., see Wang, L., *TCSI Dec. 2021* 4957-4969
Jiang, W., Zhu, Y., Chan, C., Murmann, B., and Martins, R.P., A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler; *TCSI Feb. 2021* 557-568
Jiang, Y., see Tan, T., *TCSI March 2021* 1354-1365
Jiang, Y., and Yang, J., Asymptotic Waveform Evaluation With Higher Order Poles; *TCSI April 2021* 1681-1692
Jiang, Y., Li, C., Zhang, C., Zhao, Y., and Zang, H., A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control; *TCSI Dec. 2021* 4935-4944
Jiang, Y., see Qiu, H., *TCSI Dec. 2021* 5182-5193
Jin, H., see Tao, T., *TCSI May 2021* 1906-1916
Jin, P., Wang, G., Liang, Y., Iu, H.H., and Chua, L.O., Neuromorphic Dynamics of Chua Corsage Memristor; *TCSI Nov. 2021* 4419-4432
Jo, J., see Kim, S., *TCSI May 2021* 2017-2029
Jooq, M.K.Q., see Behbahani, F., *TCSI Dec. 2021* 5108-5119
Jorges, U., see Ferschischi, A., *TCSI June 2021* 2368-2381
Joshi, R.V., see Singh, A., *TCSI May 2021* 1917-1930
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Jung, S., see Choi, S., *TCSI June 2021* 2481-2493
Jung, S., see Kim, S.M., *TCSI June 2021* 2546-2554
Juracy, L.R., Moreira, M.T., de Morais Amory, A., Hampel, A.F., and Moraes, F.G., A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators ; *TCSI Nov. 2021* 4783-4795
- K**
- Kalita, U.,** see Butz, N., *TCSI Oct. 2021* 4013-4024
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Kang, S., see Kwon, E., *TCSI Oct. 2021* 4156-4169
Kang, S.H., see Choi, S., *TCSI June 2021* 2481-2493
Kang, S.M., Choi, D., Eshraghian, J.K., Zhou, P., Kim, J., Kong, B., Zhu, X., Demirkol, A.S., Ascoli, A., Tetzlaff, R., Lu, W.D., and Chua, L.O., How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation; *TCSI Dec. 2021* 4837-4850
Kannan, A., see Pavan, S., *TCSI Aug. 2021* 3222-3231
Kao, C., see Kuo, S., *TCSI July 2021* 2890-2899
Kara, I., Becermis, M., Kamar, M.A., Aktan, M., Dogan, H., and Mutlu, S., A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH Rectifier and DC-DC Converters; *TCSI Jan. 2021* 210-223
Karami, A., see Truhachev, D., *TCSI Jan. 2021* 496-509
Karami, M.A., and Moez, K., A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation; *TCSI Nov. 2021* 4603-4615
Karimzadeh, F., Cao, N., Crafton, B., Romberg, J., and Raychowdhury, A., A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences; *TCSI Feb. 2021* 751-764
- Karman, S.,** Tesolin, F., Levantino, S., and Samori, C., A Novel Topology of Coupled Phase-Locked Loops; *TCSI March 2021* 989-997
Kassiri, H., see Taghadosi, M., *TCSI Jan. 2021* 510-523
Kawahara, T., see Iimura, R., *TCSI Dec. 2021* 5061-5071
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Kermani, M.M., see Anastasova, M., *TCSI Oct. 2021* 4129-4141
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Khaliq, A., see Butt, U.M., *TCSI Aug. 2021* 3351-3362
Khan, M.I.W., see Yi, X., *TCSI Sept. 2021* 3537-3550
Khan, R.A., Muhaisin, M.M., and Roberts, G.W., Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits; *TCSI Oct. 2021* 4025-4037
Khan, S.A., see Butt, U.M., *TCSI Aug. 2021* 3351-3362
Khocini, F., Hadidian, B., Zhang, K., and Afshari, E., A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers; *TCSI Sept. 2021* 3642-3655
Khooban, M.H., see Yildirim, B., *TCSI April 2021* 1693-1705
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Kim, D., see Bae, S., *TCSI Feb. 2021* 892-903
Kim, D., see Kwon, H., *TCSI April 2021* 1567-1577
Kim, D.K., see Choi, P., *TCSI March 2021* 1206-1216
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Kim, J., Kornijcuk, V., Ye, C., and Jeong, D.S., Hardware-Efficient Emulation of Leaky Integrate-and-Fire Model Using Template-Scaling-Based Exponential Function Approximation; *TCSI Jan. 2021* 350-362
Kim, J., see Ryu, S., *TCSI July 2021* 2876-2889
Kim, J., see Lee, E., *TCSI Aug. 2021* 3305-3316
Kim, J., see Kang, S.M., *TCSI Dec. 2021* 4837-4850
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Kim, M., see Yi, X., *TCSI Sept. 2021* 3537-3550
Kim, P., see Jeong, Y., *TCSI June 2021* 2382-2392
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Kim, S., Jo, J., and Park, I., Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing; *TCSI May 2021* 2017-2029
Kim, S., see Lee, E., *TCSI Aug. 2021* 3305-3316
Kim, S.M., Song, B., and Jung, S., Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM; *TCSI June 2021* 2546-2554
Kim, T., and Chae, Y., A 2.1 mW 2 MHz-BW 73.8 dB-SNDR Buffer-Embedded Noise-Shaping SAR ADC; *TCSI Dec. 2021* 5029-5037
Kim, T.T., see Yu, C., *TCSI Feb. 2021* 667-679
Kim, W., see Ryu, S., *TCSI July 2021* 2876-2889
Kim, Y., Kim, S., and Ahn, C.K., Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics; *TCSI Jan. 2021* 396-405
Kim, Y.H., see Kwon, H., *TCSI April 2021* 1567-1577
Kinget, P.R., see Han, G., *TCSI Jan. 2021* 550-552
Kinget, P.R., see Han, G., *TCSI Sept. 2021* 3913-3926
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Klachko, M., see Fahimi, Z., *TCSI Oct. 2021* 4090-4101
Klein, J., see Laborieux, A., *TCSI Jan. 2021* 138-147
Klemme, F., and Amrouch, H., Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization; *TCSI June 2021* 2569-2579

- Kneip, A.**, and Bol, D., Impact of Analog Non-Idealities on the Design Space of 6T-SRAM Current-Domain Dot-Product Operators for In-Memory Computing; *TCSI May 2021* 1931-1944
- Ko, J.H.**, *see* Lee, E., *TCSI Aug. 2021* 3305-3316
- Ko, S.**, *see* Asadikouhanjani, M., *TCSI May 2021* 2030-2041
- Kocarev, L.**, *see* Zhang, W., *TCSI Feb. 2021* 776-785
- Koneru, B.N.G.**, Chandrachoodan, N., and Vasudevan, V., A Smoothed LAS- SO-Based DNN Sparsification Technique; *TCSI Oct. 2021* 4287-4298
- Kong, B.**, *see* Kang, S.M., *TCSI Dec. 2021* 4837-4850
- Kong, W.**, *see* Wu, J., *TCSI May 2021* 2271-2279
- Korkmaz, A.**, *see* Zoppo, G., *TCSI Dec. 2021* 4910-4923
- Korniienko, A.**, *see* Perodou, A., *TCSI Jan. 2021* 161-174
- Kornijcuk, V.**, *see* Kim, J., *TCSI Jan. 2021* 350-362
- Korpi, D.**, *see* Campo, P.P., *TCSI Jan. 2021* 336-349
- Koskin, E.**, Bisiaux, P., Galayko, D., and Blokhina, E., All Digital Phase- Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance; *TCSI Jan. 2021* 406-415
- Koul, S.K.**, *see* Barthwal, A., *TCSI April 2021* 1421-1431
- Koyuncuoglu, A.**, *see* Ciftci, B., *TCSI April 2021* 1458-1471
- Krauss, T.A.**, *see* Reuter, M., *TCSI Jan. 2021* 114-125
- Krishna, S.**, *see* Fragasse, R., *TCSI May 2021* 1827-1840
- Krishnaswamy, H.**, *see* Tymchenko, M., *TCSI Feb. 2021* 569-580
- Krstic, M.**, *see* Fan, X., *TCSI July 2021* 3031-3043
- Krstic, M.**, *see* Schrape, O., *TCSI Nov. 2021* 4796-4809
- Kuang, J.**, *see* Ming, X., *TCSI June 2021* 2354-2367
- Kulah, H.**, *see* Ciftci, B., *TCSI April 2021* 1458-1471
- Kulkarni, A.**, Ouameur, M.A., and Massicotte, D., Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method; *TCSI Sept. 2021* 3732-3745
- Kumar, A.**, *see* Pavlidis, A., *TCSI June 2021* 2580-2593
- Kumar, A.**, and Rawat, M., Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems; *TCSI Nov. 2021* 4636-4647
- Kumar, B.**, *see* Negi, S., *TCSI Aug. 2021* 3254-3264
- Kumar, N.**, *see* Saxena, V., *TCSI Feb. 2021* 940-949
- Kumar, N.**, and Rawat, K., Coding Efficiency Enhancement Using Time Inter- leaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter; *TCSI July 2021* 2986-2997
- Kumar, S.**, *see* Yi, S., *TCSI Dec. 2021* 4970-4978
- Kung, J.**, *see* Park, G., *TCSI July 2021* 2950-2961
- Kuo, H.**, *see* Li, C., *TCSI May 2021* 1881-1891
- Kuo, H.**, *see* Wang, C., *TCSI Oct. 2021* 4182-4193
- Kuo, S.**, Huang, J., Huang, Y., Kao, C., Hsu, C., and Chen, C., A Multi-Step Incremental Analog-to-Digital Converter With a Single Opamp and Two- Capacitor SAR Extended Counting; *TCSI July 2021* 2890-2899
- Kuroda, T.**, *see* Shiba, K., *TCSI Feb. 2021* 692-703
- Kuttappa, R.**, Taskin, B., Lerner, S., and Pano, V., Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems; *TCSI April 2021* 1636-1645
- Kuznetsov, N.**, Matveev, A., Yuldashev, M., and Yuldashev, R., Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges; *TCSI Oct. 2021* 4049-4061
- Kwon, D.**, *see* Choi, J., *TCSI July 2021* 2863-2875
- Kwon, E.**, Han, S., Park, Y., Yoon, J., and Kang, S., Reinforcement Learning-Based Power Management Policy for Mobile Device Systems; *TCSI Oct. 2021* 4156-4169
- Kwon, H.**, Kim, D., Kim, Y.H., and Kang, S., Variation-Aware SRAM Cell Optimization Using Deep Neural Network-Based Sensitivity Analysis; *TCSI April 2021* 1567-1577
- Kwon, K.**, *see* Choi, J., *TCSI July 2021* 2863-2875
- Kwon, Y.M.**, *see* Choi, S., *TCSI June 2021* 2432-2443
- L**
- Laborieux, A.**, Bocquet, M., Hirtzlin, T., Klein, J., Nowak, E., Vianello, E., Portal, J., and Querlioz, D., Implementation of Ternary Weights With Resistive RAM Using a Single Sense Operation Per Synapse; *TCSI Jan. 2021* 138-147
- Lacaita, A.L.**, *see* Buccolieri, F., *TCSI July 2021* 2800-2812
- Lagos, J.**, *see* Hershberg, B., *TCSI July 2021* 2813-2826
- Langlois, J.M.P.**, *see* Ahmadi, M., *TCSI Aug. 2021* 3184-3196
- Lanniel, A.**, Boeser, T., Alpert, T., and Ortmanns, M., Noise Analysis of Charge-Balanced Readout Circuits for MEMS Accelerometers; *TCSI Jan. 2021* 175-184
- Lao, Y.**, *see* Wang, A., *TCSI June 2021* 2508-2521
- Lapucci, A.**, *see* Meucci, R., *TCSI July 2021* 3023-3030
- Larsson-Edefors, P.**, *see* Jain, V., *TCSI Jan. 2021* 25-34
- Le, H.**, *see* Tran-Dinh, T., *TCSI Jan. 2021* 103-113
- Le, K.**, *see* Cui, H., *TCSI Feb. 2021* 879-891
- Lebdeh, M.A.**, *see* Singh, A., *TCSI May 2021* 1917-1930
- Leblebici, Y.**, *see* Akkaya, A., *TCSI July 2021* 2766-2774
- Lee, B.**, *see* Namgoong, G., *TCSI June 2021* 2702-2713
- Lee, E.**, Han, T., Seo, D., Shin, G., Kim, J., Kim, S., Jeong, S., Rhe, J., Park, J., Ko, J.H., and Lee, Y., A Charge-Domain Scalable-Weight In-Memory Computing Macro With Dual-SRAM Architecture for Precision-Scalable DNN Accelerators; *TCSI Aug. 2021* 3305-3316
- Lee, H.**, *see* Asadikouhanjani, M., *TCSI May 2021* 2030-2041
- Lee, J.**, *see* Choi, S., *TCSI June 2021* 2432-2443
- Lee, M.**, *see* Choi, P., *TCSI March 2021* 1206-1216
- Lee, M.**, *see* Tavares, Y.A., *TCSI April 2021* 1444-1457
- Lee, S.**, *see* Tran-Dinh, T., *TCSI Jan. 2021* 103-113
- Lee, Y.**, *see* Park, G., *TCSI July 2021* 2950-2961
- Lee, Y.**, *see* Lee, E., *TCSI Aug. 2021* 3305-3316
- Leene, L.B.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Leene, L.B.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Leger, G.**, *see* Gines, A., *TCSI Aug. 2021* 3197-3210
- Lehrack, S.**, *see* Vallicelli, E.A., *TCSI Jan. 2021* 3-13
- Leitner, T.**, *see* Schrogendorfer, D., *TCSI May 2021* 1800-1813
- Leng, W.**, and Abidi, A.A., Approximate Equivalent Circuits to Understand Tradeoffs in Geometry of On-Chip Inductors; *TCSI March 2021* 975-988
- Lepilliet, S.**, *see* Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
- Lerner, S.**, *see* Kuttappa, R., *TCSI April 2021* 1636-1645
- Leung, K.W.**, *see* Wang, S.H., *TCSI Nov. 2021* 4556-4565
- Levantino, S.**, *see* Karman, S., *TCSI March 2021* 989-997
- Levantino, S.**, *see* Avallone, L., *TCSI July 2021* 2775-2786
- Li, B.**, Wang, H., Zhang, X., Ren, J., Liu, L., Sun, H., and Zheng, N., Dynamic Dataflow Scheduling and Computation Mapping Techniques for Efficient Depthwise Separable Convolution Acceleration; *TCSI Aug. 2021* 3279-3292
- Li, B.**, *see* Min, F., *TCSI Oct. 2021* 4207-4220
- Li, C.**, Yuan, M., Liao, C., Chang, C., Lin, Y., Tsai, T., Huang, T., Liao, H., Lu, C., Kuo, H., Ximenes, A.R., and Staszewski, R.B., A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS; *TCSI May 2021* 1881-1891
- Li, C.**, Yang, Y., Liang, H., and Wu, B., Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems; *TCSI June 2021* 2651-2664
- Li, C.**, *see* Zhang, S., *TCSI Dec. 2021* 4945-4956
- Li, C.**, *see* Jiang, Y., *TCSI Dec. 2021* 4935-4944
- Li, D.**, *see* Qian, H., *TCSI Sept. 2021* 3574-3586
- Li, E.**, *see* Tao, T., *TCSI May 2021* 1906-1916
- Li, G.**, *see* Wang, Y., *TCSI May 2021* 2257-2270
- Li, H.**, *see* Liu, X., *TCSI Jan. 2021* 238-249
- Li, H.**, *see* Yang, Q., *TCSI March 2021* 1134-1145
- Li, H.**, Xiao, L., Qi, C., and Li, J., Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets; *TCSI Oct. 2021* 4170-4181
- Li, H.**, *see* Bao, H., *TCSI Nov. 2021* 4534-4544
- Li, H.**, *see* Ye, L., *TCSI Dec. 2021* 4821-4834
- Li, J.**, Lin, Y., Ning, N., and Yu, Q., A +0.44°C/-0.4°C Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO; *TCSI March 2021* 1102-1113
- Li, J.**, *see* Li, H., *TCSI Oct. 2021* 4170-4181
- Li, J.**, *see* Min, F., *TCSI Oct. 2021* 4207-4220
- Li, K.**, Hua, C., You, X., and Ahn, C.K., Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems; *TCSI July 2021* 3069-3078
- Li, K.**, *see* Cheng, J., *TCSI Dec. 2021* 4924-4934

- Li, L.,** see Chen, H., *TCSI March 2021 1231-1244*
- Li, L.,** see Chen, H., *TCSI Aug. 2021 3293-3304*
- Li, M.,** Derudder, V., Bertrand, K., Dessel, C., and Bourdoux, A., High-Speed LDPC Decoders Towards 1 Tb/s; *TCSI May 2021 2224-2233*
- Li, M.,** see Yin, H., *TCSI Sept. 2021 3965-3974*
- Li, Q.,** Liu, C., Dong, P., Zhang, Y., Li, T., Lin, S., Yang, M., Qiao, F., Wang, Y., Luo, L., and Yang, H., NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting; *TCSI May 2021 1892-1905*
- Li, R.,** see Wang, C., *TCSI Dec. 2021 5134-5144*
- Li, S.,** see Truhachev, D., *TCSI Jan. 2021 496-509*
- Li, S.,** see Dong, W., *TCSI April 2021 1760-1768*
- Li, S.,** Zhang, Z., Mao, R., Xiao, J., Chang, L., and Zhou, J., A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning; *TCSI April 2021 1543-1552*
- Li, S.,** Zheng, Y., and Su, H., Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling; *TCSI Aug. 2021 3460-3473*
- Li, T.,** see Zhu, Z., *TCSI Jan. 2021 444-457*
- Li, T.,** see Li, Q., *TCSI May 2021 1892-1905*
- Li, T.,** see Han, Y., *TCSI July 2021 2962-2975*
- Li, T.,** and Tian, E., Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case; *TCSI Dec. 2021 5156-5167*
- Li, W.,** see Huang, M., *TCSI April 2021 1659-1670*
- Li, W.,** see Wu, Y., *TCSI June 2021 2639-2650*
- Li, X.,** see Liu, J., *TCSI May 2021 2060-2068*
- Li, X.,** Liu, J., Dong, J., Lu, L., and Lu, J., Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach; *TCSI Oct. 2021 4268-4277*
- Li, X.,** see Guo, L., *TCSI Dec. 2021 5194-5205*
- Li, Y.,** see Chen, J., *TCSI Feb. 2021 918-928*
- Li, Y.,** see Tan, C., *TCSI July 2021 3044-3057*
- Li, Y.,** see Wu, D., *TCSI Aug. 2021 3211-3221*
- Li, Y.,** see Fu, H., *TCSI Nov. 2021 4495-4507*
- Li, Z.,** see Sun, Z., *TCSI Jan. 2021 196-209*
- Li, Z.,** see Lin, D., *TCSI March 2021 1034-1044*
- Li, Z.,** see Dong, W., *TCSI April 2021 1760-1768*
- Liacha, A.,** see Oudjida, A.K., *TCSI May 2021 1979-1989*
- Lian, C.,** see Zhang, Y., *TCSI March 2021 1193-1205*
- Liang, H.,** see Wang, X., *TCSI Feb. 2021 741-750*
- Liang, H.,** see Ming, X., *TCSI June 2021 2354-2367*
- Liang, H.,** see Li, C., *TCSI June 2021 2651-2664*
- Liang, J.,** see Wang, Y., *TCSI May 2021 2257-2270*
- Liang, L.,** see Cheng, J., *TCSI Dec. 2021 4924-4934*
- Liang, S.,** Lu, S., Lin, J., and Wang, Z., Low-Latency Hardware Accelerator for Improved Engle-Granger Cointegration in Pairs Trading; *TCSI July 2021 2911-2924*
- Liang, Y.,** see Jin, P., *TCSI Nov. 2021 4419-4432*
- Liao, C.,** see Li, C., *TCSI May 2021 1881-1891*
- Liao, H.,** see Li, C., *TCSI May 2021 1881-1891*
- Liao, X.,** see Lin, D., *TCSI March 2021 1034-1044*
- Liao, Z.,** Ma, S., Feng, Q., Xia, C., and Yu, D., Frequency Splitting Elimination and Utilization in Magnetic Coupling Wireless Power Transfer Systems; *TCSI Feb. 2021 929-939*
- Liao, Z.,** Feng, Q., Jiang, C., Wu, F., Xia, C., and Yu, D., Analysis and Design of EIT-Like Magnetic Coupling Wireless Power Transfer Systems; *TCSI July 2021 3103-3113*
- Liehr, M.,** see Rafiq, S., *TCSI July 2021 2900-2910*
- Lim, C.,** see Fei, Y., *TCSI June 2021 2616-2625*
- Lim, C.,** see Liu, Y., *TCSI Aug. 2021 3449-3459*
- Lim, C.,** Choi, Y., Park, Y., Song, J., Ahn, S., Park, S., and Kim, C., A Capacitively Coupled CT Δ Σ With Chopping Artifacts Rejection for Sensor Readout ICs; *TCSI Aug. 2021 3242-3253*
- Limiti, E.,** see Ciccognani, W., *TCSI Jan. 2021 148-160*
- Lin, C.,** see Wang, X., *TCSI Jan. 2021 264-274*
- Lin, C.,** see Boljanovic, V., *TCSI April 2021 1727-1739*
- Lin, C.,** see Hu, H., *TCSI June 2021 2444-2456*
- Lin, D.,** Liao, X., Dong, L., Yang, R., Yu, S.S., Iu, H.H., Fernando, T., and Li, Z., Experimental Study of Fractional-Order RC Circuit Model Using the Caputo and Caputo-Fabrizio Derivatives; *TCSI March 2021 1034-1044*
- Lin, H.,** Wang, C., Chen, C., Sun, Y., Zhou, C., Xu, C., and Hong, Q., Neural Bursting and Synchronization Emulated by Neural Networks and Circuits; *TCSI Aug. 2021 3397-3410*
- Lin, J.,** see Cui, H., *TCSI Feb. 2021 879-891*
- Lin, J.,** Yuan, K., and Wang, L., A New Adaptive Sparse Pseudospectral Approximation Method and its Application for Stochastic Power Flow; *TCSI July 2021 3089-3102*
- Lin, J.,** see Liang, S., *TCSI July 2021 2911-2924*
- Lin, J.,** see Xie, X., *TCSI July 2021 2936-2949*
- Lin, M.,** see Yang, Z., *TCSI April 2021 1472-1480*
- Lin, Q.,** see Tan, T., *TCSI March 2021 1354-1365*
- Lin, S.,** see Li, Q., *TCSI May 2021 1892-1905*
- Lin, T.N.,** Wang, B., and Bermak, A., Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing; *TCSI Oct. 2021 3991-4000*
- Lin, W.,** see Chang, X., *TCSI April 2021 1706-1715*
- Lin, X.,** Liu, J., Liu, F., Liu, Z., Gao, Y., and Sun, G., Fractional-Order Sliding Mode Approach of Buck Converters With Mismatched Disturbances; *TCSI Sept. 2021 3890-3900*
- Lin, Y.,** see Liu, T., *TCSI Feb. 2021 904-917*
- Lin, Y.,** see Li, J., *TCSI March 2021 1102-1113*
- Lin, Y.,** see Li, C., *TCSI May 2021 1881-1891*
- Lin, Y.,** see Datta, G., *TCSI May 2021 1990-2002*
- Liu, B.,** Zhang, Y., Qiu, J., Ngo, H.C., Deng, W., Nakata, K., Yoshioka, T., Emmei, J., Pang, J., Narayanan, A.T., Zhang, H., Someya, T., Shirane, A., and Okada, K., A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration; *TCSI Feb. 2021 603-616*
- Liu, B.,** Boon, C.C., Mao, M., Choi, P., and Guo, T., A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application; *TCSI June 2021 2404-2417*
- Liu, B.,** see Lu, J., *TCSI July 2021 2976-2985*
- Liu, B.,** see Liu, P., *TCSI Nov. 2021 4444-4455*
- Liu, C.,** Jiang, B., Zhang, K., and Patton, R.J., Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies; *TCSI April 2021 1646-1658*
- Liu, C.,** see Li, Q., *TCSI May 2021 1892-1905*
- Liu, C.,** Wu, X., Niu, R., Aziz-Alaoui, M.A., and Lu, J., Opinion Diffusion in Two-Layer Interconnected Networks; *TCSI Sept. 2021 3772-3783*
- Liu, D.,** see Lu, J., *TCSI July 2021 2976-2985*
- Liu, E.,** see Tao, T., *TCSI May 2021 1906-1916*
- Liu, F.,** see Lin, X., *TCSI Sept. 2021 3890-3900*
- Liu, F.,** see Du, Y., *TCSI Oct. 2021 4324-4336*
- Liu, H.,** see Sun, Z., *TCSI Jan. 2021 196-209*
- Liu, J.,** Li, X., Lu, L., Dong, J., and Lu, J., Evaluating Performances and Importance of Venture Capitals: A Complex Network Approach; *TCSI May 2021 2060-2068*
- Liu, J.,** see Lin, X., *TCSI Sept. 2021 3890-3900*
- Liu, J.,** see Li, X., *TCSI Oct. 2021 4268-4277*
- Liu, J.,** see Wu, Y., *TCSI Nov. 2021 4735-4745*
- Liu, K.,** see Wang, W., *TCSI Sept. 2021 3822-3835*
- Liu, L.,** see Zhu, Y., *TCSI March 2021 1146-1159*
- Liu, L.,** see Jiang, H., *TCSI March 2021 1217-1230*
- Liu, L.,** see Sarajlic, M., *TCSI May 2021 2183-2195*
- Liu, L.,** see Bai, K., *TCSI July 2021 2850-2862*
- Liu, L.,** see Li, B., *TCSI Aug. 2021 3279-3292*
- Liu, L.,** Cui, Y., Liu, Y., and Tong, S., Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time; *TCSI Sept. 2021 3901-3912*
- Liu, L.,** see Huang, B., *TCSI Nov. 2021 4672-4685*
- Liu, M.,** see Chen, L., *TCSI Jan. 2021 416-425*
- Liu, M.,** see Murad, M.A.A., *TCSI July 2021 3079-3088*

- Liu, N.**, Geiger, R.L., and Chen, D., Sub-ppm/ $^{\circ}\text{C}$ Bandgap References With Natural Basis Expansion for Curvature Cancellation; *TCSI Sept. 2021* 3551-3561
- Liu, P.**, You, Z., Wu, J., Liu, B., Han, Y., and Chakrabarty, K., Fault Modeling and Efficient Testing of Memristor-Based Memory; *TCSI Nov. 2021* 4444-4455
- Liu, P.X.**, see Wang, H., *TCSI Oct. 2021* 4337-4348
- Liu, S.**, see Hu, Z., *TCSI Sept. 2021* 3857-3868
- Liu, S.**, Tang, X., Niknia, F., Reviriego, P., Liu, W., Louri, A., and Lombardi, F., Stochastic Dividers for Low Latency Neural Networks; *TCSI Oct. 2021* 4102-4115
- Liu, S.**, see Liu, Y., *TCSI Oct. 2021* 4194-4206
- Liu, S.**, see Chen, W., *TCSI Nov. 2021* 4566-4575
- Liu, T.**, Wang, S., Lin, Y., Hwang, Y., Chen, C., and Chu, Y., Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures; *TCSI Feb. 2021* 904-917
- Liu, W.**, see Yuan, T., *TCSI Jan. 2021* 250-263
- Liu, W.**, see Zhu, Q., *TCSI Sept. 2021* 3951-3964
- Liu, W.**, see Nan, G., *TCSI Sept. 2021* 3707-3718
- Liu, W.**, see Liu, S., *TCSI Oct. 2021* 4102-4115
- Liu, X.**, Mao, M., Bi, X., Li, H., and Chen, Y., Exploring Applications of STT-RAM in GPU Architectures; *TCSI Jan. 2021* 238-249
- Liu, X.**, see Chen, M., *TCSI April 2021* 1716-1726
- Liu, X.**, Chen, W., Chu, J., Ghannouchi, F.M., and Feng, Z., Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters; *TCSI July 2021* 2998-3011
- Liu, Y.**, see Wang, C., *TCSI Jan. 2021* 387-395
- Liu, Y.**, see Hua, L., *TCSI April 2021* 1599-1609
- Liu, Y.**, Shi, P., Lim, C., and Yu, H., A New Approach of Formation Control for Multi-Agent Systems With Environmental Changes; *TCSI Aug. 2021* 3449-3459
- Liu, Y.**, see Liu, L., *TCSI Sept. 2021* 3901-3912
- Liu, Y.**, Zhang, J., Liu, S., Wang, Q., Dai, W., and Cheung, R.C.C., Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication; *TCSI Oct. 2021* 4194-4206
- Liu, Y.**, see Wang, S., *TCSI Dec. 2021* 4900-4909
- Liu, Y.**, see Ye, L., *TCSI Dec. 2021* 4821-4834
- Liu, Z.**, see Huang, M., *TCSI April 2021* 1659-1670
- Liu, Z.**, see Lu, J., *TCSI July 2021* 2976-2985
- Liu, Z.**, see Lin, X., *TCSI Sept. 2021* 3890-3900
- Livanelioglu, C.**, see Rubino, A., *TCSI Jan. 2021* 45-56
- Lombardi, F.**, see Yuan, T., *TCSI Jan. 2021* 250-263
- Lombardi, F.**, see Nan, G., *TCSI Sept. 2021* 3707-3718
- Lombardi, F.**, see Liu, S., *TCSI Oct. 2021* 4102-4115
- Longhi, P.E.**, see Ciccognani, W., *TCSI Jan. 2021* 148-160
- Lopes, P.A.C.**, and Gerald, J.A.B., Low Delay Short Word Length Sigma Delta Active Noise Control; *TCSI Sept. 2021* 3746-3757
- Lopez-Martin, A.J.**, see Beloso-Legarra, J., *TCSI Sept. 2021* 3562-3573
- Lotti, N.**, see Wittenhagen, E., *TCSI Jan. 2021* 57-66
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- Lu, C.**, see Li, C., *TCSI May 2021* 1881-1891
- Lu, J.**, see Zhang, Q., *TCSI Feb. 2021* 842-855
- Lu, J.**, see Zhu, Z., *TCSI April 2021* 1671-1680
- Lu, J.**, see Xu, Y., *TCSI April 2021* 1589-1598
- Lu, J.**, see Liu, J., *TCSI May 2021* 2060-2068
- Lu, J.**, Liu, D., Liu, Z., Cheng, X., Wei, L., Zhang, C., Zou, X., and Liu, B., Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device; *TCSI July 2021* 2976-2985
- Lu, J.**, see Wang, Q., *TCSI Aug. 2021* 3436-3448
- Lu, J.**, see Wang, Y., *TCSI Aug. 2021* 3474-3484
- Lu, J.**, see Liu, C., *TCSI Sept. 2021* 3772-3783
- Lu, J.**, see Wang, W., *TCSI Sept. 2021* 3822-3835
- Lu, J.**, see Li, X., *TCSI Oct. 2021* 4268-4277
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- Lu, L.**, see Liu, J., *TCSI May 2021* 2060-2068
- Lu, L.**, see Li, X., *TCSI Oct. 2021* 4268-4277
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- Lu, W.D.**, see Kang, S.M., *TCSI Dec. 2021* 4837-4850
- Lu, Y.**, see Wang, X., *TCSI Feb. 2021* 741-750
- Lu, Y.**, see Wang, C., *TCSI June 2021* 2714-2724
- Lu, Z.**, see Chen, H., *TCSI March 2021* 1231-1244
- Lu, Z.**, see Chen, H., *TCSI Aug. 2021* 3293-3304
- Luo, J.**, see Xia, Z., *TCSI Feb. 2021* 728-740
- Luo, L.**, see Li, Q., *TCSI May 2021* 1892-1905
- Luo, W.**, see Hu, Z., *TCSI Sept. 2021* 3857-3868
- Luo, X.**, see Zhou, J., *TCSI June 2021* 2457-2469
- Luo, X.**, see Shu, Y., *TCSI June 2021* 2341-2353
- Luo, Y.**, see Wang, C., *TCSI Jan. 2021* 387-395
- Luo, Y.**, see Lyu, F., *TCSI Feb. 2021* 715-727
- Luo, Y.**, see Yu, S., *TCSI July 2021* 2753-2765
- Lyu, F.**, Xu, X., Wang, Y., Luo, Y., Wang, Y., and Pan, H., Ultralow-Latency VLSI Architecture Based on a Linear Approximation Method for Computing N^{th} Roots of Floating-Point Numbers; *TCSI Feb. 2021* 715-727
- Lyu, Y.**, see Bai, L., *TCSI Feb. 2021* 704-714

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- Ma, H.**, see Tao, T., *TCSI May 2021* 1906-1916
- Ma, H.**, see Namgoong, G., *TCSI June 2021* 2702-2713
- Ma, K.**, see Hu, J., *TCSI June 2021* 2393-2403
- Ma, L.**, see Fang, L., *TCSI June 2021* 2626-2638
- Ma, S.**, see Liao, Z., *TCSI Feb. 2021* 929-939
- Ma, T.**, see Yang, Y., *TCSI Jan. 2021* 434-443
- Ma, Y.**, see Zhang, Q., *TCSI Feb. 2021* 842-855
- Madanayake, A.**, see Malavipathirana, H., *TCSI Aug. 2021* 3363-3376
- Maffezzoni, P.**, see Bernardini, A., *TCSI March 2021* 1269-1282
- Maghari, N.**, see Park, B., *TCSI Nov. 2021* 4700-4709
- Maheshwari, S.**, Stathopoulos, S., Wang, J., Serb, A., Pan, Y., Mifsud, A., Leene, L.B., Shen, J., Papavassiliou, C., Constandinou, T.G., and Prodromakis, T., Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout; *TCSI Dec. 2021* 4876-4888
- Maheshwari, S.**, Stathopoulos, S., Wang, J., Serb, A., Pan, Y., Mifsud, A., Leene, L.B., Shen, J., Papavassiliou, C., Constandinou, T.G., and Prodromakis, T., Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps; *TCSI Dec. 2021* 4862-4875
- Mahmoodi, M.R.**, see Fahimi, Z., *TCSI Oct. 2021* 4090-4101
- Mahmoud, A.N.**, Vanderveken, F., Adelmann, C., Ciubotaru, F., Cotofana, S., and Hamdioui, S., Spin Wave Normalization Toward All Magnonic Circuits; *TCSI Jan. 2021* 536-549
- Mai, D.**, and Kennedy, M.P., MASH-Based Divider Controllers for Mitigation of Wandering Spurs in a Fractional- N Frequency Synthesizer; *TCSI Jan. 2021* 126-137
- Majhi, S.**, see Sethia, G., *TCSI March 2021* 1319-1329
- Mak, P.**, see Zhao, X., *TCSI Jan. 2021* 89-102
- Mak, P.**, see Yang, Z., *TCSI June 2021* 2307-2316
- Malagon, P.**, see Garrido, M., *TCSI Jan. 2021* 322-335
- Malavipathirana, H.**, Hariharan, S.I., Udayanga, N., Mandal, S., and Madanayake, A., A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs; *TCSI Aug. 2021* 3363-3376
- Man, J.**, see Song, X., *TCSI Jan. 2021* 363-375
- Man, J.**, see Song, X., *TCSI Sept. 2021* 3869-3880
- Mandal, S.**, see Malavipathirana, H., *TCSI Aug. 2021* 3363-3376
- Mannocci, P.**, Pedretti, G., Giannone, E., Melacarne, E., Sun, Z., and Ielmini, D., A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors; *TCSI Dec. 2021* 4889-4899
- Manoli, Y.**, see Butz, N., *TCSI Oct. 2021* 4013-4024
- Mao, B.**, see Xu, Y., *TCSI April 2021* 1589-1598
- Mao, K.**, see Zhu, Q., *TCSI Sept. 2021* 3951-3964
- Mao, M.**, see Liu, X., *TCSI Jan. 2021* 238-249
- Mao, M.**, see Liu, B., *TCSI June 2021* 2404-2417
- Mao, R.**, see Li, S., *TCSI April 2021* 1543-1552

- Mao, S.,** see Zhang, W., *TCSI Feb. 2021* 776-785
- Maqbool, K.Q.,** see Azmat, R., *TCSI Nov. 2021* 4545-4555
- Marco, M.D.,** Forti, M., Corinto, F., and Chua, L., Unfolding Nonlinear Dynamics in Analogue Systems With Mem-Elements; *TCSI Jan. 2021* 14-24
- Margalef-Rovira, M.,** Occello, O., Saadi, A.A., Avramovic, V., Lepilliet, S., Vincent, L., Barragan, M.J., Pistono, E., Bourdel, S., Gaguere, C., and Ferrari, P., mm-Wave Through-Load Element for On-Wafer Measurement Applications; *TCSI Aug. 2021* 3170-3183
- Maria Strollo, A.G.,** see Napoli, E., *TCSI Oct. 2021* 4142-4155
- Markulic, N.,** see Hershberg, B., *TCSI July 2021* 2813-2826
- Marrone, F.,** see Zoppo, G., *TCSI Dec. 2021* 4910-4923
- Martens, E.,** see Hershberg, B., *TCSI July 2021* 2813-2826
- Martin, F.,** see Ebrahimi, A., *TCSI July 2021* 2787-2799
- Martinez-Salamero, L.,** see Martinez-Trevino, B.A., *TCSI Jan. 2021* 524-535
- Martinez-Trevino, B.A.,** Aroudi, A.E., Cid-Pastor, A., Garcia, G., and Martinez-Salamero, L., Synthesis of Constant Power Loads Using Switching Converters Under Sliding-Mode Control; *TCSI Jan. 2021* 524-535
- Martins, G.C.,** and Serdijn, W.A., An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range; *TCSI March 2021* 1342-1353
- Martins, R.P.,** see Zhao, X., *TCSI Jan. 2021* 89-102
- Martins, R.P.,** see Jiang, W., *TCSI Feb. 2021* 557-568
- Martins, R.P.,** see Wang, C., *TCSI June 2021* 2714-2724
- Martins, R.P.,** see Yang, Z., *TCSI June 2021* 2307-2316
- Massicotte, D.,** see Kulkarni, A., *TCSI Sept. 2021* 3732-3745
- Matveev, A.,** see Kuznetsov, N., *TCSI Oct. 2021* 4049-4061
- Mazzaro, V.,** and Kennedy, M.P., Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities; *TCSI June 2021* 2295-2306
- Mazzaro, V.,** and Kennedy, M.P., Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers; *TCSI Oct. 2021* 4038-4048
- McAllister, J.,** see Wu, Y., *TCSI June 2021* 2675-2687
- McHugh, T.,** see Jiang, H., *TCSI April 2021* 1432-1443
- Mekie, J.,** see Jha, C.K., *TCSI Aug. 2021* 3337-3350
- Melacarne, E.,** see Mannocci, P., *TCSI Dec. 2021* 4889-4899
- Menkad, T.,** and Dounavis, A., Using Strictly Dissipative Impedance Coupling in the Waveform Relaxation Method for the Analysis of Interconnect Circuits; *TCSI March 2021* 1283-1296
- Menkad, T.,** and Dounavis, A., Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits; *TCSI Dec. 2021* 5120-5133
- Mercandelli, M.,** see Avallone, L., *TCSI July 2021* 2775-2786
- Messaris, I.,** Brown, T.D., Demirkol, A.S., Ascoli, A., Al Chawa, M.M., Williams, R.S., Tetzlaff, R., and Chua, L.O., NbO₂-Mott Memristor: A Circuit-Theoretic Investigation; *TCSI Dec. 2021* 4979-4992
- Meucci, R.,** Euzzor, S., Ciofini, M., Lapucci, A., and Zambrano, S., Demonstrating Filtered Feedback Control Near a Boundary Crisis; *TCSI July 2021* 3023-3030
- Michaelis, D.,** see Ochs, K., *TCSI Sept. 2021* 3656-3667
- Mifsud, A.,** see Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Mifsud, A.,** see Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Mikolajick, T.,** see Weiher, M., *TCSI May 2021* 2082-2095
- Mikulic, J.,** Schatzberger, G., and Baric, A., Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator; *TCSI Oct. 2021* 4076-4089
- Milano, F.,** see Tzounas, G., *TCSI June 2021* 2725-2735
- Milano, F.,** see Murad, M.A.A., *TCSI July 2021* 3079-3088
- Min, F.,** Xu, H., Wang, Y., Wang, Y., Li, J., Zou, X., Li, B., and Han, Y., Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator; *TCSI Oct. 2021* 4207-4220
- Ming, X.,** Kuang, J., Liang, H., Zhang, J., Qin, Y., Zhang, Z., Wang, Z., and Zhang, B., A Fast-Transient Low-Dropout Regulator With Current-Efficient Super Transconductance Cell and Dynamic Reference Control; *TCSI June 2021* 2354-2367
- Mirfakhraei, S.S.,** Audet, Y., Hassan, A., and Sawan, M., A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors; *TCSI April 2021* 1388-1397
- Mirzaei, A.,** Darabi, H., and Murphy, D., Reply to Comments on "Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters" *TCSI Jan. 2021* 553
- Mittal, P.,** see Negi, S., *TCSI Aug. 2021* 3254-3264
- Moaiyeri, M.H.,** see Behbahani, F., *TCSI Dec. 2021* 5108-5119
- Mobayen, S.,** Alattas, K.A., and Assawinchaichote, W., Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator; *TCSI Oct. 2021* 4403-4412
- Mocerino, L.,** and Calimera, A., Fast and Accurate Inference on Microcontrollers With Boosted Cooperative Convolutional Neural Networks (BC-Net); *TCSI Jan. 2021* 77-88
- Moez, K.,** see Karami, M.A., *TCSI Nov. 2021* 4603-4615
- Mohamed, O.A.,** see Alshalalfah, A., *TCSI Aug. 2021* 3147-3157
- Mohammad, U.,** see Yin, P., *TCSI July 2021* 2925-2935
- Mohammed, M.A.,** and Roberts, G.W., Generalized Relationship Between Frequency Response and Settling Time of CMOS OTAs: Toward Many-Stage Design; *TCSI Dec. 2021* 4993-5006
- Mohapatra, A.S.,** and Biswas, K., A Fractional Order Notch Filter to Compensate the Attenuation-Loss Due to Change in Order of the Circuit; *TCSI Feb. 2021* 655-666
- Mohapatra, S.,** see Boljanovic, V., *TCSI April 2021* 1727-1739
- Mohapatra, S.,** see Pal, S., *TCSI June 2021* 2470-2480
- Mohapatra, S.,** see Pal, S., *TCSI Aug. 2021* 3317-3327
- Moitra, A.,** and Panda, P., DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays; *TCSI Nov. 2021* 4482-4494
- Molderez, T.R.,** Rabaey, K., and Verhelst, M., A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments; *TCSI March 2021* 1068-1079
- Molnar, A.,** see Ying, R., *TCSI May 2021* 2210-2223
- Moon, J.,** see Park, S.K., *TCSI March 2021* 1183-1192
- Moon, U.,** see Shi, L., *TCSI Oct. 2021* 4001-4012
- Moraes, F.G.,** see Juracy, L.R., *TCSI Nov. 2021* 4783-4795
- Moreira, M.T.,** see Juracy, L.R., *TCSI Nov. 2021* 4783-4795
- Mosanaei-Boorani, H.,** see Farzam, M., *TCSI May 2021* 2042-2050
- Mostafa, M.,** El-Kharashi, M.W., Dessouky, M., and Zaki, A.M., A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement; *TCSI May 2021* 2003-2016
- Motomura, M.,** see Shiba, K., *TCSI Feb. 2021* 692-703
- Mozaffari-Kermani, M.,** see Bisheh-Niasar, M., *TCSI Nov. 2021* 4648-4659
- Muhaisin, M.M.,** see Khan, R.A., *TCSI Oct. 2021* 4025-4037
- Muhtaroglu, A.,** see Ciftci, B., *TCSI April 2021* 1458-1471
- Munoz-Enano, J.,** see Ebrahimi, A., *TCSI July 2021* 2787-2799
- Munoz-Ferreras, J.,** see Gomez-Garcia, R., *TCSI May 2021* 2196-2209
- Murad, M.A.A.,** Liu, M., and Milano, F., Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices; *TCSI July 2021* 3079-3088
- Murakawa, Y.,** and Hikihara, T., Output Series-Parallel Connection of Passivity-Based Controlled DC-DC Converters: Generalization of Asymptotic Stability; *TCSI April 2021* 1750-1759
- Muratore, D.G.,** see Villamizar, D.A., *TCSI April 2021* 1578-1588
- Murmann, B.,** see Jiang, W., *TCSI Feb. 2021* 557-568
- Murmann, B.,** see Villamizar, D.A., *TCSI April 2021* 1578-1588
- Murphy, D.,** see Mirzaei, A., *TCSI Jan. 2021* 553
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- Naderi, A.,** see Haghiri, S., *TCSI Jan. 2021* 275-287
- Naderi, A.,** see Ghanbarpour, M., *TCSI Dec. 2021* 5072-5080
- Nagulu, A.,** see Tymchenko, M., *TCSI Feb. 2021* 569-580
- Nakashima, Y.,** see Tran, T.H., *TCSI Oct. 2021* 4245-4258
- Nakata, K.,** see Liu, B., *TCSI Feb. 2021* 603-616
- Nam, I.,** see Bae, S., *TCSI Feb. 2021* 892-903
- Nambiar, V.P.,** see Pu, J., *TCSI Dec. 2021* 5081-5094
- Namgoong, G.,** Choi, E., Park, W., Lee, B., Park, H., Ma, H., and Bien, F., 3–12-V Wide Input Range Adaptive Delay Compensated Active Rectifier

- for 6.78-MHz Loosely Coupled Wireless Power Transfer System; *TCSI June 2021* 2702-2713
- Nan, G.**, Wang, Z., Wang, C., Wu, B., Wang, Z., Liu, W., and Lombardi, F., An Energy Efficient Accelerator for Bidirectional Recurrent Neural Networks (BiRNNs) Using Hybrid-Iterative Compression With Error Sensitivity; *TCSI Sept. 2021* 3707-3718
- Napoli, E.**, Zacharelos, E., D'Arco, M., and Maria Strollo, A.G., Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures; *TCSI Oct. 2021* 4142-4155
- Narayanan, A.T.**, *see* Liu, B., *TCSI Feb. 2021* 603-616
- Naseri, F.**, Farjah, E., Schaltz, E., Lu, K., and Tashakor, N., Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications ; *TCSI March 2021* 1308-1318
- Nasrin, S.**, Badawi, D., Cetin, A.E., Gomes, W., and Trivedi, A.R., MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators; *TCSI May 2021* 1966-1978
- Natarajan, A.**, *see* Hasler, J., *TCSI Feb. 2021* 765-775
- Nayak, S.K.**, *see* Sethia, G., *TCSI March 2021* 1319-1329
- Neale, A.**, *see* Patel, D., *TCSI Aug. 2021* 3265-3278
- Negi, S.**, Mittal, P., and Kumar, B., Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer; *TCSI Aug. 2021* 3254-3264
- Ngo, H.C.**, *see* Liu, B., *TCSI Feb. 2021* 603-616
- Nguyen, H.T.**, and Peterson, A.F., Machine Learning for Automating the Design of Millimeter-Wave Baluns; *TCSI June 2021* 2329-2340
- Nicollini, G.**, *see* Cavallaro, M., *TCSI Dec. 2021* 5018-5028
- Niknia, F.**, *see* Liu, S., *TCSI Oct. 2021* 4102-4115
- Nikolic, B.**, *see* Han, J., *TCSI March 2021* 1012-1022
- Nili, H.**, *see* Fahimi, Z., *TCSI Oct. 2021* 4090-4101
- Ning, N.**, *see* Li, J., *TCSI March 2021* 1102-1113
- Niu, R.**, *see* Liu, C., *TCSI Sept. 2021* 3772-3783
- Nonis, R.**, *see* Biccario, G.E., *TCSI Nov. 2021* 4626-4635
- Nowak, E.**, *see* Laborieux, A., *TCSI Jan. 2021* 138-147

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- O'Connor, I.**, *see* Perodou, A., *TCSI Jan. 2021* 161-174
- Occello, O.**, *see* Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
- Ochs, K.**, Michaelis, D., and Jenderney, S., Synthesis of an Equivalent Circuit for Spike-Timing-Dependent Axon Growth: What Fires Together Now Really Wires Together; *TCSI Sept. 2021* 3656-3667
- Oh, B.**, *see* Choi, S., *TCSI June 2021* 2432-2443
- Okada, K.**, *see* Sun, Z., *TCSI Jan. 2021* 196-209
- Okada, K.**, *see* Liu, B., *TCSI Feb. 2021* 603-616
- Olivera, F.**, *see* Pinheiro, C.A., *TCSI Nov. 2021* 4810-4819
- Omori, T.**, *see* Shiba, K., *TCSI Feb. 2021* 692-703
- Onabajo, M.**, *see* Chen, K., *TCSI Oct. 2021* 4062-4075
- Onizawa, N.**, *see* Arakawa, R., *TCSI Jan. 2021* 67-76
- Oreggioni, J.**, *see* Cabrera, C., *TCSI Aug. 2021* 3232-3241
- Orellana, L.**, Sainz, L., Prieto-Araujo, E., and Gomis-Bellmunt, O., Stability Assessment for Multi-Infeed Grid-Connected VSCs Modeled in the Admittance Matrix Form; *TCSI Sept. 2021* 3758-3771
- Ortmanns, M.**, *see* Lanniel, A., *TCSI Jan. 2021* 175-184
- Ost, L.**, *see* Abich, G., *TCSI Nov. 2021* 4772-4782
- Ouameur, M.A.**, *see* Kulkarni, A., *TCSI Sept. 2021* 3732-3745
- Oudjida, A.K.**, and Liacha, A., Radix-2^w Arithmetic for Scalar Multiplication in Elliptic Curve Cryptography; *TCSI May 2021* 1979-1989

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- Pace, L.**, *see* Ciccognani, W., *TCSI Jan. 2021* 148-160
- Pahwa, G.**, *see* Paim, G., *TCSI April 2021* 1481-1492
- Paim, G.**, Zervakis, G., Pahwa, G., Chauhan, Y.S., da Costa, E.A.C., Bampi, S., Henkel, J., and Amrouch, H., On the Resiliency of NCFET Circuits Against Voltage Over-Scaling; *TCSI April 2021* 1481-1492
- Paim, G.**, *see* Seidel, H.B., *TCSI May 2021* 1814-1826
- Pal, S.**, Mohapatra, S., Ki, W., and Islam, A., Design of Soft-Error-Aware SRAM With Multi-Node Upset Recovery for Aerospace Applications; *TCSI June 2021* 2470-2480
- Pal, S.**, Mohapatra, S., Ki, W., and Islam, A., Soft-Error-Immune Read-Stability-Improved SRAM for Multi-Node Upset Tolerance in Space Applications; *TCSI Aug. 2021* 3317-3327
- Palermo, S.**, *see* Zoppo, G., *TCSI Dec. 2021* 4910-4923
- Palumbo, G.**, *see* Centurelli, F., *TCSI Feb. 2021* 680-691
- Palumbo, G.**, *see* Giustolisi, G., *TCSI March 2021* 998-1011
- Pan, D.**, Duan, Z., Wu, B., Wang, Y., Huang, D., Wang, Y., Sun, L., Gui, P., and Cheng, L., A 76-81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars; *TCSI March 2021* 1091-1101
- Pan, H.**, *see* Lyu, F., *TCSI Feb. 2021* 715-727
- Pan, H.**, *see* Chang, X., *TCSI April 2021* 1706-1715
- Pan, Y.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Pan, Y.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Panda, P.**, *see* Moitra, A., *TCSI Nov. 2021* 4482-4494
- Pang, J.**, *see* Sun, Z., *TCSI Jan. 2021* 196-209
- Pang, J.**, *see* Liu, B., *TCSI Feb. 2021* 603-616
- Pang, X.**, Song, W., Shen, Y., You, X., and Zhang, C., Efficient Row-Layered Decoder for Sparse Code Multiple Access; *TCSI Aug. 2021* 3495-3507
- Pang, Z.**, *see* Zhan, M., *TCSI June 2021* 2688-2701
- Panigrahi, B.K.**, *see* Saxena, V., *TCSI Feb. 2021* 940-949
- Pannu, J.S.**, *see* Rafiq, S., *TCSI July 2021* 2900-2910
- Pano, V.**, *see* Kuttappa, R., *TCSI April 2021* 1636-1645
- Papavassiliou, C.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Papavassiliou, C.**, *see* Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Park, B.**, Forte, D., Tehranipoor, M.M., and Maghari, N., A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC; *TCSI Nov. 2021* 4700-4709
- Park, C.S.**, *see* Choi, S., *TCSI June 2021* 2432-2443
- Park, C.Y.**, *see* Ryu, S., *TCSI July 2021* 2876-2889
- Park, E.**, *see* Choi, J., *TCSI July 2021* 2863-2875
- Park, G.**, Kung, J., and Lee, Y., Design and Analysis of Approximate Compressors for Balanced Error Accumulation in MAC Operator; *TCSI July 2021* 2950-2961
- Park, H.**, *see* Namgoong, G., *TCSI June 2021* 2702-2713
- Park, I.**, *see* Kim, S., *TCSI May 2021* 2017-2029
- Park, J.**, *see* Lee, E., *TCSI Aug. 2021* 3305-3316
- Park, J.H.**, *see* Shen, H., *TCSI Feb. 2021* 818-828
- Park, J.H.**, *see* Fang, L., *TCSI June 2021* 2626-2638
- Park, J.H.**, *see* Cheng, J., *TCSI Dec. 2021* 4924-4934
- Park, S.**, *see* Lim, C., *TCSI Aug. 2021* 3242-3253
- Park, S.K.**, and Moon, J., Characterization of Inter-Cell Interference in 3D NAND Flash Memory ; *TCSI March 2021* 1183-1192
- Park, W.**, *see* Namgoong, G., *TCSI June 2021* 2702-2713
- Park, Y.**, *see* Lim, C., *TCSI Aug. 2021* 3242-3253
- Park, Y.**, *see* Kwon, E., *TCSI Oct. 2021* 4156-4169
- Parodi, K.**, *see* Vallicelli, E.A., *TCSI Jan. 2021* 3-13
- Passos, W.L.**, Araujo, G.M., Gois, J.N., and de Lima, A.A., A Gait Energy Image-Based System for Brazilian Sign Language Recognition; *TCSI Nov. 2021* 4761-4771
- Patel, D.**, Neale, A., Wright, D., and Sachdev, M., Body Biased Sense Amplifier With Auto-Offset Mitigation for Low-Voltage SRAMs; *TCSI Aug. 2021* 3265-3278
- Patton, R.J.**, *see* Liu, C., *TCSI April 2021* 1646-1658
- Pavan, P.**, *see* Puglisi, F.M., *TCSI Nov. 2021* 4433-4443
- Pavan, S.**, Halder, T., and Kannan, A., Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback; *TCSI Aug. 2021* 3222-3231
- Pavlidis, A.**, Louerat, M., Faehn, E., Kumar, A., and Stratigopoulos, H., Sym-BIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety; *TCSI June 2021* 2580-2593
- Payvand, M.**, *see* Rubino, A., *TCSI Jan. 2021* 45-56
- Pedram, M.**, *see* Vahdat, S., *TCSI Aug. 2021* 3411-3421
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- Peng, C.**, *see* Zhao, M., *TCSI Jan. 2021* 426-433

- Peng, K.,** see Zhang, Y., *TCSI April 2021* 1553-1566
- Peng, K.,** see Chen, K., *TCSI Oct. 2021* 4062-4075
- Peng, N.,** see Zhao, D., *TCSI Oct. 2021* 3977-3990
- Peng, N.,** see Zhao, D., *TCSI Oct. 2021* 4413
- Peng, X.,** see Yu, S., *TCSI July 2021* 2753-2765
- Peng, X.,** Zhang, Y., Wang, W., and Yang, S., Broadband Mismatch Calibration for Time-Interleaved ADC Based on Linear Frequency Modulated Signal; *TCSI Sept. 2021* 3621-3630
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- Peralias, E.,** see Gines, A., *TCSI Aug. 2021* 3197-3210
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- Perodou, A.,** Kornienko, A., Scorletti, G., Zarudniev, M., David, J., and O'Conor, I., Frequency Design of Lossless Passive Electronic Filters: A State-Space Formulation of the Direct Synthesis Approach; *TCSI Jan. 2021* 161-174
- Peterson, A.F.,** see Nguyen, H.T., *TCSI June 2021* 2329-2340
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- Pham, H.L.,** see Tran, T.H., *TCSI Oct. 2021* 4245-4258
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- Picos, R.,** see Chawa, M.M.A., *TCSI Sept. 2021* 3631-3641
- Pilipovic, R.,** Bulic, P., and Lotric, U., A Two-Stage Operand Trimming Approximate Logarithmic Multiplier; *TCSI June 2021* 2535-2545
- Pinheiro, C.A.,** Olivera, F., and Petraglia, A., A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS; *TCSI Nov. 2021* 4810-4819
- Pistono, E.,** see Gomes, L., *TCSI Aug. 2021* 3158-3169
- Pistono, E.,** see Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
- Portal, J.,** see Laborieux, A., *TCSI Jan. 2021* 138-147
- Pour, F.L.,** see Salem, J.M., *TCSI Feb. 2021* 581-591
- Preissl, C.,** Preyler, P., Springer, A., and Huemer, M., Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers; *TCSI May 2021* 2234-2245
- Preyler, P.,** see Preissl, C., *TCSI May 2021* 2234-2245
- Prieto-Araujo, E.,** see Orellana, L., *TCSI Sept. 2021* 3758-3771
- Priyadarshini, N.,** and Sarkar, M., A 2e_{rms} -Temporal Noise CMOS Image Sensor With In-Pixel 1/f Noise Reduction and Conversion Gain Modulation for Low Light Imaging; *TCSI Jan. 2021* 185-195
- Prodromakis, T.,** see Maheshwari, S., *TCSI Dec. 2021* 4876-4888
- Prodromakis, T.,** see Maheshwari, S., *TCSI Dec. 2021* 4862-4875
- Psychogiou, D.,** see Gomez-Garcia, R., *TCSI May 2021* 2196-2209
- Pu, J.,** Goh, W.L., Nambiar, V.P., Wong, M.M., and Do, A.T., A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Congestion-Aware Router; *TCSI Dec. 2021* 5081-5094
- Puglisi, F.M.,** Zanotti, T., and Pavan, P., Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories; *TCSI Nov. 2021* 4433-4443
- Pun, K.,** see Wang, H., *TCSI March 2021* 1114-1122
- Pun, K.P.,** see Shi, E., *TCSI March 2021* 1045-1054
- Q**
- Qi, C.,** see Li, H., *TCSI Oct. 2021* 4170-4181
- Qi, H.,** see Wang, X., *TCSI Feb. 2021* 741-750
- Qi, W.,** Zong, G., and Zheng, W.X., Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model; *TCSI Feb. 2021* 786-796
- Qi, W.,** Hou, Y., Zong, G., and Ahn, C.K., Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications; *TCSI June 2021* 2665-2674
- Qian, H.,** Song, X., Li, D., and Wang, Z., Generalized Analog-to-Information Converter With Analysis Sparse Prior; *TCSI Sept. 2021* 3574-3586
- Qian, H.J.,** see Zhou, J., *TCSI June 2021* 2457-2469
- Qian, H.J.,** see Shu, Y., *TCSI June 2021* 2341-2353
- Qiao, F.,** see Li, Q., *TCSI May 2021* 1892-1905
- Qiao, J.,** see Wang, H., *TCSI Oct. 2021* 4337-4348
- Qiao, J.,** see Cui, Y., *TCSI Oct. 2021* 4360-4370
- Qiao, N.,** see Rubino, A., *TCSI Jan. 2021* 45-56
- Qin, W.,** see Wu, D., *TCSI Aug. 2021* 3211-3221
- Qin, Y.,** see Ming, X., *TCSI June 2021* 2354-2367
- Qiu, H.,** Jiang, Y., Shi, Y., Sakurai, T., and Takamiya, M., Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System; *TCSI Dec. 2021* 5182-5193
- Qiu, J.,** see Wang, C., *TCSI Jan. 2021* 387-395
- Qiu, J.,** see Liu, B., *TCSI Feb. 2021* 603-616
- Qiu, J.,** see Wang, H., *TCSI July 2021* 3058-3068
- Qiu, J.,** see Du, Y., *TCSI Oct. 2021* 4324-4336
- Qu, H.,** and Zhao, J., Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service; *TCSI June 2021* 2604-2615
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- Rabaey, K.,** see Molderez, T.R., *TCSI March 2021* 1068-1079
- Radogna, A.V.,** Capone, S., Franciosi, L., Siciliano, P.A., and D'Amico, S., A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors; *TCSI March 2021* 1123-1133
- Rafiq, S.,** Hazra, J., Liehr, M., Beckmann, K., Abedin, M., Pannu, J.S., Jha, S.K., and Cadby, N.C., Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays; *TCSI July 2021* 2900-2910
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- Ramirez-Angulo, J.,** see Beloso-Legarra, J., *TCSI Sept. 2021* 3562-3573
- Ranjandish, R.,** and Schmid, A., Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems; *TCSI April 2021* 1740-1749
- Ravezzi, L.,** Failure in Ring Oscillators With Capacitive Load; *TCSI Aug. 2021* 3388-3396
- Rawat, K.,** see Barthwal, A., *TCSI April 2021* 1421-1431
- Rawat, K.,** see Kumar, N., *TCSI July 2021* 2986-2997
- Rawat, M.,** see Tripathi, G.C., *TCSI Aug. 2021* 3508-3519
- Rawat, M.,** see Kumar, A., *TCSI Nov. 2021* 4636-4647
- Raychowdhury, A.,** see Karimzadeh, F., *TCSI Feb. 2021* 751-764
- Razavi, B.,** Jitter-Power Trade-Offs in PLLs; *TCSI April 2021* 1381-1387
- Recski, A.,** and Vekassy, A., Interconnection, Reciprocity and a Hierarchical Classification of Generalized Multiports; *TCSI Sept. 2021* 3682-3692
- Regev, D.,** Zolkov, E., Ginzberg, N., Keren, R., Shilo, S., Ezri, D., and Cohen, E., Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless; *TCSI Dec. 2021* 5168-5181
- Reis, R.,** see Abich, G., *TCSI Nov. 2021* 4772-4782
- Rekhi, A.S.,** So, E., Gural, A., and Arbabian, A., CRADLE: Combined RF/Acoustic Detection and Localization of Passive Tags; *TCSI June 2021* 2555-2568
- Ren, H.,** see Tian, K., *TCSI July 2021* 3012-3022
- Ren, J.,** see Cao, Y., *TCSI Feb. 2021* 641-654
- Ren, J.,** see Li, B., *TCSI Aug. 2021* 3279-3292
- Ren, Z.,** see Wang, Q., *TCSI Aug. 2021* 3436-3448
- Reuter, M.,** Pfau, J., Krauss, T.A., Becker, J., and Hofmann, K., From MOS-FETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology; *TCSI Jan. 2021* 114-125
- Reviriego, P.,** see Butt, U.M., *TCSI Aug. 2021* 3351-3362
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- Riccobene, G.,** see Vallicelli, E.A., *TCSI Jan. 2021* 3-13
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- Rivet, F.**, and Silveira, F., Guest Editorial: Special Issue Based on the 12th Edition of the Latin American Symposium on Circuits and Systems ; *TCSI Nov. 2021* 4760
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- Roberts, G.W.**, see Khan, R.A., *TCSI Oct. 2021* 4025-4037
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- Rossi-Aicardi, C.**, see Cabrera, C., *TCSI Aug. 2021* 3232-3241
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- Ru, T.**, see Wang, J., *TCSI Jan. 2021* 458-468
- Rubino, A.**, Livanelioglu, C., Qiao, N., Payvand, M., and Indiveri, G., Ultra-Low-Power FDSOI Neural Circuits for Extreme-Edge Neuromorphic Intelligence; *TCSI Jan. 2021* 45-56
- Rubino, R.**, Crovetti, P.S., and Musolino, F., FPGA-Based Relaxation D/A Converters With Parasitics-Induced Error Suppression and Digital Self-Calibration; *TCSI June 2021* 2494-2507
- Runge, M.**, see Wittenhagen, E., *TCSI Jan. 2021* 57-66
- Ryu, S.**, Park, C.Y., Kim, W., Son, S., and Kim, J., A Time-Based Pipelined ADC Using Integrate-and-Fire Multiplying-DAC ; *TCSI July 2021* 2876-2889
- S**
- Saadi, A.A.**, see Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
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- Sainz, L.**, see Orellana, L., *TCSI Sept. 2021* 3758-3771
- Sakurai, T.**, see Qiu, H., *TCSI Dec. 2021* 5182-5193
- Salamin, S.**, Zervakis, G., Chauhan, Y.S., Henkel, J., and Amrouch, H., PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits; *TCSI Oct. 2021* 4299-4309
- Salem, J.M.**, Pour, F.L., and Sam Ha, D., A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design; *TCSI Feb. 2021* 581-591
- Sam Ha, D.**, see Salem, J.M., *TCSI Feb. 2021* 581-591
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- Samori, C.**, see Avallone, L., *TCSI July 2021* 2775-2786
- Sanabria-Borbon, A.C.**, and Sanchez-Sinencio, E., Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters; *TCSI May 2021* 1841-1854
- Sanches, B.**, and Van Noije, W., An Optimized Radiation Tolerant Baseline Correction Filter for HEP Using AI Methodologies; *TCSI May 2021* 1789-1799
- Sanchez-Sinencio, E.**, see Sanabria-Borbon, A.C., *TCSI May 2021* 1841-1854
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- Santiccioli, A.**, see Avallone, L., *TCSI July 2021* 2775-2786
- Sanyal, A.**, see Tannirkulam Chandrasekaran, S., *TCSI March 2021* 1023-1033
- Sarajlic, M.**, Sheikhi, A., Liu, L., Sjoland, H., and Edfors, O., Power Scaling Laws for Radio Receiver Front Ends; *TCSI May 2021* 2183-2195
- Sarkar, M.**, see Priyadarshini, N., *TCSI Jan. 2021* 185-195
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- Sawan, M.**, see Akbari, M., *TCSI Aug. 2021* 3133-3146
- Saxena, V.**, Kumar, N., Singh, B., and Panigrahi, B.K., A Rapid Circle Centre-Line Concept-Based MPPT Algorithm for Solar Photovoltaic Energy Conversion Systems; *TCSI Feb. 2021* 940-949
- Schaltz, E.**, see Naseri, F., *TCSI March 2021* 1308-1318
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- Scheytt, J.C.**, see Wu, L., *TCSI Sept. 2021* 3668-3681
- Schmid, A.**, see Ranjandish, R., *TCSI April 2021* 1740-1749
- Schrappe, O.**, Andjelkovic, M., Breitenreiter, A., Zeidler, S., Balashov, A., and Krstic, M., Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops; *TCSI Nov. 2021* 4796-4809
- Schreurs, D.**, see Anjos, E.V.P., *TCSI Sept. 2021* 3927-3940
- Schrogendorfer, D.**, and Leitner, T., Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs; *TCSI May 2021* 1800-1813
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- Scott, J.**, see Ebrahimi, A., *TCSI July 2021* 2787-2799
- Scotti, G.**, see Centurelli, F., *TCSI Feb. 2021* 680-691
- Seidel, H.B.**, da Rosa, M.M.A., Paim, G., da Costa, E.A.C., Almeida, S.J.M., and Bampi, S., Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing; *TCSI May 2021* 1814-1826
- Seo, D.**, see Lee, E., *TCSI Aug. 2021* 3305-3316
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- Sethia, G.**, Nayak, S.K., and Majhi, S., An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer; *TCSI March 2021* 1319-1329
- Sferlazza, A.**, see Albea, C., *TCSI Aug. 2021* 3485-3494
- Shabani, A.**, Sabri, M., Khabbazan, B., and Timarchi, S., Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem; *TCSI March 2021* 1259-1268
- Shah, S.**, see Hasler, J., *TCSI Feb. 2021* 592-602
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- Shen, H.**, see Wang, J., *TCSI Jan. 2021* 458-468
- Shen, H.**, see Fan, S., *TCSI Feb. 2021* 856-867
- Shen, H.**, Xing, M., Xu, S., Basin, M.V., and Park, J.H., $H\infty$ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information; *TCSI Feb. 2021* 818-828
- Shen, J.**, see Maheshwari, S., *TCSI Dec. 2021* 4876-4888
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- Shen, L.**, see Ye, L., *TCSI Dec. 2021* 4821-4834
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- Shen, Y.**, see Pang, X., *TCSI Aug. 2021* 3495-3507
- Sheng, C.**, see Bi, X., *TCSI Nov. 2021* 4589-4602
- Shi, B.**, Chen, Y., Chen, K., Ju, J., Yu, Z., and Zhao, Z., Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter; *TCSI Nov. 2021* 4746-4759
- Shi, E.**, Tang, X., and Pun, K.P., A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection; *TCSI March 2021* 1045-1054
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- Shi, J.**, and Zhao, J., State Bumpless Transfer Control for a Class of Switched Descriptor Systems; *TCSI Sept. 2021* 3846-3856
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- Shi, K.**, see Wang, Y., *TCSI Aug. 2021* 3474-3484
- Shi, L.**, see Chen, M., *TCSI Feb. 2021* 950-962
- Shi, L.**, Thiagarajan, E., Singh, R., Hancioglu, E., Moon, U., and Temes, G.C., Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR; *TCSI Oct. 2021* 4001-4012
- Shi, P.**, see Fei, Y., *TCSI June 2021* 2616-2625
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- Shi, Y.,** and Sun, X., Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines; *TCSI May 2021 2171-2182*
- Shi, Y.,** see Qiu, H., *TCSI Dec. 2021 5182-5193*
- Shiba, K.,** Omori, T., Ueyoshi, K., Takamaeda-Yamazaki, S., Motomura, M., Hamada, M., and Kuroda, T., A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS ; *TCSI Feb. 2021 692-703*
- Shilo, S.,** see Regev, D., *TCSI Dec. 2021 5168-5181*
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- Shirane, A.,** see Liu, B., *TCSI Feb. 2021 603-616*
- Shu, F.,** and Zhai, J., Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems; *TCSI Feb. 2021 808-817*
- Shu, F.,** and Zhai, J., Global Event-Triggered Output Feedback Stabilization for a Class of Nonlinear Time-Delay Systems; *TCSI Oct. 2021 4371-4380*
- Shu, Y.,** Qian, H.J., and Luo, X., A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator; *TCSI June 2021 2341-2353*
- Shu, Z.,** see Yin, P., *TCSI July 2021 2925-2935*
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- Simic, D.,** and Reynaert, P., Analysis and Design of Lossy Capacitive Over-Neutralization Technique for Amplifiers Operating Near f_{MAX} ; *TCSI May 2021 1945-1955*
- Singh, A.,** Lebdeh, M.A., Gebregiorgis, A., Bishnoi, R., Joshi, R.V., and Hamdioui, S., SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures; *TCSI May 2021 1917-1930*
- Singh, B.,** see Saxena, V., *TCSI Feb. 2021 940-949*
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- Song, J.,** Wang, Y., Guo, M., Ji, X., Cheng, K., Hu, Y., Tang, X., Wang, R., and Huang, R., TD-SRAM: Time-Domain-Based In-Memory Computing Macro for Binary Neural Networks; *TCSI Aug. 2021 3377-3387*
- Song, M.,** see Zhang, Y., *TCSI April 2021 1553-1566*
- Song, S.,** see Song, X., *TCSI Jan. 2021 363-375*
- Song, W.,** see Ji, H., *TCSI March 2021 1330-1341*
- Song, W.,** see Chen, H., *TCSI March 2021 1231-1244*
- Song, W.,** see Pang, X., *TCSI Aug. 2021 3495-3507*
- Song, X.,** Man, J., Song, S., and Ahn, C.K., Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms; *TCSI Jan. 2021 363-375*
- Song, X.,** see Qian, H., *TCSI Sept. 2021 3574-3586*
- Song, X.,** Man, J., and Ahn, C.K., Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement; *TCSI Sept. 2021 3869-3880*
- Souza, A.A.L.,** see Gomes, L., *TCSI Aug. 2021 3158-3169*
- Specht, T.,** see Fragasse, R., *TCSI May 2021 1827-1840*
- Springer, A.,** see Preissl, C., *TCSI May 2021 2234-2245*
- Sreeram, V.,** see Wang, J., *TCSI Jan. 2021 458-468*
- Staszewski, R.B.,** see Li, C., *TCSI May 2021 1881-1891*
- Staszewski, R.B.,** see Zhang, F., *TCSI May 2021 1855-1868*
- Staszewski, R.B.,** see Hu, S., *TCSI June 2021 2317-2328*
- Stathopoulos, S.,** see Maheshwari, S., *TCSI Dec. 2021 4876-4888*
- Stathopoulos, S.,** see Maheshwari, S., *TCSI Dec. 2021 4862-4875*
- Stratigopoulos, H.,** see Pavlidis, A., *TCSI June 2021 2580-2593*
- Strukov, D.B.,** see Fahimi, Z., *TCSI Oct. 2021 4090-4101*
- Studer, C.,** see Zhang, C., *TCSI Dec. 2021 5049-5060*
- Su, H.,** see Li, S., *TCSI Aug. 2021 3460-3473*
- Su, H.,** see Ye, Y., *TCSI Sept. 2021 3881-3889*
- Su, H.,** see Wang, X., *TCSI Dec. 2021 5145-5155*
- Su, X.,** see Yang, Y., *TCSI Jan. 2021 434-443*
- Su, X.,** see Wang, C., *TCSI Dec. 2021 5134-5144*
- Sun, G.,** see Lin, X., *TCSI Sept. 2021 3890-3900*
- Sun, H.,** see Li, B., *TCSI Aug. 2021 3279-3292*
- Sun, J.,** see Xu, Y., *TCSI May 2021 2246-2256*
- Sun, J.,** see Fu, H., *TCSI Nov. 2021 4495-4507*
- Sun, L.,** see Pan, D., *TCSI March 2021 1091-1101*
- Sun, N.,** see Wang, C., *TCSI June 2021 2714-2724*
- Sun, N.,** see Hong, S., *TCSI Nov. 2021 4576-4588*
- Sun, S.,** see Wang, C., *TCSI Oct. 2021 4182-4193*
- Sun, W.,** see Chen, M., *TCSI Feb. 2021 950-962*
- Sun, W.,** see Ji, L., *TCSI July 2021 2841-2849*
- Sun, X.,** see Duan, N., *TCSI April 2021 1610-1623*
- Sun, X.,** see Shi, Y., *TCSI May 2021 2171-2182*
- Sun, Y.,** see Lin, H., *TCSI Aug. 2021 3397-3410*
- Sun, Y.,** see Chen, L., *TCSI Nov. 2021 4616-4625*
- Sun, Z.,** Liu, H., Huang, H., Tang, D., Xu, D., Kaneko, T., Li, Z., Pang, J., Wu, R., Deng, W., Shirane, A., and Okada, K., A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuity With T/R Switch; *TCSI Jan. 2021 196-209*
- Sun, Z.,** see Mannocci, P., *TCSI Dec. 2021 4889-4899*
- Sun, Z.,** see Wang, S., *TCSI Dec. 2021 4900-4909*
- Sung, E.,** see Jiang, H., *TCSI April 2021 1432-1443*

T

- Taghadosi, M.,** and Kassiri, H., A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers; *TCSI Jan. 2021 510-523*
- Taghipour, Z.,** see Fragasse, R., *TCSI May 2021 1827-1840*
- Tajalli, A.,** Power-Speed Trade-Offs in Design of Scaled FET Circuits Using C/I_{DS} Methodology; *TCSI Feb. 2021 631-640*
- Takamaeda-Yamazaki, S.,** see Shiba, K., *TCSI Feb. 2021 692-703*
- Takamiya, M.,** see Qiu, H., *TCSI Dec. 2021 5182-5193*
- Tamerisit, K.,** see Behbahani, F., *TCSI Dec. 2021 5108-5119*
- Tan, C.,** Li, Y., Cheng, X., Han, J., and Zeng, X., General Efficient TMR for Combinational Circuit Hardening Against Soft Errors and Improved Multi-Objective Optimization Framework; *TCSI July 2021 3044-3057*
- Tan, M.,** see Xie, Z., *TCSI March 2021 1366-1376*
- Tan, S.,** see Tao, T., *TCSI May 2021 1906-1916*
- Tan, T.,** Chen, K., Lin, Q., Jiang, Y., Yuan, L., and Zhao, Z., Impedance Shaping Control Strategy for Wireless Power Transfer System Based on Dynamic Small-Signal Analysis; *TCSI March 2021 1354-1365*
- Tan, W.,** see Wang, A., *TCSI June 2021 2508-2521*
- Tan, Z.,** see Wang, Z., *TCSI March 2021 1160-1170*
- Tan, Z.,** see Ye, L., *TCSI Dec. 2021 4821-4834*
- Tang, A.,** Huang, R., Virbila, G., and Chang, M.F., Self-Synchronized DS/SS With High Spread Factors for Robust Millimeter-Wave Datalinks; *TCSI Sept. 2021 3941-3950*
- Tang, D.,** see Sun, Z., *TCSI Jan. 2021 196-209*
- Tang, F.,** see Yin, P., *TCSI July 2021 2925-2935*
- Tang, X.,** see Shi, E., *TCSI March 2021 1045-1054*
- Tang, X.,** see Song, J., *TCSI Aug. 2021 3377-3387*
- Tang, X.,** see Liu, S., *TCSI Oct. 2021 4102-4115*

- Tang, Y.,** see Zhang, W., *TCSI Feb. 2021* 776-785
- Tang, Y.,** see Hua, L., *TCSI April 2021* 1599-1609
- Tannirkulam Chandrasekaran, S.,** Jayaraj, A., Elkori Ghantala Karnam, V., Banerjee, I., and Sanyal, A., Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification; *TCSI March 2021* 1023-1033
- Tantawy, R.,** see Fragasse, R., *TCSI May 2021* 1827-1840
- Tao, T.,** Ma, H., Chen, Q., Gu, Z., Jin, H., Ahmed, M., Tan, S., Wang, A., Liu, E., and Li, E., Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme; *TCSI May 2021* 1906-1916
- Tashakor, N.,** see Naseri, F., *TCSI March 2021* 1308-1318
- Taskin, B.,** see Kuttappa, R., *TCSI April 2021* 1636-1645
- Tavares, Y.A.,** and Lee, M., A Foreground Calibration for M-Channel Time-Interleaved Analog-to-Digital Converters Based on Genetic Algorithm; *TCSI April 2021* 1444-1457
- Taylor, C.,** see Fragasse, R., *TCSI May 2021* 1827-1840
- Tehranipoor, M.M.,** see Park, B., *TCSI Nov. 2021* 4700-4709
- Temes, G.C.,** see Shi, L., *TCSI Oct. 2021* 4001-4012
- Teng, C.,** and Wu, A., A 7.8-13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support; *TCSI May 2021* 1956-1965
- Tesolin, F.,** see Karman, S., *TCSI March 2021* 989-997
- Tetzlaff, R.,** see Weiher, M., *TCSI May 2021* 2082-2095
- Tetzlaff, R.,** see Chawa, M.M.A., *TCSI Sept. 2021* 3631-3641
- Tetzlaff, R.,** see Messaris, I., *TCSI Dec. 2021* 4979-4992
- Tetzlaff, R.,** see Kang, S.M., *TCSI Dec. 2021* 4837-4850
- Thakker, R.,** see Jha, C.K., *TCSI Aug. 2021* 3337-3350
- Thiagarajan, E.,** see Shi, L., *TCSI Oct. 2021* 4001-4012
- Tian, E.,** see Zhao, M., *TCSI Jan. 2021* 426-433
- Tian, E.,** see Wei, B., *TCSI Oct. 2021* 4381-4392
- Tian, E.,** see Li, T., *TCSI Dec. 2021* 5156-5167
- Tian, J.,** Wu, B., and Wang, Z., High-Speed FPGA Implementation of SIKE Based on an Ultra-Low-Latency Modular Multiplier; *TCSI Sept. 2021* 3719-3731
- Tian, K.,** Grebogi, C., and Ren, H., Chaos Generation With Impulse Control: Application to Non-Chaotic Systems and Circuit Design; *TCSI July 2021* 3012-3022
- Tian, Y.,** see Wittenhagen, E., *TCSI Jan. 2021* 57-66
- Timarchi, S.,** see Shabani, A., *TCSI March 2021* 1259-1268
- Toledo, P.,** Crovetti, P., Aiello, O., and Alioto, M., Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting; *TCSI Sept. 2021* 3693-3706
- Tong, S.,** see Liu, L., *TCSI Sept. 2021* 3901-3912
- Torfs, G.,** see Verbeke, M., *TCSI Jan. 2021* 469-482
- Tran, T.H.,** Pham, H.L., Phan, T.D., and Nakashima, Y., BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks; *TCSI Oct. 2021* 4245-4258
- Tran-Dinh, T.,** Pham, H.M., Pham-Nguyen, L., Lee, S., and Le, H., Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators; *TCSI Jan. 2021* 103-113
- Trefzer, M.A.,** see Cao, L., *TCSI Nov. 2021* 4660-4671
- Trifiletti, A.,** see Centurelli, F., *TCSI Feb. 2021* 680-691
- Tripathi, G.C.,** and Rawat, M., Baseband Fusion Technique for Filter-Less Wideband Transmitters; *TCSI Aug. 2021* 3508-3519
- Trivedi, A.R.,** see Nasrin, S., *TCSI May 2021* 1966-1978
- Truhachev, D.,** El-Sankary, K., Karami, A., Zokaei, A., and Li, S., Efficient Implementation of 400 Gbps Optical Communication FEC; *TCSI Jan. 2021* 496-509
- Tsai, K.,** Chang, Y., Wang, C., and Chiang, C., Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme; *TCSI Aug. 2021* 3328-3336
- Tsai, T.,** see Hussain, M.A., *TCSI April 2021* 1507-1519
- Tsai, T.,** see Li, C., *TCSI May 2021* 1881-1891
- Turunen, M.,** see Campo, P.P., *TCSI Jan. 2021* 336-349
- Tymchenko, M.,** Nagulu, A., Krishnaswamy, H., and Alu, A., Universal Frequency-Domain Analysis of N-Path Networks; *TCSI Feb. 2021* 569-580
- Tzounas, G.,** Sipahi, R., and Milano, F., Damping Power System Electromechanical Oscillations Using Time Delays; *TCSI June 2021* 2725-2735
- U**
- Udayanga, N.,** see Malavipathirana, H., *TCSI Aug. 2021* 3363-3376
- Ueyoshi, K.,** see Shiba, K., *TCSI Feb. 2021* 692-703
- Ullah, A.,** see Butt, U.M., *TCSI Aug. 2021* 3351-3362
- V**
- Vahdat, S.,** Kamal, M., Afzali-Kusha, A., and Pedram, M., Loading-Aware Reliability Improvement of Ultra-Low Power Memristive Neural Networks; *TCSI Aug. 2021* 3411-3421
- Vahdat, S.,** Kamal, M., Afzali-Kusha, A., and Pedram, M., Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities; *TCSI Oct. 2021* 4310-4323
- Vakili, S.,** see Ahmadi, M., *TCSI Aug. 2021* 3184-3196
- Valkama, M.,** see Campo, P.P., *TCSI Jan. 2021* 336-349
- Valle, M.,** see Younes, H., *TCSI Oct. 2021* 4232-4244
- Vallicelli, E.A.,** Baschiroto, A., Lehrack, S., Assmann, W., Parodi, K., Viola, S., Riccobene, G., and De Matteis, M., 22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification; *TCSI Jan. 2021* 3-13
- van Liempd, B.,** see Hershberg, B., *TCSI July 2021* 2813-2826
- Van Noije, W.,** see Sanches, B., *TCSI May 2021* 1789-1799
- Vandenbosch, G.A.E.,** see Anjos, E.V.P., *TCSI Sept. 2021* 3927-3940
- Vanderveken, F.,** see Mahmoud, A.N., *TCSI Jan. 2021* 536-549
- Vasudevan, V.,** see Koneru, B.N.G., *TCSI Oct. 2021* 4287-4298
- Vekassy, A.,** see Recski, A., *TCSI Sept. 2021* 3682-3692
- Velez, P.,** see Ebrahimi, A., *TCSI July 2021* 2787-2799
- Verbeke, M.,** Torfs, G., and Rombouts, P., The Truth About 2-Level Transition Elimination in Bang-Bang PAM-4 CDRs; *TCSI Jan. 2021* 469-482
- Verhelst, M.,** see Molderez, T.R., *TCSI March 2021* 1068-1079
- Verma, N.,** see Zhang, B., *TCSI April 2021* 1532-1542
- Vianello, E.,** see Laborieux, A., *TCSI Jan. 2021* 138-147
- Villamizar, D.A.,** Muratore, D.G., Wieser, J.B., and Murmann, B., An 800 nW Switched-Capacitor Feature Extraction Filterbank for Sound Classification; *TCSI April 2021* 1578-1588
- Vincent, L.,** see Margalef-Rovira, M., *TCSI Aug. 2021* 3170-3183
- Viola, S.,** see Vallicelli, E.A., *TCSI Jan. 2021* 3-13
- Virbila, G.,** see Tang, A., *TCSI Sept. 2021* 3941-3950
- Vitrenko, O.,** see Biccario, G.E., *TCSI Nov. 2021* 4626-4635
- W**
- Walling, J.S.,** see Zhang, F., *TCSI May 2021* 1855-1868
- Wan, M.,** see Zhang, Y., *TCSI April 2021* 1553-1566
- Wan, X.,** see Xu, Y., *TCSI Oct. 2021* 4278-4286
- Wan, Z.,** see Xu, X., *TCSI Sept. 2021* 3611-3620
- Wang, A.,** see Tao, T., *TCSI May 2021* 1906-1916
- Wang, A.,** Tan, W., Wen, Y., and Lao, Y., NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs; *TCSI June 2021* 2508-2521
- Wang, B.,** see Zeng, Z., *TCSI Sept. 2021* 3587-3597
- Wang, B.,** see Lin, T.N., *TCSI Oct. 2021* 3991-4000
- Wang, C.,** Chen, X., Cao, J., Qiu, J., Liu, Y., and Luo, Y., Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks; *TCSI Jan. 2021* 387-395
- Wang, C.,** see Wu, J., *TCSI June 2021* 2522-2534
- Wang, C.,** Lu, Y., Sun, N., and Martins, R.P., A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution; *TCSI June 2021* 2714-2724
- Wang, C.,** see Agrawal, A., *TCSI June 2021* 2281-2294
- Wang, C.,** see Lin, H., *TCSI Aug. 2021* 3397-3410
- Wang, C.,** see Tsai, K., *TCSI Aug. 2021* 3328-3336
- Wang, C.,** see Yi, X., *TCSI Sept. 2021* 3537-3550
- Wang, C.,** see Nan, G., *TCSI Sept. 2021* 3707-3718

- Wang, C.**, Ding, Y., Chiu, C., Huang, C., Cheng, Y., Sun, S., Cheng, C., and Kuo, H., Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA; *TCSI Oct. 2021* 4182-4193
- Wang, C.**, *see Azmat, R.*, *TCSI Nov. 2021* 4545-4555
- Wang, C.**, *see Fu, H.*, *TCSI Nov. 2021* 4495-4507
- Wang, C.**, Li, R., Su, X., and Shi, P., Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter; *TCSI Dec. 2021* 5134-5144
- Wang, G.**, *see Zhang, Y.*, *TCSI March 2021* 1193-1205
- Wang, G.**, *see Xu, Y.*, *TCSI May 2021* 2246-2256
- Wang, G.**, *see Jin, P.*, *TCSI Nov. 2021* 4419-4432
- Wang, H.**, Basak, D., Zhang, Y., and Pun, K., A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers; *TCSI March 2021* 1114-1122
- Wang, H.**, *see Chen, M.*, *TCSI April 2021* 1716-1726
- Wang, H.**, Xu, K., and Qiu, J., Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems; *TCSI July 2021* 3058-3068
- Wang, H.**, *see Li, B.*, *TCSI Aug. 2021* 3279-3292
- Wang, H.**, Xu, K., Liu, P.X., and Qiao, J., Adaptive Fuzzy Fast Finite-Time Dynamic Surface Tracking Control for Nonlinear Systems; *TCSI Oct. 2021* 4337-4348
- Wang, J.**, Ru, T., Xia, J., Shen, H., and Sreeram, V., Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval; *TCSI Jan. 2021* 458-468
- Wang, J.**, *see Zhang, Y.*, *TCSI March 2021* 1193-1205
- Wang, J.**, Yu, S., Zhang, X., Wei, Z., Jiang, N., Chen, W., and Du, E., Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Parallelized SiC MOSFETs; *TCSI May 2021* 2107-2120
- Wang, J.**, Xu, J., and Wang, S., Lattice Trajectory Piecewise Linear Method for the Simulation of Diode Circuits; *TCSI May 2021* 2069-2081
- Wang, J.**, *see Zhu, Y.*, *TCSI Dec. 2021* 5007-5017
- Wang, J.**, *see Maheshwari, S.*, *TCSI Dec. 2021* 4876-4888
- Wang, J.**, *see Maheshwari, S.*, *TCSI Dec. 2021* 4862-4875
- Wang, K.**, *see Wang, Y.*, *TCSI May 2021* 2257-2270
- Wang, K.X.**, *see Xie, Z.*, *TCSI March 2021* 1366-1376
- Wang, L.**, *see Han, Y.*, *TCSI July 2021* 2962-2975
- Wang, L.**, *see Lin, J.*, *TCSI July 2021* 3089-3102
- Wang, L.**, *see Azmat, R.*, *TCSI Nov. 2021* 4545-4555
- Wang, L.**, Jiang, S., Ge, M., Hu, C., and Hu, J., Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application; *TCSI Dec. 2021* 4957-4969
- Wang, L.**, *see Zhou, Y.*, *TCSI Dec. 2021* 4851-4861
- Wang, Q.**, *see Zhang, X.*, *TCSI March 2021* 1297-1307
- Wang, Q.**, Dong, X., Yu, J., Lu, J., and Ren, Z., Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs; *TCSI Aug. 2021* 3436-3448
- Wang, Q.**, Yu, S., Guyeux, C., and Wang, W., Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm; *TCSI Sept. 2021* 3794-3807
- Wang, Q.**, *see Liu, Y.*, *TCSI Oct. 2021* 4194-4206
- Wang, R.**, *see Song, J.*, *TCSI Aug. 2021* 3377-3387
- Wang, S.**, *see Liu, T.*, *TCSI Feb. 2021* 904-917
- Wang, S.**, *see Wang, J.*, *TCSI May 2021* 2069-2081
- Wang, S.**, Sun, Z., Liu, Y., Bao, S., Cai, Y., Ielmini, D., and Huang, R., Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays; *TCSI Dec. 2021* 4900-4909
- Wang, S.H.**, Zheng, S.Y., Leung, K.W., and Xia, M.H., A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting; *TCSI Nov. 2021* 4556-4565
- Wang, T.**, Zhang, B., Yuan, D., and Zhang, Y., Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays; *TCSI Nov. 2021* 4520-4533
- Wang, W.**, Han, Z., Liu, K., and Lu, J., Distributed Adaptive Resilient Formation Control of Uncertain Nonholonomic Mobile Robots Under Deception Attacks; *TCSI Sept. 2021* 3822-3835
- Wang, W.**, *see Peng, X.*, *TCSI Sept. 2021* 3621-3630
- Wang, W.**, *see Zhang, Z.*, *TCSI Sept. 2021* 3598-3610
- Wang, W.**, *see Wang, Q.*, *TCSI Sept. 2021* 3794-3807
- Wang, X.**, Zhou, P., Eshraghian, J.K., Lin, C., Iu, H.H., Chang, T., and Kang, S., High-Density Memristor-CMOS Ternary Logic Family; *TCSI Jan. 2021* 264-274
- Wang, X.**, Liang, H., Wang, Y., Yao, L., Guo, Y., Yi, M., Huang, Z., Qi, H., and Lu, Y., High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure; *TCSI Feb. 2021* 741-750
- Wang, X.**, *see Yin, P.*, *TCSI July 2021* 2925-2935
- Wang, X.**, *see Zhang, S.*, *TCSI Dec. 2021* 4945-4956
- Wang, X.**, Su, H., and Jiang, G., Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks; *TCSI Dec. 2021* 5145-5155
- Wang, Y.**, *see Guo, B.*, *TCSI Jan. 2021* 224-237
- Wang, Y.**, *see Lyu, F.*, *TCSI Feb. 2021* 715-727
- Wang, Y.**, *see Lyu, F.*, *TCSI Feb. 2021* 715-727
- Wang, Y.**, *see Wang, X.*, *TCSI Feb. 2021* 741-750
- Wang, Y.**, *see Pan, D.*, *TCSI March 2021* 1091-1101
- Wang, Y.**, *see Pan, D.*, *TCSI March 2021* 1091-1101
- Wang, Y.**, *see Wang, Z.*, *TCSI March 2021* 1160-1170
- Wang, Y.**, *see Wang, Z.*, *TCSI April 2021* 1624-1635
- Wang, Y.**, *see Chen, J.*, *TCSI April 2021* 1520-1531
- Wang, Y.**, Yuan, Y., Li, G., Ye, Y., Wang, K., and Liang, J., A T-Type Switched-Capacitor Multilevel Inverter With Low Voltage Stress and Self-Balancing; *TCSI May 2021* 2257-2270
- Wang, Y.**, *see Li, Q.*, *TCSI May 2021* 1892-1905
- Wang, Y.**, *see Wu, J.*, *TCSI May 2021* 2271-2279
- Wang, Y.**, Lu, J., Zheng, W.X., and Shi, K., Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy; *TCSI Aug. 2021* 3474-3484
- Wang, Y.**, *see Song, J.*, *TCSI Aug. 2021* 3377-3387
- Wang, Y.**, *see Min, F.*, *TCSI Oct. 2021* 4207-4220
- Wang, Y.**, *see Min, F.*, *TCSI Oct. 2021* 4207-4220
- Wang, Y.**, *see Wu, Y.*, *TCSI Nov. 2021* 4735-4745
- Wang, Y.**, *see Ye, L.*, *TCSI Dec. 2021* 4821-4834
- Wang, Z.**, *see Cui, H.*, *TCSI Feb. 2021* 879-891
- Wang, Z.**, Ye, L., Huang, Q., Du, K., Tan, Z., Wang, Y., and Huang, R., Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process; *TCSI March 2021* 1160-1170
- Wang, Z.**, *see Han, J.*, *TCSI March 2021* 1012-1022
- Wang, Z.**, Ye, L., Huang, Q., Wang, Y., and Huang, R., Re-Assessment of Steep-Slope Device Design From a Circuit-Level Perspective Using Novel Evaluation Criteria and Model-Less Method; *TCSI April 2021* 1624-1635
- Wang, Z.**, *see Ming, X.*, *TCSI June 2021* 2354-2367
- Wang, Z.**, *see Liang, S.*, *TCSI July 2021* 2911-2924
- Wang, Z.**, *see Xie, X.*, *TCSI July 2021* 2936-2949
- Wang, Z.**, *see Nan, G.*, *TCSI Sept. 2021* 3707-3718
- Wang, Z.**, *see Nan, G.*, *TCSI Sept. 2021* 3707-3718
- Wang, Z.**, *see Xu, X.*, *TCSI Sept. 2021* 3611-3620
- Wang, Z.**, *see Qian, H.*, *TCSI Sept. 2021* 3574-3586
- Wang, Z.**, *see Tian, J.*, *TCSI Sept. 2021* 3719-3731
- Wang, Z.**, *see Ye, L.*, *TCSI Dec. 2021* 4821-4834
- Wei, B.**, Tian, E., Zhang, T., and Zhao, X., Probabilistic-Constrained H_∞ Tracking Control for a Class of Stochastic Nonlinear Systems Subject to DoS Attacks and Measurement Outliers; *TCSI Oct. 2021* 4381-4392
- Wei, H.**, *see Ye, Y.*, *TCSI Sept. 2021* 3881-3889
- Wei, J.**, *see Xie, X.*, *TCSI July 2021* 2936-2949
- Wei, L.**, *see Lu, J.*, *TCSI July 2021* 2976-2985
- Wei, S.**, *see Zhu, Y.*, *TCSI March 2021* 1146-1159
- Wei, Z.**, *see Wang, J.*, *TCSI May 2021* 2107-2120
- Weiher, M.**, Herzig, M., Tetzlaff, R., Ascoli, A., Mikolajick, T., and Slesazeck, S., Improved Vertex Coloring With NbO_x Memristor-Based Oscillatory Networks; *TCSI May 2021* 2082-2095
- Weltin-Wu, C.**, *see Alvarez-Fontecilla, E.*, *TCSI March 2021* 965-974
- Wen, G.**, *see Chen, Z.*, *TCSI Jan. 2021* 376-386
- Wen, H.**, *see Zhan, M.*, *TCSI June 2021* 2688-2701

- Wen, S.,** see Wu, A., *TCSI Nov. 2021* 4508-4519
Wen, Y., see Wang, A., *TCSI June 2021* 2508-2521
Weng, S., see Chen, J., *TCSI Feb. 2021* 918-928
Wieser, J.B., see Villamizar, D.A., *TCSI April 2021* 1578-1588
Williams, R.S., see Messaris, I., *TCSI Dec. 2021* 4979-4992
Williams, R.S., see Zoppo, G., *TCSI Dec. 2021* 4910-4923
Williams, R.S., see Yi, S., *TCSI Dec. 2021* 4970-4978
Wittenhagen, E., Runge, M., Lotfi, N., Ghafarian, H., Tian, Y., and Gerfers, F., Advanced Mixed Signal Concepts Exploiting the Strong Body-Bias Effect in CMOS 22FDX®; *TCSI Jan. 2021* 57-66
Wong, M.M., see Pu, J., *TCSI Dec. 2021* 5081-5094
Woo, J., see Choi, J., *TCSI July 2021* 2863-2875
Wright, D., see Patel, D., *TCSI Aug. 2021* 3265-3278
Wu, A., see Teng, C., *TCSI May 2021* 1956-1965
Wu, A., Chen, Y., Zhu, S., and Wen, S., Positivity and Stability of Cohen-Grossberg-Type Memristor Neural Networks With Unbounded Delays; *TCSI Nov. 2021* 4508-4519
Wu, B., see Pan, D., *TCSI March 2021* 1091-1101
Wu, B., see Li, C., *TCSI June 2021* 2651-2664
Wu, B., see Nan, G., *TCSI Sept. 2021* 3707-3718
Wu, B., see Tian, J., *TCSI Sept. 2021* 3719-3731
Wu, D., Li, Y., Xue, Q., Qin, W., and Hu, B., Balanced and Unbalanced Duplexers Using Common Oval Dielectric Resonators; *TCSI Aug. 2021* 3211-3221
Wu, F., see Liao, Z., *TCSI July 2021* 3103-3113
Wu, J., Bie, L., Kong, W., Gao, P., and Wang, Y., Multi-Frequency Multi-Amplitude Superposition Modulation Method With Phase Shift Optimization for Single Inverter of Wireless Power Transfer System; *TCSI May 2021* 2271-2279
Wu, J., Zhan, Y., Peng, Z., Ji, X., Yu, G., Zhao, R., and Wang, C., Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC; *TCSI June 2021* 2522-2534
Wu, J., see Liu, P., *TCSI Nov. 2021* 4444-4455
Wu, K., see Guo, L., *TCSI Dec. 2021* 5194-5205
Wu, L., see Hu, Z., *TCSI June 2021* 2594-2603
Wu, L., see Hu, Z., *TCSI Sept. 2021* 3857-3868
Wu, L., and Scheytt, J.C., Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS; *TCSI Sept. 2021* 3668-3681
Wu, M., see Ye, L., *TCSI Dec. 2021* 4821-4834
Wu, Q., see Zhu, Q., *TCSI Sept. 2021* 3951-3964
Wu, R., see Sun, Z., *TCSI Jan. 2021* 196-209
Wu, W., see Zhu, Y., *TCSI Dec. 2021* 5007-5017
Wu, X., see Xu, Y., *TCSI April 2021* 1589-1598
Wu, X., see Liu, C., *TCSI Sept. 2021* 3772-3783
Wu, X., see Xu, Y., *TCSI Oct. 2021* 4278-4286
Wu, Y., see Duan, N., *TCSI April 2021* 1610-1623
Wu, Y., Shen, B., Ahn, C.K., and Li, W., Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks; *TCSI June 2021* 2639-2650
Wu, Y., and McAllister, J., Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications; *TCSI June 2021* 2675-2687
Wu, Y., see Ye, Y., *TCSI Sept. 2021* 3881-3889
Wu, Y., Wang, Y., Liu, J., and Xu, Y., Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy; *TCSI Nov. 2021* 4735-4745
Wu, Z., see Xu, Y., *TCSI May 2021* 2246-2256
Wu, Z., see Zhang, C., *TCSI Dec. 2021* 5049-5060
- X**
- Xia, C.,** see Liao, Z., *TCSI Feb. 2021* 929-939
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Xia, J., see Wang, J., *TCSI Jan. 2021* 458-468
Xia, M.H., see Wang, S.H., *TCSI Nov. 2021* 4556-4565
Xia, Y., see Yin, P., *TCSI July 2021* 2925-2935
- Xia, Z.,** Chen, J., Huang, Q., Luo, J., and Hu, J., Neural Synaptic Plasticity-Inspired Computing: A High Computing Efficient Deep Convolutional Neural Network Accelerator; *TCSI Feb. 2021* 728-740
Xiao, B., see Han, T., *TCSI Oct. 2021* 4393-4402
Xiao, J., see Li, S., *TCSI April 2021* 1543-1552
Xiao, L., see Li, H., *TCSI Oct. 2021* 4170-4181
Xie, C., see Xu, Y., *TCSI April 2021* 1589-1598
Xie, C., see Xu, Y., *TCSI Oct. 2021* 4278-4286
Xie, R., Yin, J., and Han, J., DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks; *TCSI Dec. 2021* 5095-5107
Xie, X., Lin, J., Wang, Z., and Wei, J., An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks; *TCSI July 2021* 2936-2949
Xie, Z., and Zhang, X., Fast Nested Key Equation Solvers for Generalized Integrated Interleaved Decoder; *TCSI Jan. 2021* 483-495
Xie, Z., Ye, K., Wang, K.X., Cheng, Q., and Tan, M., A Time-Division-Multiplexed Clocked-Analog Low-Dropout Regulator; *TCSI March 2021* 1366-1376
Ximenes, A.R., see Li, C., *TCSI May 2021* 1881-1891
Xing, M., see Shen, H., *TCSI Feb. 2021* 818-828
Xu, C., see Lin, H., *TCSI Aug. 2021* 3397-3410
Xu, D., see Sun, Z., *TCSI Jan. 2021* 196-209
Xu, D., see Yang, Y., *TCSI Jan. 2021* 434-443
Xu, H., see Yang, Z., *TCSI April 2021* 1472-1480
Xu, H., see Min, F., *TCSI Oct. 2021* 4207-4220
Xu, J., see Wang, J., *TCSI May 2021* 2069-2081
Xu, J., Armstrong, M., and Al-Greer, M., Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach; *TCSI Aug. 2021* 3520-3533
Xu, J., see Huang, B., *TCSI Nov. 2021* 4672-4685
Xu, K., see Wang, H., *TCSI July 2021* 3058-3068
Xu, K., see Wang, H., *TCSI Oct. 2021* 4337-4348
Xu, Q., see Bi, X., *TCSI Nov. 2021* 4589-4602
Xu, S., see Shen, H., *TCSI Feb. 2021* 818-828
Xu, S., see Chen, M., *TCSI Feb. 2021* 950-962
Xu, W., see Chen, Z., *TCSI Jan. 2021* 376-386
Xu, X., see Lyu, F., *TCSI Feb. 2021* 715-727
Xu, X., Wan, Z., Rhee, W., and Wang, Z., A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation; *TCSI Sept. 2021* 3611-3620
Xu, Y., Wu, X., Mao, B., Lu, J., and Xie, C., Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks; *TCSI April 2021* 1589-1598
Xu, Y., Sun, J., Wang, G., and Wu, Z., Dynamic Triggering Mechanisms for Distributed Adaptive Synchronization Control and Its Application to Circuit Systems; *TCSI May 2021* 2246-2256
Xu, Y., Wu, X., Wan, X., and Xie, C., Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation; *TCSI Oct. 2021* 4278-4286
Xu, Y., see Wu, Y., *TCSI Nov. 2021* 4735-4745
Xue, C., see Jhang, C., *TCSI May 2021* 1773-1786
Xue, D., DeBrunner, L.S., and DeBrunner, V., Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform; *TCSI May 2021* 2051-2059
Xue, Q., see Wu, D., *TCSI Aug. 2021* 3211-3221
- Y**
- Yan, H.,** see Fan, S., *TCSI Feb. 2021* 856-867
Yan, H., see Boljanovic, V., *TCSI April 2021* 1727-1739
Yan, H., see Han, T., *TCSI Oct. 2021* 4393-4402
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Yan, J., see Zhang, J., *TCSI Feb. 2021* 829-841
Yang, B., see Zhu, Y., *TCSI March 2021* 1146-1159
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Yang, H., see Li, Q., *TCSI May 2021* 1892-1905
Yang, J., see Jiang, Y., *TCSI April 2021* 1681-1692

- Yang, M.,** see Li, Q., *TCSI May 2021* 1892-1905
- Yang, M.,** see Zhao, D., *TCSI Oct. 2021* 3977-3990
- Yang, M.,** see Zhao, D., *TCSI Oct. 2021* 4413
- Yang, Q.,** and Li, H., BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices; *TCSI March 2021* 1134-1145
- Yang, R.,** see Lin, D., *TCSI March 2021* 1034-1044
- Yang, S.,** see Peng, X., *TCSI Sept. 2021* 3621-3630
- Yang, Y.,** Xu, D., Ma, T., and Su, X., Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems ; *TCSI Jan. 2021* 434-443
- Yang, Y.,** He, W., and Han, Q., Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays; *TCSI May 2021* 2148-2157
- Yang, Y.,** see Li, C., *TCSI June 2021* 2651-2664
- Yang, Y.,** see Cui, Y., *TCSI Oct. 2021* 4360-4370
- Yang, Z.,** Guan, D., Zhang, Q., Xu, H., Lin, M., Zhang, X., Hong, R., and Yong, S., Mode Composite Waveguide Based on Hybrid Substrate Integrated Waveguide and Spoof Surface Plasmon Polariton Structure; *TCSI April 2021* 1472-1480
- Yang, Z.,** Chen, Y., Mak, P., and Martins, R.P., A 0.003-mm² 440fs_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS; *TCSI June 2021* 2307-2316
- Yao, L.,** see Wang, X., *TCSI Feb. 2021* 741-750
- Yao, X.,** Zhang, L., and Zheng, W.X., Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm; *TCSI Sept. 2021* 3836-3845
- Yao, Y.,** see Ge, X., *TCSI June 2021* 2736-2748
- Yao, Y.,** see Chen, W., *TCSI Nov. 2021* 4566-4575
- Yazicigil, R.T.,** see Yi, X., *TCSI Sept. 2021* 3537-3550
- Ye, C.,** see Kim, J., *TCSI Jan. 2021* 350-362
- Ye, F.,** see Cao, Y., *TCSI Feb. 2021* 641-654
- Ye, K.,** see Xie, Z., *TCSI March 2021* 1366-1376
- Ye, L.,** see Wang, Z., *TCSI March 2021* 1160-1170
- Ye, L.,** see Wang, Z., *TCSI April 2021* 1624-1635
- Ye, L.,** Wang, Z., Liu, Y., Chen, P., Li, H., Zhang, H., Wu, M., He, W., Shen, L., Zhang, Y., Tan, Z., Wang, Y., and Huang, R., The Challenges and Emerging Technologies for Low-Power Artificial Intelligence IoT Systems ; *TCSI Dec. 2021* 4821-4834
- Ye, Y.,** see Wang, Y., *TCSI May 2021* 2257-2270
- Ye, Y.,** Wei, H., Lu, R., Su, H., and Wu, Y., Containment Control for Networked Fractional-Order Systems With Sampled Position Data; *TCSI Sept. 2021* 3881-3889
- Ye, Z.,** see Zhang, D., *TCSI May 2021* 2158-2170
- Yi, M.,** see Wang, X., *TCSI Feb. 2021* 741-750
- Yi, S.,** Kumar, S., and Williams, R.S., Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses; *TCSI Dec. 2021* 4970-4978
- Yi, X.,** Wang, C., Hu, Z., Holloway, J.W., Khan, M.I.W., Ibrahim, M.I., Kim, M., Dogiamis, G.C., Perkins, B., Kaynak, M., Yazicigil, R.T., Chandrakasan, A.P., and Han, R., Emerging Terahertz Integrated Systems in Silicon ; *TCSI Sept. 2021* 3537-3550
- Yi, Y.,** see Bai, K., *TCSI July 2021* 2850-2862
- Yi, Y.,** see Zhao, D., *TCSI Oct. 2021* 3977-3990
- Yi, Y.,** see Zhao, D., *TCSI Oct. 2021* 4413
- Yildirim, B.,** Gheisarnejad, M., and Khooban, M.H., Delay-Dependent Stability Analysis of Modern Shipboard Microgrids; *TCSI April 2021* 1693-1705
- Yin, H.,** Chen, Y., Chen, Z., and Li, M., Adaptive Fast Fault Location for Open-Switch Faults of Voltage Source Inverter; *TCSI Sept. 2021* 3965-3974
- Yin, J.,** see Xie, R., *TCSI Dec. 2021* 5095-5107
- Yin, N.,** Huang, B., Chen, X., Chen, J., and Yu, Z., An MTJ-Based Asynchronous System With Extremely Fine-Grained Voltage Scaling; *TCSI Jan. 2021* 311-321
- Yin, P.,** Shu, Z., Xia, Y., Shen, T., Guan, X., Wang, X., Mohammad, U., Zang, J., Fu, D., Zeng, X., Tang, F., and Bermak, A., A Low-Area and Low-Power Comma Detection and Word Alignment Circuits for JESD204B/C Controller; *TCSI July 2021* 2925-2935
- Ying, R.,** and Molnar, A., Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications; *TCSI May 2021* 2210-2223
- Yong, S.,** see Yang, Z., *TCSI April 2021* 1472-1480
- Yoo, T.,** see Yu, C., *TCSI Feb. 2021* 667-679
- Yoon, J.,** see Kwon, E., *TCSI Oct. 2021* 4156-4169
- Yoshioka, T.,** see Liu, B., *TCSI Feb. 2021* 603-616
- You, X.,** see Ji, H., *TCSI March 2021* 1330-1341
- You, X.,** see Gu, P., *TCSI April 2021* 1398-1408
- You, X.,** see Zhong, J., *TCSI May 2021* 1869-1880
- You, X.,** see Li, K., *TCSI July 2021* 3069-3078
- You, X.,** see Pang, X., *TCSI Aug. 2021* 3495-3507
- You, X.,** see Zhao, D., *TCSI Oct. 2021* 3977-3990
- You, X.,** see Zhao, D., *TCSI Oct. 2021* 4413
- You, X.,** see Zhang, C., *TCSI Dec. 2021* 5049-5060
- You, Z.,** see Liu, P., *TCSI Nov. 2021* 4444-4455
- Younes, H.,** Ibrahim, A., Rizk, M., and Valle, M., A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing; *TCSI Oct. 2021* 4232-4244
- Yu, C.,** Yoo, T., Kim, H., Kim, T.T., Chuan, K.C.T., and Kim, B., A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks; *TCSI Feb. 2021* 667-679
- Yu, D.,** see Liao, Z., *TCSI Feb. 2021* 929-939
- Yu, D.,** see Liao, Z., *TCSI July 2021* 3103-3113
- Yu, G.,** see Wu, J., *TCSI June 2021* 2522-2534
- Yu, H.,** see Liu, Y., *TCSI Aug. 2021* 3449-3459
- Yu, J.,** see Wang, Q., *TCSI Aug. 2021* 3436-3448
- Yu, K.,** see Zhang, M., *TCSI June 2021* 2688-2701
- Yu, L.,** see Chen, P., *TCSI Feb. 2021* 797-807
- Yu, Q.,** see Li, J., *TCSI March 2021* 1102-1113
- Yu, S.,** see Wang, J., *TCSI May 2021* 2107-2120
- Yu, S.,** Shim, W., Peng, X., and Luo, Y., RRAM for Compute-in-Memory: From Inference to Training; *TCSI July 2021* 2753-2765
- Yu, S.,** see Wang, Q., *TCSI Sept. 2021* 3794-3807
- Yu, S.S.,** see Lin, D., *TCSI March 2021* 1034-1044
- Yu, X.,** see Chen, Z., *TCSI Jan. 2021* 376-386
- Yu, Z.,** see Yin, N., *TCSI Jan. 2021* 311-321
- Yu, Z.,** see Chen, H., *TCSI March 2021* 1231-1244
- Yu, Z.,** see Chen, H., *TCSI Aug. 2021* 3293-3304
- Yu, Z.,** see Shi, B., *TCSI Nov. 2021* 4746-4759
- Yuan, D.,** see Wang, T., *TCSI Nov. 2021* 4520-4533
- Yuan, K.,** see Lin, J., *TCSI July 2021* 3089-3102
- Yuan, L.,** see Tan, T., *TCSI March 2021* 1354-1365
- Yuan, M.,** see Li, C., *TCSI May 2021* 1881-1891
- Yuan, T.,** Liu, W., Han, J., and Lombardi, F., High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization; *TCSI Jan. 2021* 250-263
- Yuan, Y.,** see Wang, Y., *TCSI May 2021* 2257-2270
- Yue, C.P.,** see Azmat, R., *TCSI Nov. 2021* 4545-4555
- Yue, D.,** see Chen, J., *TCSI Feb. 2021* 918-928
- Yuldashev, M.,** see Kuznetsov, N., *TCSI Oct. 2021* 4049-4061
- Yuldashev, R.,** see Kuznetsov, N., *TCSI Oct. 2021* 4049-4061

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- Zacharelos, E.,** see Napoli, E., *TCSI Oct. 2021* 4142-4155
- Zahir, A.,** see Butt, U.M., *TCSI Aug. 2021* 3351-3362
- Zakharov, A.,** Parametric and Structural-Parametric Synthesis of Nonuniform Transmission Line Resonators; *TCSI March 2021* 1055-1067
- Zaki, A.M.,** see Mostafa, M., *TCSI May 2021* 2003-2016
- Zambrano, S.,** see Meucci, R., *TCSI July 2021* 3023-3030
- Zang, H.,** see Jiang, Y., *TCSI Dec. 2021* 4935-4944
- Zang, J.,** see Yin, P., *TCSI July 2021* 2925-2935
- Zanotti, T.,** see Puglisi, F.M., *TCSI Nov. 2021* 4433-4443
- Zarei, A.,** and Safaei, F., LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications; *TCSI Nov. 2021* 4686-4699
- Zarudniev, M.,** see Perodou, A., *TCSI Jan. 2021* 161-174

- Zeidler, S.,** see Schrape, O., *TCSI Nov. 2021* 4796-4809
- Zeng, X.,** see Han, Y., *TCSI July 2021* 2962-2975
- Zeng, X.,** see Tan, C., *TCSI July 2021* 3044-3057
- Zeng, X.,** see Yin, P., *TCSI July 2021* 2925-2935
- Zeng, Z.,** Estrada-Lopez, J.J., Wang, B., and Sanchez-Sinencio, E., A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine; *TCSI Sept. 2021* 3587-3597
- Zeng, Z.,** see Huang, T., *TCSI Nov. 2021* 4417-4418
- Zeng, Z.,** see Huang, T., *TCSI Dec. 2021* 4835-4836
- Zeng, Z.,** see Zhang, S., *TCSI Dec. 2021* 4945-4956
- Zervakis, G.,** see Paim, G., *TCSI April 2021* 1481-1492
- Zervakis, G.,** see Salamin, S., *TCSI Oct. 2021* 4299-4309
- Zervakis, G.,** see Balaskas, K., *TCSI Nov. 2021* 4710-4721
- Zhai, J.,** see Shu, F., *TCSI Feb. 2021* 808-817
- Zhai, J.,** see Shu, F., *TCSI Oct. 2021* 4371-4380
- Zhan, M.,** see Zhang, Y., *TCSI April 2021* 1553-1566
- Zhan, M.,** Pang, Z., Yu, K., and Wen, H., Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications; *TCSI June 2021* 2688-2701
- Zhan, Y.,** see Wu, J., *TCSI June 2021* 2522-2534
- Zhang, B.,** Chen, L., and Verma, N., Neural Network Training With Stochastic Hardware Models and Software Abstractions; *TCSI April 2021* 1532-1542
- Zhang, B.,** see Datta, G., *TCSI May 2021* 1990-2002
- Zhang, B.,** see Ming, X., *TCSI June 2021* 2354-2367
- Zhang, B.,** see Wang, T., *TCSI Nov. 2021* 4520-4533
- Zhang, C.,** see Ji, H., *TCSI March 2021* 1330-1341
- Zhang, C.,** see Lu, J., *TCSI July 2021* 2976-2985
- Zhang, C.,** see Pang, X., *TCSI Aug. 2021* 3495-3507
- Zhang, C.,** Wu, Z., Studer, C., Zhang, Z., and You, X., Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems; *TCSI Dec. 2021* 5049-5060
- Zhang, C.,** see Jiang, Y., *TCSI Dec. 2021* 4935-4944
- Zhang, D.,** see Zhu, Z., *TCSI Jan. 2021* 444-457
- Zhang, D.,** see Chen, P., *TCSI Feb. 2021* 797-807
- Zhang, D.,** Ye, Z., and Dong, X., Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack; *TCSI May 2021* 2158-2170
- Zhang, F.,** Chen, P., Walling, J.S., Zhu, A., and Staszewski, R.B., An Active-Under-Coil RFDAC With Analog Linear Interpolation in 28-nm CMOS; *TCSI May 2021* 1855-1868
- Zhang, G.,** and Zhu, Q., Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems; *TCSI Sept. 2021* 3808-3821
- Zhang, H.,** see Chen, L., *TCSI Jan. 2021* 416-425
- Zhang, H.,** see Fan, S., *TCSI Feb. 2021* 856-867
- Zhang, H.,** see Liu, B., *TCSI Feb. 2021* 603-616
- Zhang, H.,** see Asadikouhanjani, M., *TCSI May 2021* 2030-2041
- Zhang, H.,** see Ye, L., *TCSI Dec. 2021* 4821-4834
- Zhang, J.,** and Shen, C., Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks; *TCSI Jan. 2021* 288-300
- Zhang, J.,** Zhang, P., and Yan, J., Distributed Adaptive Finite-Time Compensation Control for UAV Swarm With Uncertain Disturbances; *TCSI Feb. 2021* 829-841
- Zhang, J.,** see Ming, X., *TCSI June 2021* 2354-2367
- Zhang, J.,** see Zhao, D., *TCSI Oct. 2021* 3977-3990
- Zhang, J.,** see Liu, Y., *TCSI Oct. 2021* 4194-4206
- Zhang, J.,** see Zhao, D., *TCSI Oct. 2021* 4413
- Zhang, K.,** see Zhang, Y., *TCSI March 2021* 1193-1205
- Zhang, K.,** see Liu, C., *TCSI April 2021* 1646-1658
- Zhang, K.,** see Cheng, W., *TCSI May 2021* 2121-2133
- Zhang, K.,** see Khoeini, F., *TCSI Sept. 2021* 3642-3655
- Zhang, L.,** see Yao, X., *TCSI Sept. 2021* 3836-3845
- Zhang, M.,** see Zhang, Y., *TCSI April 2021* 1553-1566
- Zhang, P.,** see Zhang, J., *TCSI Feb. 2021* 829-841
- Zhang, Q.,** Lu, J., Ma, Y., and Chen, Y., Time Domain Solution Analysis and Novel Admissibility Conditions of Singular Fractional-Order Systems; *TCSI Feb. 2021* 842-855
- Zhang, Q.,** see Yang, Z., *TCSI April 2021* 1472-1480
- Zhang, S.,** see Cao, Y., *TCSI Feb. 2021* 641-654
- Zhang, S.,** see Fan, X., *TCSI July 2021* 3031-3043
- Zhang, S.,** Li, C., Zheng, J., Wang, X., Zeng, Z., and Chen, G., Generating Any Number of Diversified Hidden Attractors via Memristor Coupling; *TCSI Dec. 2021* 4945-4956
- Zhang, T.,** see Cao, Y., *TCSI Feb. 2021* 641-654
- Zhang, T.,** see Wei, B., *TCSI Oct. 2021* 4381-4392
- Zhang, W.,** Mao, S., Huang, J., Kocarev, L., and Tang, Y., Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks; *TCSI Feb. 2021* 776-785
- Zhang, X.,** see Xie, Z., *TCSI Jan. 2021* 483-495
- Zhang, X.,** Acharya, J., and Basu, A., A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing; *TCSI Feb. 2021* 617-630
- Zhang, X.,** Huang, W., and Wang, Q., Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances; *TCSI March 2021* 1297-1307
- Zhang, X.,** see Yang, Z., *TCSI April 2021* 1472-1480
- Zhang, X.,** see Wang, J., *TCSI May 2021* 2107-2120
- Zhang, X.,** see Li, B., *TCSI Aug. 2021* 3279-3292
- Zhang, Y.,** see Liu, B., *TCSI Feb. 2021* 603-616
- Zhang, Y.,** Wang, J., Lian, C., Bai, Y., Wang, G., Zhang, Z., Zheng, Z., Chen, L., Zhang, K., and Sirakoulis, G., Time-Domain Computing in Memory Using Spintronics for Energy-Efficient Convolutional Neural Network; *TCSI March 2021* 1193-1205
- Zhang, Y.,** see Zhang, Y., *TCSI March 2021* 1193-1205
- Zhang, Y.,** see Wang, H., *TCSI March 2021* 1114-1122
- Zhang, Y.,** He, Z., Wan, M., Zhan, M., Zhang, M., Peng, K., Song, M., and Gu, H., A New Message Expansion Structure for Full Pipeline SHA-2; *TCSI April 2021* 1553-1566
- Zhang, Y.,** see Li, Q., *TCSI May 2021* 1892-1905
- Zhang, Y.,** see Chen, H., *TCSI Aug. 2021* 3293-3304
- Zhang, Y.,** see Peng, X., *TCSI Sept. 2021* 3621-3630
- Zhang, Y.,** see Wang, T., *TCSI Nov. 2021* 4520-4533
- Zhang, Y.,** see Ye, L., *TCSI Dec. 2021* 4821-4834
- Zhang, Z.,** see Ji, H., *TCSI March 2021* 1330-1341
- Zhang, Z.,** see Zhang, Y., *TCSI March 2021* 1193-1205
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- Zhao, D.,** Gu, P., Zhong, J., Peng, N., Yang, M., Yi, Y., Zhang, J., He, P., Chai, Y., Chen, Z., and You, X., Corrections to “Millimeter-Wave Integrated Phased Arrays” [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]; *TCSI Oct. 2021* 4413
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 A Two-Stage Operand Trimming Approximate Logarithmic Multiplier. *Pilipovic, R., +, TCSI June 2021 2535-2545*
 Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*
 Experimental Study of Fractional-Order RC Circuit Model Using the Caputo and Caputo-Fabrizio Derivatives. *Lin, D., +, TCSI March 2021 1034-1044*
 Lattice Trajectory Piecewise Linear Method for the Simulation of Diode Circuits. *Wang, J., +, TCSI May 2021 2069-2081*
 Ultralow-Latency VLSI Architecture Based on a Linear Approximation Method for Computing Nth Roots of Floating-Point Numbers. *Lyu, F., +, TCSI Feb. 2021 715-727*

Architecture

- Analog Neural Computing With Super-Resolution Memristor Crossbars. *James, A.P., +, TCSI Nov. 2021 4470-4481*

Array signal processing

- Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. *Boljanovic, V., +, TCSI April 2021 1727-1739*
 Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters. *Liu, X., +, TCSI July 2021 2998-3011*

Arrays

- A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021 4456-4469*
 IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*

Artificial biological organs

- Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020. *David, J., +, TCSI Aug. 2021 3131-3132*

Artificial intelligence

- An Optimized Radiation Tolerant Baseline Correction Filter for HEP Using AI Methodologies. *Sanches, B., +, TCSI May 2021 1789-1799*
 Applications of Artificial Intelligence on the Modeling and Optimization for Analog and Mixed-Signal Circuits: A Review. *Fayazi, M., +, TCSI June 2021 2418-2431*
 The Challenges and Emerging Technologies for Low-Power Artificial Intelligence IoT Systems. *Ye, L., +, TCSI Dec. 2021 4821-4834*

Artificial neural networks

- Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. *Vahdat, S., +, TCSI Oct. 2021 4310-4323*

Artificial organs

- Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaftah, A., +, TCSI Aug. 2021 3147-3157*

Assistive technology

- A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*

Asymptotic stability

- H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021 818-828*
 Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*
 Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021 2171-2182*
 Distributed Fault Detection and Control for Markov Jump Systems Over Sensor Networks With Round-Robin Protocol. *Gong, C., +, TCSI Aug. 2021 3422-3435*
 Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*

Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application.

Fan, S., +, TCSI Feb. 2021 856-867

Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*

Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*

Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms. *Song, X., +, TCSI Jan. 2021 363-375*

Global Event-Triggered Output Feedback Stabilization for a Class of Nonlinear Time-Delay Systems. *Shu, F., +, TCSI Oct. 2021 4371-4380*

Output Series-Parallel Connection of Passivity-Based Controlled DC-DC Converters: Generalization of Asymptotic Stability. *Murakawa, Y., +, TCSI April 2021 1750-1759*

State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021 3846-3856*

Asynchronous circuits

An MTJ-Based Asynchronous System With Extremely Fine-Grained Voltage Scaling. *Yin, N., +, TCSI Jan. 2021 311-321*

Attenuation

A Fractional Order Notch Filter to Compensate the Attenuation-Loss Due to Change in Order of the Circuit. *Mohapatra, A.S., +, TCSI Feb. 2021 655-666*

Millimeter-Wave Integrated Phased Arrays. *Zhao, D., +, TCSI Oct. 2021 3977-3990*

Authorization

NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs. *Wang, A., +, TCSI June 2021 2508-2521*

Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*

Automata

Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter. *Shi, B., +, TCSI Nov. 2021 4746-4759*

Autonomous aerial vehicles

A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*

Distributed Adaptive Finite-Time Compensation Control for UAV Swarm With Uncertain Disturbances. *Zhang, J., +, TCSI Feb. 2021 829-841*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Autonomous underwater vehicles

Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021 2158-2170*

Autoregressive processes

Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*

Avalanche photodiodes

Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems. *Kumar, A., +, TCSI Nov. 2021 4636-4647*

B**Backpropagation**

Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021 1760-1768*

Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*

Backstepping

Adaptive Fuzzy Fast Finite-Time Dynamic Surface Tracking Control for Nonlinear Systems. *Wang, H., +, TCSI Oct. 2021 4337-4348*

Baluns

Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

Band-pass filters

A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021 1045-1054*

A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. *Cavallaro, M., +, TCSI Dec. 2021 5018-5028*

Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

Comments on “Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters”. *Han, G., +, TCSI Jan. 2021 550-552*

Reply to Comments on “Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters”. *Mirzaei, A., +, TCSI Jan. 2021 553*

Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

Band-stop filters

Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

Bandwidth

A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021 4889-4899*

Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits. *Khan, R.A., +, TCSI Oct. 2021 4025-4037*

Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures. *Napoli, E., +, TCSI Oct. 2021 4142-4155*

Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*

Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. *Liu, Y., +, TCSI Oct. 2021 4194-4206*

Batteries

A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*

The Challenges and Emerging Technologies for Low-Power Artificial Intelligence IoT Systems. *Ye, L., +, TCSI Dec. 2021 4821-4834*

Bayes methods

QuantBayes: Weight Optimization for Memristive Neural Networks via Quantization-Aware Bayesian Inference. *Zhou, Y., +, TCSI Dec. 2021 4851-4861*

BCH codes

Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021 496-509*

Fast Nested Key Equation Solvers for Generalized Integrated Interleaved Decoder. *Xie, Z., +, TCSI Jan. 2021 483-495*

Beam steering

Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. *Boljanovic, V., +, TCSI April 2021 1727-1739*

Belief propagation

A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

BiCMOS integrated circuits

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021 3668-3681*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*
mm-Wave Through-Load Element for On-Wafer Measurement Applications. *Margalef-Rovira, M., +, TCSI Aug. 2021 3170-3183*

Bifurcation

A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

Chaos Generation With Impulse Control: Application to Non-Chaotic Systems and Circuit Design. *Tian, K., +, TCSI July 2021 3012-3022*

Continuation-Based Method to Find Periodic Windows in Bifurcation Diagrams With Applications to the Chua’s Circuit With a Cubic Nonlinearity. *Galias, Z., TCSI Sept. 2021 3784-3793*

Unfolding Nonlinear Dynamics in Analogue Systems With Mem-Elements. *Marco, M.D., +, TCSI Jan. 2021 14-24*

BIMOS integrated circuits

A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH Rectifier and DC-DC Converters. *Kara, I., +, TCSI Jan. 2021 210-223*

Binary codes

Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

Bioelectric phenomena

High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions. *Haghiri, S., +, TCSI Jan. 2021 275-287*

Bioelectric potentials

Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems. *Ranjandish, R., +, TCSI April 2021 1740-1749*

Biological neural networks

Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays. *Wang, T., +, TCSI Nov. 2021 4520-4533*

Biological system modeling

An Efficient Digital Realization of Retinal Light Adaptation in Cone Photoreceptors. *Ghanbarpour, M., +, TCSI Dec. 2021 5072-5080*

Biological techniques

High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions. *Haghiri, S., +, TCSI Jan. 2021 275-287*

Biology

Neuromorphic Dynamics of Chua Corsage Memristor. *Jin, P., +, TCSI Nov. 2021 4419-4432*

Biomedical electrodes

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Biomedical electronics

A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*

A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Low-Voltage Low-Noise High-CMRR Biopotential Integrated Preamplifier. *Cabrera, C., +, TCSI Aug. 2021 3232-3241*

Biosensors

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Bipolar MIMIC

Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021 3668-3681*

Bipolar transistors

Sub-ppm/ $^{\circ}\text{C}$ Bandgap References With Natural Basis Expansion for Curvature Cancellation. *Liu, N., +, TCSI Sept. 2021 3551-3561*

Biquadratic filters

Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

Bitcoin

BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks. *Tran, T.H., +, TCSI Oct. 2021 4245-4258*

Blockchains

BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks. *Tran, T.H., +, TCSI Oct. 2021 4245-4258*

Blood

Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaifah, A., +, TCSI Aug. 2021 3147-3157*

Bluetooth

A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*

Boolean functions

Time-Domain Computing in Memory Using Spintronics for Energy-Efficient Convolutional Neural Network. *Zhang, Y., +, TCSI March 2021 1193-1205*

Boosting

A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

Brain

A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*

Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. *Lin, H., +, TCSI Aug. 2021 3397-3410*

Brain modeling

How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation. *Kang, S.M., +, TCSI Dec. 2021 4837-4850*

Bridge circuits

Fault Modeling and Efficient Testing of Memristor-Based Memory. *Liu, P., +, TCSI Nov. 2021 4444-4455*

Brightness

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Broadband antennas

Millimeter-Wave Integrated Phased Arrays. *Zhao, D., +, TCSI Oct. 2021 3977-3990*

Brushless DC motors

Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*

Buffer circuits

A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler. *Jiang, W., +, TCSI Feb. 2021 557-568*

A High-Performance Bidirectional Architecture for the Quasi-Comparison-Free Sorting Algorithm. *Chen, W., +, TCSI April 2021 1493-1506*

A Novel Topology of Coupled Phase-Locked Loops. *Karman, S., +, TCSI March 2021 989-997*

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

Built-in self test

SymBIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021 2580-2593*

Busbars

Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Paralleled SiC MOSFETs. *Wang, J., +, TCSI May 2021 2107-2120*

C**Cache storage**

Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021 238-249*

Magnetoresistive Circuits and Systems: Embedded Non-Volatile Memory to Crossbar Arrays. *Agrawal, A., +, TCSI June 2021 2281-2294*

CAD

A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*

Calibration

22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification. *Vallicelli, E.A., +, TCSI Jan. 2021 3-13*

A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler. *Jiang, W., +, TCSI Feb. 2021 557-568*

A 91.0-dB SFDR Single-Coarse Dual-Fine Pipelined-SAR ADC With Split-Based Background Calibration in 28-nm CMOS. *Cao, Y., +, TCSI Feb. 2021 641-654*

A Foreground Calibration for M-Channel Time-Interleaved Analog-to-Digital Converters Based on Genetic Algorithm. *Tavares, Y.A., +, TCSI April 2021 1444-1457*

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

A Time-Based Pipelined ADC Using Integrate-and-Fire Multiplying-DAC. *Ryu, S., +, TCSI July 2021 2876-2889*

Broadband Mismatch Calibration for Time-Interleaved ADC Based on Linear Frequency Modulated Signal. *Peng, X., +, TCSI Sept. 2021 3621-3630*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Digital Non-Linearity Calibration for ADCs With Redundancy Using a New LUT Approach. *Gines, A., +, TCSI Aug. 2021 3197-3210*

FPGA-Based Relaxation D/A Converters With Parasitics-Induced Error Suppression and Digital Self-Calibration. *Rubino, R., +, TCSI June 2021 2494-2507*

Impact of Analog Non-Idealities on the Design Space of 6T-SRAM Current-Domain Dot-Product Operators for In-Memory Computing. *Kneip, A., +, TCSI May 2021 1931-1944*

Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*

Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. *Mikulic, J., +, TCSI Oct. 2021 4076-4089*

Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*

Cameras

A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*

Cancer

22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification. *Vallicelli, E.A., +, TCSI Jan. 2021 3-13*

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Capacitance

High-Dimensional Extension of the TICER Algorithm. *Hao, L., +, TCSI Nov. 2021 4722-4734*

- Multi-Objective Digital Design Optimization via Improved Drive Granularity Standard Cells. *Cao, L., +, TCSI Nov. 2021 4660-4671*
- Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*
- PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits. *Salamin, S., +, TCSI Oct. 2021 4299-4309*

Capacitance measurement

- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

Capacitive sensors

- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

Capacitors

- A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing. *Zhang, X., +, TCSI Feb. 2021 617-630*

- A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

- A 2.1 mW 2 MHz-BW 73.8 dB-SNDR Buffer-Embedded Noise-Shaping SAR ADC. *Kim, T., +, TCSI Dec. 2021 5029-5037*

- A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

- A Multi-Step Incremental Analog-to-Digital Converter With a Single Opamp and Two- Capacitor SAR Extended Counting. *Kuo, S., +, TCSI July 2021 2890-2899*

- A New Boosted Active-Capacitor With Negative- G_m for Wide Tuning Range VCOs. *Agarwal, P., +, TCSI March 2021 1080-1090*

- A T-Type Switched-Capacitor Multilevel Inverter With Low Voltage Stress and Self-Balancing. *Wang, Y., +, TCSI May 2021 2257-2270*

- A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

- Experimental Study of Fractional-Order RC Circuit Model Using the Caputo and Caputo-Fabrizio Derivatives. *Lin, D., +, TCSI March 2021 1034-1044*

- Failure in Ring Oscillators With Capacitive Load. *Ravezzi, L., TCSI Aug. 2021 3388-3396*

- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

- Noise Analysis of Charge-Balanced Readout Circuits for MEMS Accelerometers. *Lanniel, A., +, TCSI Jan. 2021 175-184*

- Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*

- Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*

- Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. *Fu, H., +, TCSI Nov. 2021 4495-4507*

- Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters. *Sanabria-Borbon, A.C., +, TCSI May 2021 1841-1854*

Cascade networks

- Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

Cellular biophysics

- High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions. *Haghiri, S., +, TCSI Jan. 2021 275-287*

Cellular radio

- A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*

Central Processing Unit

- Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. *Kwon, E., +, TCSI Oct. 2021 4156-4169*

Channel bank filters

- A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021 1045-1054*

- An 800 nW Switched-Capacitor Feature Extraction Filterbank for Sound Classification. *Villamizar, D.A., +, TCSI April 2021 1578-1588*

Channel coding

- A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

- Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. *Cui, H., +, TCSI Feb. 2021 879-891*

- Fast Nested Key Equation Solvers for Generalized Integrated Interleaved Decoder. *Xie, Z., +, TCSI Jan. 2021 483-495*

- High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Chaos

- A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

- Chaos Generation With Impulse Control: Application to Non-Chaotic Systems and Circuit Design. *Tian, K., +, TCSI July 2021 3012-3022*

- Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm. *Wang, Q., +, TCSI Sept. 2021 3794-3807*

- Continuation-Based Method to Find Periodic Windows in Bifurcation Diagrams With Applications to the Chua's Circuit With a Cubic Nonlinearity. *Galias, Z., TCSI Sept. 2021 3784-3793*

- Discrete Memristor Hyperchaotic Maps. *Bao, H., +, TCSI Nov. 2021 4534-4544*

- Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. *Lin, H., +, TCSI Aug. 2021 3397-3410*

- Neuromorphic Dynamics of Chua Corsage Memristor. *Jin, P., +, TCSI Nov. 2021 4419-4432*

Chaos generators

- Chaos Generation With Impulse Control: Application to Non-Chaotic Systems and Circuit Design. *Tian, K., +, TCSI July 2021 3012-3022*

Chaotic communication

- A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

- Discrete Memristor Hyperchaotic Maps. *Bao, H., +, TCSI Nov. 2021 4534-4544*

- Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*

- Generating Any Number of Diversified Hidden Attractors via Memristor Coupling. *Zhang, S., +, TCSI Dec. 2021 4945-4956*

Charge pump circuits

- Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Charge pumps

- A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

- Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges. *Kuznetsov, N., +, TCSI Oct. 2021 4049-4061*

Chemical sensors

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Choppers (circuits)

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- Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*

Chua's circuit

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 - A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021 2003-2016*
 - An MTJ-Based Asynchronous System With Extremely Fine-Grained Voltage Scaling. *Yin, N., +, TCSI Jan. 2021 311-321*
 - Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*
 - From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*
 - High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021 264-274*
 - Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021 301-310*
 - Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*
 - Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*
 - Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*
- CMOS memory circuits**
- A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021 692-703*
 - A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*
 - Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*
 - Body Biased Sense Amplifier With Auto-Offset Mitigation for Low-Voltage SRAMs. *Patel, D., +, TCSI Aug. 2021 3265-3278*
 - Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*
 - MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators. *Nasrin, S., +, TCSI May 2021 1966-1978*
 - Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021 67-76*
 - Radiation Hardened 12T SRAM With Crossbar-Based Peripheral Circuit in 28nm CMOS Technology. *Han, Y., +, TCSI July 2021 2962-2975*
 - Soft-Error-Immune Read-Stability-Improved SRAM for Multi-Node Upset Tolerance in Space Applications. *Pal, S., +, TCSI Aug. 2021 3317-3327*
 - Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*
 - TD-SRAM: Time-Domain-Based In-Memory Computing Macro for Binary Neural Networks. *Song, J., +, TCSI Aug. 2021 3377-3387*
- CMOS technology**
- A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021 4626-4635*
 - A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5 -dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*
- CNTFETs**
- Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*
- Code division multiple access**
- Self-Synchronized DS/SS With High Spread Factors for Robust Millimeter-Wave Datalinks. *Tang, A., +, TCSI Sept. 2021 3941-3950*
- Cognitive radio**
- Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*
- Coils**
- A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*
 - Analysis and Design of EIT-Like Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI July 2021 3103-3113*
 - Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. *Qu, H., +, TCSI Dec. 2021 5182-5193*
 - Portable CMOS NMR System With 50-kHz IF, 10- μ s Dead Time, and Frequency Tracking. *Hong, S., +, TCSI Nov. 2021 4576-4588*
- Collision avoidance**
- Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021 2616-2625*
- Combinational circuits**
- General Efficient TMR for Combinational Circuit Hardening Against Soft Errors and Improved Multi-Objective Optimization Framework. *Tan, C., +, TCSI July 2021 3044-3057*
- Companies**
- Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. *Li, X., +, TCSI Oct. 2021 4268-4277*
- Comparators (circuits)**
- An 8-Bit 800 MS/s Loop-Unrolled SAR ADC With Common-Mode Adaptive Background Offset Calibration in 28 nm FDSOI. *Akkaya, A., +, TCSI July 2021 2766-2774*
- Compensation**
- Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*
- Complex networks**
- Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*
 - Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*
 - Evaluating Performances and Importance of Venture Capitals: A Complex Network Approach. *Liu, J., +, TCSI May 2021 2060-2068*
 - Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. *Li, X., +, TCSI Oct. 2021 4268-4277*
 - Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy. *Wu, Y., +, TCSI Nov. 2021 4735-4745*
 - Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. *Xu, Y., +, TCSI April 2021 1589-1598*
 - Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*
- Complexity theory**
- A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. *Cavallaro, M., +, TCSI Dec. 2021 5018-5028*
 - Adaptive Fuzzy Fast Finite-Time Dynamic Surface Tracking Control for Nonlinear Systems. *Wang, H., +, TCSI Oct. 2021 4337-4348*
 - Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*
 - Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*
- Compressed sensing**
- Generalized Analog-to-Information Converter With Analysis Sparse Prior. *Qian, H., +, TCSI Sept. 2021 3574-3586*
- Computational complexity**
- Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*
 - Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*
 - Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

- Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021* 3732-3745
- Interconnection, Reciprocity and a Hierarchical Classification of Generalized Multiports. *Recski, A., +, TCSI Sept. 2021* 3682-3692
- Low Delay Short Word Length Sigma Delta Active Noise Control. *Lopes, P.A.C., +, TCSI Sept. 2021* 3746-3757
- NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021* 1892-1905
- Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy. *Wang, Y., +, TCSI Aug. 2021* 3474-3484
- Computational modeling**
- A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Congestion-Aware Router. *Pu, J., +, TCSI Dec. 2021* 5081-5094
- Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021* 4910-4923
- Applying Lightweight Soft Error Mitigation Techniques to Embedded Mixed Precision Deep Neural Networks. *Abich, G., +, TCSI Nov. 2021* 4772-4782
- How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation. *Kang, S.M., +, TCSI Dec. 2021* 4837-4850
- Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021* 4970-4978
- Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021* 4900-4909
- Computer architecture**
- A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021* 4456-4469
- A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. *Eggemann, M., +, TCSI Oct. 2021* 4116-4128
- A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. *Juracy, L.R., +, TCSI Nov. 2021* 4783-4795
- A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing. *Younes, H., +, TCSI Oct. 2021* 4232-4244
- Analog Neural Computing With Super-Resolution Memristor Crossbars. *James, A.P., +, TCSI Nov. 2021* 4470-4481
- Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021* 5061-5071
- Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. *Schrape, O., +, TCSI Nov. 2021* 4796-4809
- DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays. *Moitra, A., +, TCSI Nov. 2021* 4482-4494
- Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021* 5049-5060
- Fault Modeling and Efficient Testing of Memristor-Based Memory. *Liu, P., +, TCSI Nov. 2021* 4444-4455
- IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021* 4672-4685
- Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021* 4648-4659
- Computer centers**
- A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021* 2714-2724
- Computer crime**
- NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs. *Wang, A., +, TCSI June 2021* 2508-2521
- Computer vision**
- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021* 704-714
- Computers**
- Fast Strategies for the Implementation of SIKE Round 3 on ARM Cortex-M4. *Anastasova, M., +, TCSI Oct. 2021* 4129-4141
- Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021* 4648-4659

Concatenated codes

Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021* 496-509

Conductors

Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits. *Khan, R.A., +, TCSI Oct. 2021* 4025-4037

Content-addressable storage

Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021* 67-76

Continuous time filters

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021* 592-602

Continuous time systems

A Capacitively Coupled CT Δ SM With Chopping Artifacts Rejection for Sensor Readout ICs. *Lim, C., +, TCSI Aug. 2021* 3242-3253

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021* 786-796

Bounded-Input Bounded-Output Stability Tests for Two-Dimensional Continuous-Time Systems. *Bistritz, Y., TCSI May 2021* 2134-2147

Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback. *Pavan, S., +, TCSI Aug. 2021* 3222-3231

Control engineering computing

Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021* 3857-3868

Control nonlinearities

Adaptive Practical Fixed-Time Tracking Control With Prescribed Boundary Constraints. *Chen, M., +, TCSI April 2021* 1716-1726

Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021* 3058-3068

Control system analysis

Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021* 3485-3494

Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021* 1693-1705

Control system synthesis

H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021* 818-828

Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems. *Yang, Y., +, TCSI Jan. 2021* 434-443

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021* 786-796

Adaptive Fault Estimation for Unmanned Surface Vessels With a Neural Network Observer Approach. *Chen, L., +, TCSI Jan. 2021* 416-425

Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021* 2626-2638

Adaptive Practical Fixed-Time Tracking Control With Prescribed Boundary Constraints. *Chen, M., +, TCSI April 2021* 1716-1726

Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021* 458-468

Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021* 2171-2182

Containment Control for Networked Fractional-Order Systems With Sampled Position Data. *Ye, Y., +, TCSI Sept. 2021* 3881-3889

Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021* 3485-3494

Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021* 776-785

Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021* 1693-1705

- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021 1659-1670*
- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*
- Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems. *Shu, F., +, TCSI Feb. 2021 808-817*
- Dynamic Triggering Mechanisms for Distributed Adaptive Synchronization Control and Its Application to Circuit Systems. *Xu, Y., +, TCSI May 2021 2246-2256*
- Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*
- Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*
- Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*
- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*
- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*
- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*
- Fractional-Order Sliding Mode Approach of Buck Converters With Mismatched Disturbances. *Lin, X., +, TCSI Sept. 2021 3890-3900*
- Frequency Design of Lossless Passive Electronic Filters: A State-Space Formulation of the Direct Synthesis Approach. *Perodou, A., +, TCSI Jan. 2021 161-174*
- Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*
- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunction Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*
- LMI-Based Robust Stability Analysis of Discrete-Time Fractional-Order Systems With Interval Uncertainties. *Zhu, Z., +, TCSI April 2021 1671-1680*
- Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*
- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*
- Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021 3901-3912*
- Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021 3069-3078*
- Polytopic Event-Triggered Robust Model Predictive Control for Constrained Linear Systems. *Hu, Z., +, TCSI June 2021 2594-2603*
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021 3436-3448*
- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021 1297-1307*
- State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021 3846-3856*
- Synthesis of Constant Power Loads Using Switching Converters Under Sliding-Mode Control. *Martinez-Trevino, B.A., +, TCSI Jan. 2021 524-535*
- Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm. *Yao, X., +, TCSI Sept. 2021 3836-3845*

Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*

Control systems

- Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021 4403-4412*
- Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. *Cui, Y., +, TCSI Oct. 2021 4360-4370*
- Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*
- Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*
- Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021 5156-5167*

Convergence

- A Smoothed LASSO-Based DNN Sparsification Technique. *Koneru, B.N.G., +, TCSI Oct. 2021 4287-4298*
- Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence. *Han, T., +, TCSI Oct. 2021 4393-4402*
- Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. *Menkad, T., +, TCSI Dec. 2021 5120-5133*
- Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*
- Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*
- Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*
- Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. *Xu, Y., +, TCSI April 2021 1589-1598*
- Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation. *Xu, Y., +, TCSI Oct. 2021 4278-4286*
- Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021 4324-4336*
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021 3436-3448*

Convergence of numerical methods

- Continuous-Time, Configurable Analog Linear System Solutions With Transconductance Amplifiers. *Hasler, J., +, TCSI Feb. 2021 765-775*

Converters

- TD-SRAM: Time-Domain-Based In-Memory Computing Macro for Binary Neural Networks. *Song, J., +, TCSI Aug. 2021 3377-3387*

Convex programming

- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*

Convolution

- A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. *Juracy, L.R., +, TCSI Nov. 2021 4783-4795*

- Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing. *Kim, S., +, TCSI May 2021 2017-2029*

- IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*

- Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform. *Xue, D., +, TCSI May 2021 2051-2059*

Convolutional codes

- CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*

- Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications. *Zhan, M., +, TCSI June 2021 2688-2701*

Convolutional neural networks

- A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*
- A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. *Juracy, L.R., +, TCSI Nov. 2021 4783-4795*
- A Mixed-Pruning Based Framework for Embedded Convolutional Neural Network Acceleration. *Chang, X., +, TCSI April 2021 1706-1715*
- An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*
- BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*
- CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*
- DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks. *Xie, R., +, TCSI Dec. 2021 5095-5107*
- Dynamic Dataflow Scheduling and Computation Mapping Techniques for Efficient Depthwise Separable Convolution Acceleration. *Li, B., +, TCSI Aug. 2021 3279-3292*
- Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device. *Lu, J., +, TCSI July 2021 2976-2985*
- Fast and Accurate Inference on Microcontrollers With Boosted Cooperative Convolutional Neural Networks (BC-Net). *Mocerino, L., +, TCSI Jan. 2021 77-88*
- Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020. *David, J., +, TCSI Aug. 2021 3131-3132*
- High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*
- Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing. *Kim, S., +, TCSI May 2021 2017-2029*
- IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*
- Impact of Analog Non-Idealities on the Design Space of 6T-SRAM Current-Domain Dot-Product Operators for In-Memory Computing. *Kneip, A., +, TCSI May 2021 1931-1944*
- Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021 67-76*
- Neural Synaptic Plasticity-Inspired Computing: A High Computing Efficient Deep Convolutional Neural Network Accelerator. *Xia, Z., +, TCSI Feb. 2021 728-740*
- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Coplanar waveguides

- 77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

Coprocessors

- CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*
- ECC Coprocessor Over a NIST Prime Field Using Fast Partial Montgomery Reduction. *Choi, P., +, TCSI March 2021 1206-1216*

Copy protection

- Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*

Cost function

- A Smoothed LASSO-Based DNN Sparsification Technique. *Koneru, B.N.G., +, TCSI Oct. 2021 4287-4298*

Coupled circuits

- Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*
- All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*
- Frequency Selective Impedance Transformer With High-Impedance Transforming Ratio and Extremely High/Low Termination Impedances. *Jeong, Y., +, TCSI June 2021 2382-2392*

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- Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. *Qiu, H., +, TCSI Dec. 2021 5182-5193*
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- Cyber-Physical Systems With Multiple Denial-of-Service Attackers: A Game-Theoretic Framework. *Huang, Y., +, TCSI Oct. 2021 4349-4359*
- Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy. *Wu, Y., +, TCSI Nov. 2021 4735-4745*
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- Generating Any Number of Diversified Hidden Attractors via Memristor Coupling. *Zhang, S., +, TCSI Dec. 2021 4945-4956*
- Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021 5156-5167*

Covariance matrices

- A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021 4889-4899*
- Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*
- Towards Low Latency and Resource-Efficient FPGA Implementations of the MUSIC Algorithm for Direction of Arrival Estimation. *Butt, U.M., +, TCSI Aug. 2021 3351-3362*

Cryptographic protocols

- Hardware Architecture for Supersingular Isogeny Diffie-Hellman and Key Encapsulation Using a Fast Montgomery Multiplier. *Farzam, M., +, TCSI May 2021 2042-2050*
- High-Speed FPGA Implementation of SIKE Based on an Ultra-Low-Latency Modular Multiplier. *Tian, J., +, TCSI Sept. 2021 3719-3731*

Cryptography

- A New Message Expansion Structure for Full Pipeline SHA-2. *Zhang, Y., +, TCSI April 2021 1553-1566*
- Fast Strategies for the Implementation of SIKE Round 3 on ARM Cortex-M4. *Anastasova, M., +, TCSI Oct. 2021 4129-4141*
- High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*
- Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021 4648-4659*
- NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs. *Wang, A., +, TCSI June 2021 2508-2521*
- Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy. *Wang, Y., +, TCSI Aug. 2021 3474-3484*
- Quantum Sealed-Bid Auction Without a Trusted Third Party. *Shi, R., TCSI Oct. 2021 4221-4231*

Current measurement

- Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021 4324-4336*

Current transformers

- A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhraei, S.S., +, TCSI April 2021 1388-1397*

Current-mode logic

- Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*

Cyberattack

- Probabilistic-Constrained H_∞ Tracking Control for a Class of Stochastic Nonlinear Systems Subject to DoS Attacks and Measurement Outliers. *Wei, B., +, TCSI Oct. 2021 4381-4392*

D**Damping**

- Damping Power System Electromechanical Oscillations Using Time Delays. *Tzounas, G., +, TCSI June 2021 2725-2735*

- Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021 1693-1705*
- Stability Assessment for Multi-Infeed Grid-Connected VSCs Modeled in the Admittance Matrix Form. *Orellana, L., +, TCSI Sept. 2021 3758-3771*
- Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*

Data compression

- A Mixed-Pruning Based Framework for Embedded Convolutional Neural Network Acceleration. *Chang, X., +, TCSI April 2021 1706-1715*
- An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*
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- Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*

Data handling

- Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*

Data mining

- BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks. *Tran, T.H., +, TCSI Oct. 2021 4245-4258*

Data privacy

- Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy. *Wang, Y., +, TCSI Aug. 2021 3474-3484*

Data transfer

- Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. *Qiu, H., +, TCSI Dec. 2021 5182-5193*

DC motor drives

- Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*

DC motors

- State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021 3846-3856*

DC-DC power converters

- A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021 2714-2724*

- A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*

- A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH Rectifier and DC-DC Converters. *Kara, I., +, TCSI Jan. 2021 210-223*

- A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

- An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

- Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021 1760-1768*

- Fractional-Order Sliding Mode Approach of Buck Converters With Mismatched Disturbances. *Lin, X., +, TCSI Sept. 2021 3890-3900*

- Impedance Shaping Control Strategy for Wireless Power Transfer System Based on Dynamic Small-Signal Analysis. *Tan, T., +, TCSI March 2021 1354-1365*

- Output Series-Parallel Connection of Passivity-Based Controlled DC-DC Converters: Generalization of Asymptotic Stability. *Murakawa, Y., +, TCSI April 2021 1750-1759*

- Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*

Decentralized control

- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Decision feedback equalizers

- A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD. *Chen, W., +, TCSI Nov. 2021 4566-4575*
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

Decision making

- Opinion Diffusion in Two-Layer Interconnected Networks. *Liu, C., +, TCSI Sept. 2021 3772-3783*

Decoding

- A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

- Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

- Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. *Cui, H., +, TCSI Feb. 2021 879-891*

- Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021 496-509*

- Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

- Fast Nested Key Equation Solvers for Generalized Integrated Interleaved Decoder. *Xie, Z., +, TCSI Jan. 2021 483-495*

- Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

- Hardware Implementation for Belief Propagation Flip Decoding of Polar Codes. *Ji, H., +, TCSI March 2021 1330-1341*

- Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications. *Zhan, M., +, TCSI June 2021 2688-2701*

- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*

Deep learning

- DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks. *Xie, R., +, TCSI Dec. 2021 5095-5107*

Deep learning (artificial intelligence)

- A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*

- A Real-Time Architecture for Pruning the Effectual Computations in Deep Neural Networks. *Asadikouhanjani, M., +, TCSI May 2021 2030-2041*

- BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*

- Fast and Accurate Inference on Microcontrollers With Boosted Cooperative Convolutional Neural Networks (BC-Net). *Mocerino, L., +, TCSI Jan. 2021 77-88*

- Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing. *Kim, S., +, TCSI May 2021 2017-2029*

- Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*

- Neural Network Training With Stochastic Hardware Models and Software Abstractions. *Zhang, B., +, TCSI April 2021 1532-1542*

- Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

- Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*

- TD-SRAM: Time-Domain-Based In-Memory Computing Macro for Binary Neural Networks. *Song, J., +, TCSI Aug. 2021 3377-3387*

- Variation-Aware SRAM Cell Optimization Using Deep Neural Network-Based Sensitivity Analysis. *Kwon, H., +, TCSI April 2021 1567-1577*

Degradation

- Automated Design Approximation to Overcome Circuit Aging. *Balaskas, K., +, TCSI Nov. 2021 4710-4721*

Delay filters

Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

Delay lock loops

A Fully Synthesizable Fractional- N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

Delays

A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD. *Chen, W., +, TCSI Nov. 2021 4566-4575*

Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme. *Tsai, K., +, TCSI Aug. 2021 3328-3336*

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Automated Design Approximation to Overcome Circuit Aging. *Balaskas, K., +, TCSI Nov. 2021 4710-4721*

Containment Control for Networked Fractional-Order Systems With Sampled Position Data. *Ye, Y., +, TCSI Sept. 2021 3881-3889*

Damping Power System Electromechanical Oscillations Using Time Delays. *Tzounas, G., +, TCSI June 2021 2725-2735*

Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021 1693-1705*

Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*

Dynamic Write V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI Dec. 2021 5038-5048*

Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays. *Wang, T., +, TCSI Nov. 2021 4520-4533*

Event-Triggered H_{∞} Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*

Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*

Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. *Boljanovic, V., +, TCSI April 2021 1727-1739*

Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*

Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. *Xu, Y., +, TCSI April 2021 1589-1598*

Global Event-Triggered Output Feedback Stabilization for a Class of Nonlinear Time-Delay Systems. *Shu, F., +, TCSI Oct. 2021 4371-4380*

Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*

Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*

Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*

Positivity and Stability of Cohen-Grossberg-Type Memristor Neural Networks With Unbounded Delays. *Wu, A., +, TCSI Nov. 2021 4508-4519*

Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays. *Yang, Y., +, TCSI May 2021 2148-2157*

Robust H_{∞} Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021 5156-5167*

Vector Wave Digital Filters and Their Application to Circuits With Two-Port Elements. *Bernardini, A., +, TCSI March 2021 1269-1282*

Delta-sigma modulation

A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*

Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback. *Pavan, S., +, TCSI Aug. 2021 3222-3231*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional- N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Folded Noise Prediction in Nonlinear Fractional- N Frequency Synthesizers. *Mazzaro, V., +, TCSI Oct. 2021 4038-4048*

Power Bound Analysis of a Two-Step MASH Incremental ADC Based on Noise-Shaping SAR ADCs. *Akbari, M., +, TCSI Aug. 2021 3133-3146*
Spur Immunity in MASH-Based Fractional- N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

Demodulation

Self-Synchronized DS/SS With High Spread Factors for Robust Millimeter-Wave Datalinks. *Tang, A., +, TCSI Sept. 2021 3941-3950*

Denial-of-service attack

Cyber-Physical Systems With Multiple Denial-of-Service Attackers: A Game-Theoretic Framework. *Huang, Y., +, TCSI Oct. 2021 4349-4359*

Design methodology

Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. *Butz, N., +, TCSI Oct. 2021 4013-4024*

Detection algorithms

Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*

Detector circuits

Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

Detectors

DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays. *Moitra, A., +, TCSI Nov. 2021 4482-4494*

Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*

Dielectric resonators

Balanced and Unbalanced Duplexers Using Common Oval Dielectric Resonators. *Wu, D., +, TCSI Aug. 2021 3211-3221*

Differential algebraic equations

Frequency Design of Lossless Passive Electronic Filters: A State-Space Formulation of the Direct Synthesis Approach. *Perodou, A., +, TCSI Jan. 2021 161-174*

Differential amplifiers

A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. *Guo, B., +, TCSI Jan. 2021 224-237*

Differential equations

Continuous-Time, Configurable Analog Linear System Solutions With Transconductance Amplifiers. *Hasler, J., +, TCSI Feb. 2021 765-775*

Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. *Fu, H., +, TCSI Nov. 2021 4495-4507*

Synthesis of an Equivalent Circuit for Spike-Timing-Dependent Axon Growth: What Fires Together Now Really Wires Together. *Ochs, K., +, TCSI Sept. 2021 3656-3667*

Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink. *Duan, N., +, TCSI April 2021 1610-1623*

Differentiation

Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*

Diffusion

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Digital arithmetic

A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*

A Two-Stage Operand Trimming Approximate Logarithmic Multiplier. *Pilipovic, R., +, TCSI June 2021 2535-2545*

Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme. *Tsai, K., +, TCSI Aug. 2021 3328-3336*

Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem. *Shabani, A., +, TCSI March 2021 1259-1268*

Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021 2522-2534*

High-Speed FPGA Implementation of SIKE Based on an Ultra-Low-Latency Modular Multiplier. *Tian, J., +, TCSI Sept. 2021 3719-3731*

Low-Complexity High-Precision Method and Architecture for Computing the Logarithm of Complex Numbers. *Chen, H., +, TCSI Aug. 2021 3293-3304*

LWRpro: An Energy-Efficient Configurable Crypto-Processor for Module-LWR. *Zhu, Y., +, TCSI March 2021 1146-1159*

Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*

Symmetric-Mapping LUT-Based Method and Architecture for Computing XY-Like Functions. *Chen, H., +, TCSI March 2021 1231-1244*

Ultralow-Latency VLSI Architecture Based on a Linear Approximation Method for Computing Nth Roots of Floating-Point Numbers. *Lyu, F., +, TCSI Feb. 2021 715-727*

Digital circuits

Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures. *Napoli, E., +, TCSI Oct. 2021 4142-4155*

Digital control

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*

A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

Analysis and Design of a CMOS Bidirectional Passive Vector-Modulated Phase Shifter. *Gu, P., +, TCSI April 2021 1398-1408*

Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*

Digital filters

An Active-Under-Coil RFDAC With Analog Linear Interpolation in 28-nm CMOS. *Zhang, F., +, TCSI May 2021 1855-1868*

An Optimized Radiation Tolerant Baseline Correction Filter for HEP Using AI Methodologies. *Sanches, B., +, TCSI May 2021 1789-1799*

Digital phase locked loops

A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalone, L., +, TCSI July 2021 2775-2786*

All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Digital signal processing chips

An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*

Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*

NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021 1892-1905*

Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform. *Xue, D., +, TCSI May 2021 2051-2059*

Digital-analog conversion

A 91.0-dB SFDR Single-Coarse Dual-Fine Pipelined-SAR ADC With Split-Based Background Calibration in 28-nm CMOS. *Cao, Y., +, TCSI Feb. 2021 641-654*

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

A Multi-Step Incremental Analog-to-Digital Converter With a Single Opamp and Two-Capacitor SAR Extended Counting. *Kuo, S., +, TCSI July 2021 2890-2899*

A Time-Based Pipelined ADC Using Integrate-and-Fire Multiplying-DAC. *Ryu, S., +, TCSI July 2021 2876-2889*

Advanced Mixed Signal Concepts Exploiting the Strong Body-Bias Effect in CMOS 22FDX®. *Wittenhagen, E., +, TCSI Jan. 2021 57-66*

An Active-Under-Coil RFDAC With Analog Linear Interpolation in 28-nm CMOS. *Zhang, F., +, TCSI May 2021 1855-1868*

Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback. *Pavan, S., +, TCSI Aug. 2021 3222-3231*

Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers. *Preissl, C., +, TCSI May 2021 2234-2245*

FPGA-Based Relaxation D/A Converters With Parasitics-Induced Error Suppression and Digital Self-Calibration. *Rubino, R., +, TCSI June 2021 2494-2507*

High-Resolution Wideband Vector-Sum Digital Phase Shifter With On-Chip Phase Linearity Enhancement Technology. *Zhou, J., +, TCSI June 2021 2457-2469*

MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators. *Nasrin, S., +, TCSI May 2021 1966-1978*

Directed graphs

Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*

Distributed Adaptive Resilient Formation Control of Uncertain Nonholonomic Mobile Robots Under Deception Attacks. *Wang, W., +, TCSI Sept. 2021 3822-3835*

Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*

Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*

Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021 444-457*

Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021 3069-3078*

Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy. *Wang, Y., +, TCSI Aug. 2021 3474-3484*

Direction-of-arrival estimation

Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. *Boljanovic, V., +, TCSI April 2021 1727-1739*

Towards Low Latency and Resource-Efficient FPGA Implementations of the MUSIC Algorithm for Direction of Arrival Estimation. *Butt, U.M., +, TCSI Aug. 2021 3351-3362*

Discrete cosine transforms

Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem. *Shabani, A., +, TCSI March 2021 1259-1268*

Discrete Fourier transforms

Fault Modeling and Efficient Testing of Memristor-Based Memory. *Liu, P., +, TCSI Nov. 2021 4444-4455*

Discrete time systems

H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021 818-828*

- Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021* 786-796
- Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021* 458-468
- Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021* 776-785
- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021* 856-867
- Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021* 2604-2615
- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021* 797-807
- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021* 2665-2674
- LMI-Based Robust Stability Analysis of Discrete-Time Fractional-Order Systems With Interval Uncertainties. *Zhu, Z., +, TCSI April 2021* 1671-1680
- Discrete transforms**
- Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform. *Xue, D., +, TCSI May 2021* 2051-2059
- Discrete wavelet transforms**
- Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021* 1814-1826
- Diseases**
- Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021* 2651-2664
- Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaftah, A., +, TCSI Aug. 2021* 3147-3157
- Distance measurement**
- A 1.25 μJ per Measurement Ultrasound Rangefinder System in 65 nm CMOS for Explorations With a Swarm of Sensor Nodes. *Berkol, G., +, TCSI April 2021* 1409-1420
- Distortion**
- An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021* 592-602
- Distributed control**
- Containment Control for Networked Fractional-Order Systems With Sampled Position Data. *Ye, Y., +, TCSI Sept. 2021* 3881-3889
- Distributed Control of Multi-Functional Grid-Tied Inverters for Power Quality Improvement. *Chen, J., +, TCSI Feb. 2021* 918-928
- Distributed Fault Detection and Control for Markov Jump Systems Over Sensor Networks With Round-Robin Protocol. *Gong, C., +, TCSI Aug. 2021* 3422-3435
- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021* 1646-1658
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021* 1659-1670
- Dynamic Triggering Mechanisms for Distributed Adaptive Synchronization Control and Its Application to Circuit Systems. *Xu, Y., +, TCSI May 2021* 2246-2256
- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021* 2121-2133
- Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021* 2639-2650
- Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021* 376-386
- Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021* 444-457
- Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021* 3069-3078
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021* 3436-3448
- Distributed parameter systems**
- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021* 3869-3880
- Distributed power generation**
- Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems. *Yang, Y., +, TCSI Jan. 2021* 434-443
- Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021* 1693-1705
- Distributed Control of Multi-Functional Grid-Tied Inverters for Power Quality Improvement. *Chen, J., +, TCSI Feb. 2021* 918-928
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021* 1659-1670
- Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021* 2639-2650
- Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021* 376-386
- Distributed processing**
- Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices. *Jhang, C., +, TCSI May 2021* 1773-1786
- Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO_2 -Based ReRAM Arrays. *Rafiq, S., +, TCSI July 2021* 2900-2910
- Doping profiles**
- A $+0.44^\circ\text{C}/-0.4^\circ\text{C}$ Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021* 1102-1113
- Dosimetry**
- 22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification. *Vallicelli, E.A., +, TCSI Jan. 2021* 3-13
- DRAM chips**
- A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021* 692-703
- A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021* 667-679
- CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021* 3184-3196
- Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM. *Kim, S.M., +, TCSI June 2021* 2546-2554
- Zero Aware Configurable Data Encoding by Skipping Transfer for Error Resilient Applications. *Jha, C.K., +, TCSI Aug. 2021* 3337-3350
- Driver circuits**
- 3–12-V Wide Input Range Adaptive Delay Compensated Active Rectifier for 6.78-MHz Loosely Coupled Wireless Power Transfer System. *Namgoong, G., +, TCSI June 2021* 2702-2713
- Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021* 4545-4555
- Drives**
- Multi-Objective Digital Design Optimization via Improved Drive Granularity Standard Cells. *Cao, L., +, TCSI Nov. 2021* 4660-4671
- Dual band**
- A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021* 4556-4565
- Dynamic programming**
- Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021* 1760-1768
- Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021* 3808-3821

Dynamic response

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

Dynamic scheduling

DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks. *Xie, R., +, TCSI Dec. 2021 5095-5107*

Dynamical systems

A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021 4924-4934*

Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation. *Xu, Y., +, TCSI Oct. 2021 4278-4286*

E**Economics**

Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. *Li, X., +, TCSI Oct. 2021 4268-4277*

Edge detection

Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays. *Rafiq, S., +, TCSI July 2021 2900-2910*

Eigenvalues and eigenfunctions

Frequency Splitting Elimination and Utilization in Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI Feb. 2021 929-939*

Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021 4900-4909*

Stability Assessment for Multi-Infeed Grid-Connected VSCs Modeled in the Admittance Matrix Form. *Orellana, L., +, TCSI Sept. 2021 3758-3771*

Towards Low Latency and Resource-Efficient FPGA Implementations of the MUSIC Algorithm for Direction of Arrival Estimation. *Butt, U.M., +, TCSI Aug. 2021 3351-3362*

Electric current control

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices. *Murad, M.A.A., +, TCSI July 2021 3079-3088*

Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*

The Analog Behavior of Pseudo Digital Ring Oscillators Used in VCO ADCs. *Borgmans, J., +, TCSI July 2021 2827-2840*

Electric current measurement

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Electric impedance

Frequency Selective Impedance Transformer With High-Impedance Transforming Ratio and Extremely High/Low Termination Impedances. *Jeong, Y., +, TCSI June 2021 2382-2392*

Electric potential

A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*

Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. *Butz, N., +, TCSI Oct. 2021 4013-4024*

Multi-Frequency Multi-Amplitude Superposition Modulation Method With Phase Shift Optimization for Single Inverter of Wireless Power Transfer System. *Wu, J., +, TCSI May 2021 2271-2279*

Electric resistance measurement

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Electric sensing devices

A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhraei, S.S., +, TCSI April 2021 1388-1397*

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Electric vehicles

Analysis and Design of EIT-Like Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI July 2021 3103-3113*

Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021 3857-3868*

Electrical stimulation

Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. *Butz, N., +, TCSI Oct. 2021 4013-4024*

Electrocardiography

Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*

Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device. *Lu, J., +, TCSI July 2021 2976-2985*

Electrochemical electrodes

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Electrochemical sensors

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Electrodes

Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. *Butz, N., +, TCSI Oct. 2021 4013-4024*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*

The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits. *Fahimi, Z., +, TCSI Oct. 2021 4090-4101*

Electromagnetic interference

Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021 4545-4555*

Electromagnetics

A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021 4556-4565*

Electron-hole recombination

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Electronic design automation

High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*

Electronic engineering computing

Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*

Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*

LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies. *Han, J., +, TCSI March 2021 1012-1022*

Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. *Klemme, F., +, TCSI June 2021 2569-2579*

Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*

Elemental semiconductors

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*

- Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*
- Elliptic curve cryptography**
- Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bishesh-Niasar, M., +, TCSI Nov. 2021 4648-4659*
- Embedded systems**
- Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*
- NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021 1892-1905*
- Encapsulation**
- Fast Strategies for the Implementation of SIKE Round 3 on ARM Cortex-M4. *Anastasova, M., +, TCSI Oct. 2021 4129-4141*
- Encoding**
- A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. *Eggemann, M., +, TCSI Oct. 2021 4116-4128*
- Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021 4207-4220*
- Zero Aware Configurable Data Encoding by Skipping Transfer for Error Resilient Applications. *Jha, C.K., +, TCSI Aug. 2021 3337-3350*
- Encryption**
- Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*
- Energy conservation**
- A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*
- A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*
- A 91.0-dB SFDR Single-Coarse Dual-Fine Pipelined-SAR ADC With Split-Based Background Calibration in 28-nm CMOS. *Cao, Y., +, TCSI Feb. 2021 641-654*
- A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning. *Li, S., +, TCSI April 2021 1543-1552*
- An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*
- Analysis and Design of EIT-Like Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI July 2021 3103-3113*
- Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*
- BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*
- Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*
- Design and Analysis of Approximate Compressors for Balanced Error Accumulation in MAC Operator. *Park, G., +, TCSI July 2021 2950-2961*
- Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing. *Kim, S., +, TCSI May 2021 2017-2029*
- Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*
- Neural Network Training With Stochastic Hardware Models and Software Abstractions. *Zhang, B., +, TCSI April 2021 1532-1542*
- Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*
- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021 536-549*
- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*
- Energy consumption**
- A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*
- A Two-Stage Operand Trimming Approximate Logarithmic Multiplier. *Pilipovic, R., +, TCSI June 2021 2535-2545*
- Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation. *Xu, Y., +, TCSI Oct. 2021 4278-4286*
- Energy harvesting**
- A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. *Zeng, Z., +, TCSI Sept. 2021 3587-3597*
- A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*
- A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021 4556-4565*
- An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*
- Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*
- Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*
- The Challenges and Emerging Technologies for Low-Power Artificial Intelligence IoT Systems. *Ye, L., +, TCSI Dec. 2021 4821-4834*
- Energy management systems**
- A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*
- Energy storage**
- A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*
- Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems. *Yang, Y., +, TCSI Jan. 2021 434-443*
- Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021 376-386*
- Entropy**
- A Metal-Via Resistance Based Physically Unclonable Function With Back-end Incremental ADC. *Park, B., +, TCSI Nov. 2021 4700-4709*
- Environmental monitoring (geophysics)**
- A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*
- Epidemics**
- Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*
- Equalizers**
- Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM. *Kim, S.M., +, TCSI June 2021 2546-2554*
- Equivalent circuits**
- Approximate Equivalent Circuits to Understand Tradeoffs in Geometry of On-Chip Inductors. *Leng, W., +, TCSI March 2021 975-988*
- Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*
- Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*
- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*
- Error analysis**
- A New Adaptive Sparse Pseudospectral Approximation Method and its Application for Stochastic Power Flow. *Lin, J., +, TCSI July 2021 3089-3102*
- Error correction**
- A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

Error correction codes

- Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. *Cui, H., +, TCSI Feb. 2021* 879-891
- Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021* 496-509
- Fast Nested Key Equation Solvers for Generalized Integrated Interleaved Decoder. *Xie, Z., +, TCSI Jan. 2021* 483-495
- Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021* 2183-2195
- Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications. *Zhan, M., +, TCSI June 2021* 2688-2701
- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021* 25-34

Error statistics

- Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021* 496-509
- Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021* 3495-3507
- Hardware Implementation for Belief Propagation Flip Decoding of Polar Codes. *Ji, H., +, TCSI March 2021* 1330-1341
- Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021* 2183-2195
- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021* 25-34

Estimation

- A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. *Juracy, L.R., +, TCSI Nov. 2021* 4783-4795
- Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021* 4207-4220
- Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation. *Xu, Y., +, TCSI Oct. 2021* 4278-4286
- Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021* 4324-4336
- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021* 1297-1307

F**Failure analysis**

- SymBIST*: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021* 2580-2593
- Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI March 2021* 1171-1182
- Failure in Ring Oscillators With Capacitive Load. *Ravezzi, L., TCSI Aug. 2021* 3388-3396
- Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021* 1990-2002

Fast Fourier transforms

- Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021* 3031-3043

The Constant Multiplier FFT. *Garrido, M., +, TCSI Jan. 2021* 322-335

Fault diagnosis

- Adaptive Fast Fault Location for Open-Switch Faults of Voltage Source Inverter. *Yin, H., +, TCSI Sept. 2021* 3965-3974
- Adaptive Fault Estimation for Unmanned Surface Vessels With a Neural Network Observer Approach. *Chen, L., +, TCSI Jan. 2021* 416-425
- Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021* 2158-2170
- Distributed Fault Detection and Control for Markov Jump Systems Over Sensor Networks With Round-Robin Protocol. *Gong, C., +, TCSI Aug. 2021* 3422-3435
- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021* 1646-1658

Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021* 2841-2849

Fault location

- Adaptive Fast Fault Location for Open-Switch Faults of Voltage Source Inverter. *Yin, H., +, TCSI Sept. 2021* 3965-3974

Fault tolerance

- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021* 1646-1658

Fault tolerant computing

- Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021* 2522-2534

Fault tolerant control

- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021* 1659-1670

- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021* 1297-1307

Feature extraction

- A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021* 1045-1054

- An 800 nW Switched-Capacitor Feature Extraction Filterbank for Sound Classification. *Villamizar, D.A., +, TCSI April 2021* 1578-1588

- Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021* 4207-4220

- NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Networks for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021* 1892-1905

- Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA. *Wang, C., +, TCSI Oct. 2021* 4182-4193

- Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021* 2841-2849

Feedback

- A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021* 950-962

- Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021* 2626-2638

- Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021* 2171-2182

- Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021* 1760-1768

- Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems. *Shu, F., +, TCSI Feb. 2021* 808-817

- Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021* 3058-3068

- Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms. *Song, X., +, TCSI Jan. 2021* 363-375

- Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021* 3901-3912

- Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021* 3069-3078

- State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021* 3846-3856

- Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021* 396-405

Feedback amplifiers

- Analysis and Design of Lossy Capacitive Over-Neutralization Technique for Amplifiers Operating Near f_{MAX} . *Simic, D., +, TCSI May 2021* 1945-1955

- Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021* 3693-3706

Feedforward

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

Ferroelectric devices

Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*

Field effect MIMIC

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*

A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator. *Shu, Y., +, TCSI June 2021 2341-2353*

Analysis and Design of Lossy Capacitive Over-Neutralization Technique for Amplifiers Operating Near f_{MAX} . *Simic, D., +, TCSI May 2021 1945-1955*

Field effect MMIC

A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. *Zhong, J., +, TCSI May 2021 1869-1880*

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

Field effect transistor circuits

On the Resiliency of NCFET Circuits Against Voltage Over-Scaling. *Pain, G., +, TCSI April 2021 1481-1492*

Field effect transistors

From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*

High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021 301-310*

Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

Field programmable analog arrays

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Field programmable gate arrays

A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning. *Li, S., +, TCSI April 2021 1543-1552*

A Low-Area and Low-Power Comma Detection and Word Alignment Circuits for JESD204B/C Controller. *Yin, P., +, TCSI July 2021 2925-2935*

A Mixed-Pruning Based Framework for Embedded Convolutional Neural Network Acceleration. *Chang, X., +, TCSI April 2021 1706-1715*

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*

An Efficient Digital Realization of Retinal Light Adaptation in Cone Photoreceptors. *Ghanbarpour, M., +, TCSI Dec. 2021 5072-5080*

An Energy Efficient Accelerator for Bidirectional Recurrent Neural Networks (BiRNNs) Using Hybrid-Iterative Compression With Error Sensitivity. *Nan, G., +, TCSI Sept. 2021 3707-3718*

Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

Dynamic Dataflow Scheduling and Computation Mapping Techniques for Efficient Depthwise Separable Convolution Acceleration. *Li, B., +, TCSI Aug. 2021 3279-3292*

Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021 2522-2534*

FPGA-Based Relaxation D/A Converters With Parasitics-Induced Error Suppression and Digital Self-Calibration. *Rubino, R., +, TCSI June 2021 2494-2507*

Hardware Architecture for Supersingular Isogeny Diffie-Hellman and Key Encapsulation Using a Fast Montgomery Multiplier. *Farzam, M., +, TCSI May 2021 2042-2050*

Hardware Self-Organizing Map Based on Digital Frequency-Locked Loop and Triangular Neighborhood Function. *Hikawa, H., TCSI March 2021 1245-1258*

Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021 3732-3745*

Hardware-Efficient Emulation of Leaky Integrate-and-Fire Model Using Template-Scaling-Based Exponential Function Approximation. *Kim, J., +, TCSI Jan. 2021 350-362*

High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*

High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions. *Haghiri, S., +, TCSI Jan. 2021 275-287*

High-Speed FPGA Implementation of SIKE Based on an Ultra-Low-Latency Modular Multiplier. *Tian, J., +, TCSI Sept. 2021 3719-3731*

High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*

Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021 4648-4659*

Low-Latency Hardware Accelerator for Improved Engle-Granger Cointegration in Pairs Trading. *Liang, S., +, TCSI July 2021 2911-2924*

LWRpro: An Energy-Efficient Configurable Crypto-Processor for Module-LWR. *Zhu, Y., +, TCSI March 2021 1146-1159*

Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA. *Wang, C., +, TCSI Oct. 2021 4182-4193*

Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications. *Zhan, M., +, TCSI June 2021 2688-2701*

RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Symmetric-Mapping LUT-Based Method and Architecture for Computing XY-Like Functions. *Chen, H., +, TCSI March 2021 1231-1244*

The Constant Multiplier FFT. *Garrido, M., +, TCSI Jan. 2021 322-335*

Towards Low Latency and Resource-Efficient FPGA Implementations of the MUSIC Algorithm for Direction of Arrival Estimation. *Butt, U.M., +, TCSI Aug. 2021 3351-3362*

Filtering theory

Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*

Demonstrating Filtered Feedback Control Near a Boundary Crisis. *Meucci, R., +, TCSI July 2021 3023-3030*

Filters

A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler. *Jiang, W., +, TCSI Feb. 2021 557-568*

Low Delay Short Word Length Sigma Delta Active Noise Control. *Lopes, P.A.C., +, TCSI Sept. 2021 3746-3757*

Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform. *Xue, D., +, TCSI May 2021 2051-2059*

FinFETs

Dynamic Write V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI Dec. 2021 5038-5048*

PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits. *Salamin, S., +, TCSI Oct. 2021 4299-4309*

Finite difference time-domain analysis

A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs. *Malavipathirana, H., +, TCSI Aug. 2021 3363-3376*

FIR filters

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

A Multi-Step Incremental Analog-to-Digital Converter With a Single Opamp and Two- Capacitor SAR Extended Counting. *Kuo, S., +, TCSI July 2021 2890-2899*

Broadband Mismatch Calibration for Time-Interleaved ADC Based on Linear Frequency Modulated Signal. *Peng, X., +, TCSI Sept. 2021 3621-3630*

- Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback.
Pavan, S., +, TCSI Aug. 2021 3222-3231
- Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*
- High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*
- Flash memories**
Characterization of Inter-Cell Interference in 3D NAND Flash Memory.
Park, S.K., +, TCSI March 2021 1183-1192
- Flexible electronics**
Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*
- Flicker noise**
A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*
- A Generalization of the Groszkowski's Result in Differential Oscillator Topologies. *Buccolieri, F., +, TCSI July 2021 2800-2812*
- Flip-flops**
Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*
- High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*
- Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*
- Floating point arithmetic**
Symmetric-Mapping LUT-Based Method and Architecture for Computing XY-Like Functions. *Chen, H., +, TCSI March 2021 1231-1244*
- Ultralow-Latency VLSI Architecture Based on a Linear Approximation Method for Computing Nth Roots of Floating-Point Numbers. *Lyu, F., +, TCSI Feb. 2021 715-727*
- Fluorescence**
Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*
- Forward error correction**
Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021 496-509*
- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*
- Fourier transforms**
Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*
- Frequency control**
Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021 1693-1705*
- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*
- Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021 3857-3868*
- Frequency dividers**
Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*
- MASH-Based Divider Controllers for Mitigation of Wandering Spurs in a Fractional-N Frequency Synthesizer. *Mai, D., +, TCSI Jan. 2021 126-137*
- Frequency division multiplexing**
Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems. *Ranjandish, R., +, TCSI April 2021 1740-1749*
- Frequency locked loops**
Hardware Self-Organizing Map Based on Digital Frequency-Locked Loop and Triangular Neighborhood Function. *Hikawa, H., TCSI March 2021 1245-1258*
- Frequency measurement**
Portable CMOS NMR System With 50-kHz IF, 10-μs Dead Time, and Frequency Tracking. *Hong, S., +, TCSI Nov. 2021 4576-4588*
- Frequency modulation**
An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. *Donnelly, Y., +, TCSI Oct. 2021 4259-4267*
- Broadband Mismatch Calibration for Time-Interleaved ADC Based on Linear Frequency Modulated Signal. *Peng, X., +, TCSI Sept. 2021 3621-3630*
- Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*
- Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*
- Frequency response**
Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*
- Generalized Relationship Between Frequency Response and Settling Time of CMOS OTAs: Toward Many-Stage Design. *Mohammed, M.A., +, TCSI Dec. 2021 4993-5006*
- NbO₂-Mott Memristor: A Circuit-Theoretic Investigation. *Messaris, I., +, TCSI Dec. 2021 4979-4992*
- Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*
- Frequency shift keying**
Portable CMOS NMR System With 50-kHz IF, 10-μs Dead Time, and Frequency Tracking. *Hong, S., +, TCSI Nov. 2021 4576-4588*
- Frequency synthesizers**
An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. *Donnelly, Y., +, TCSI Oct. 2021 4259-4267*
- Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*
- Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers. *Preissl, C., +, TCSI May 2021 2234-2245*
- Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers. *Mazzaro, V., +, TCSI Oct. 2021 4038-4048*
- MASH-Based Divider Controllers for Mitigation of Wandering Spurs in a Fractional-N Frequency Synthesizer. *Mai, D., +, TCSI Jan. 2021 126-137*
- Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*
- Frequency-domain analysis**
Comments on “Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters”. *Han, G., +, TCSI Jan. 2021 550-552*
- Experimental Study of Fractional-Order RC Circuit Model Using the Caputo and Caputo-Fabrizio Derivatives. *Lin, D., +, TCSI March 2021 1034-1044*
- Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. *Boljanovic, V., +, TCSI April 2021 1727-1739*
- Nonlinear Analysis of Cross-Coupled Super-Regenerative Oscillators. *Ferschischi, A., +, TCSI June 2021 2368-2381*
- Reply to Comments on “Architectural Evolution of Integrated M-Phase High-Q Bandpass Filters”. *Mirzaei, A., +, TCSI Jan. 2021 553*
- Universal Frequency-Domain Analysis of N-Path Networks. *Tymchenko, M., +, TCSI Feb. 2021 569-580*
- Friction**
Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. *Cui, Y., +, TCSI Oct. 2021 4360-4370*
- Function approximation**
Hardware-Efficient Emulation of Leaky Integrate-and-Fire Model Using Template-Scaling-Based Exponential Function Approximation. *Kim, J., +, TCSI Jan. 2021 350-362*
- MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators. *Nasrin, S., +, TCSI May 2021 1966-1978*
- Fuzzy control**
Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021 2626-2638*
- Adaptive Practical Fixed-Time Tracking Control With Prescribed Boundary Constraints. *Chen, M., +, TCSI April 2021 1716-1726*

- Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*
- Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*
- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021 1297-1307*
- Fuzzy logic**
- Adaptive Fuzzy Fast Finite-Time Dynamic Surface Tracking Control for Nonlinear Systems. *Wang, H., +, TCSI Oct. 2021 4337-4348*
- Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021 2626-2638*
- Adaptive Practical Fixed-Time Tracking Control With Prescribed Boundary Constraints. *Chen, M., +, TCSI April 2021 1716-1726*
- Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*
- G**
- Gain**
- Millimeter-Wave Integrated Phased Arrays. *Zhao, D., +, TCSI Oct. 2021 3977-3990*
- Gallium arsenide**
- Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*
- Gallium compounds**
- A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*
- A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021 2714-2724*
- A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*
- Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*
- Games**
- Cyber-Physical Systems With Multiple Denial-of-Service Attackers: A Game-Theoretic Framework. *Huang, Y., +, TCSI Oct. 2021 4349-4359*
- Gas sensors**
- A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*
- Gaussian distribution**
- A High-Performance Bidirectional Architecture for the Quasi-Comparison-Free Sorting Algorithm. *Chen, W., +, TCSI April 2021 1493-1506*
- Dynamic Write V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI Dec. 2021 5038-5048*
- Gaussian processes**
- Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays. *Rafiq, S., +, TCSI July 2021 2900-2910*
- Ge-Si alloys**
- Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021 3668-3681*
- Generators**
- Generating Any Number of Diversified Hidden Attractors via Memristor Coupling. *Zhang, S., +, TCSI Dec. 2021 4945-4956*
- Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. *Mikulic, J., +, TCSI Oct. 2021 4076-4089*
- Stochastic Dividers for Low Latency Neural Networks. *Liu, S., +, TCSI Oct. 2021 4102-4115*
- Genetic algorithms**
- A Foreground Calibration for M-Channel Time-Interleaved Analog-to-Digital Converters Based on Genetic Algorithm. *Tavares, Y.A., +, TCSI April 2021 1444-1457*
- General Efficient TMR for Combinational Circuit Hardening Against Soft Errors and Improved Multi-Objective Optimization Framework. *Tan, C., +, TCSI July 2021 3044-3057*
- Geometry**
- A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*
- Gesture recognition**
- A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*
- Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA. *Wang, C., +, TCSI Oct. 2021 4182-4193*
- Gradient methods**
- Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*
- Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*
- Graph coloring**
- Improved Vertex Coloring With NbO_x Memristor-Based Oscillatory Networks. *Weilher, M., +, TCSI May 2021 2082-2095*
- Graph theory**
- Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*
- Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*
- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*
- Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021 444-457*
- Graphics processing units**
- Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021 238-249*
- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*
- H**
- H^∞ control**
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021 1659-1670*
- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021 1297-1307*
- Haar transforms**
- Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*
- Hadamard transforms**
- Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems. *Ranjandish, R., +, TCSI April 2021 1740-1749*
- Hafnium compounds**
- Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays. *Rafiq, S., +, TCSI July 2021 2900-2910*
- Hall effect devices**
- A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhraei, S.S., +, TCSI April 2021 1388-1397*
- Hall effect transducers**
- A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhraei, S.S., +, TCSI April 2021 1388-1397*
- Hamming codes**
- Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021 496-509*
- Hardware**
- A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Congestion-Aware Router. *Pu, J., +, TCSI Dec. 2021 5081-5094*

- A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing. *Younes, H., +, TCSI Oct. 2021 4232-4244*
- BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks. *Tran, T.H., +, TCSI Oct. 2021 4245-4258*
- Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021 4207-4220*
- DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays. *Moitra, A., +, TCSI Nov. 2021 4482-4494*
- IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*
- Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021 4648-4659*
- Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*
- Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA. *Wang, C., +, TCSI Oct. 2021 4182-4193*
- Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. *Kwon, E., +, TCSI Oct. 2021 4156-4169*
- Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. *Liu, Y., +, TCSI Oct. 2021 4194-4206*
- Stochastic Dividers for Low Latency Neural Networks. *Liu, S., +, TCSI Oct. 2021 4102-4115*
- Hardware acceleration**
- A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. *Juracy, L.R., +, TCSI Nov. 2021 4783-4795*
- DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks. *Xie, R., +, TCSI Dec. 2021 5095-5107*
- Hardware accelerators**
- An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*
- Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*
- Dynamic Dataflow Scheduling and Computation Mapping Techniques for Efficient Depthwise Separable Convolution Acceleration. *Li, B., +, TCSI Aug. 2021 3279-3292*
- Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*
- Hardware description languages**
- A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021 2003-2016*
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- High-Speed FPGA Implementation of SIKE Based on an Ultra-Low-Latency Modular Multiplier. *Tian, J., +, TCSI Sept. 2021 3719-3731*
- Hardware-software codesign**
- Neural Network Training With Stochastic Hardware Models and Software Abstractions. *Zhang, B., +, TCSI April 2021 1532-1542*
- Harmonic distortion**
- A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*
- A Capacitively Coupled CT Δ ΣM With Chopping Artifacts Rejection for Sensor Readout ICs. *Lim, C., +, TCSI Aug. 2021 3242-3253*
- Harmonics suppression**
- Two- and Three-Way Filtering Power Dividers With Harmonic Suppression Using Triangle Patch Resonator. *Zhu, Y., +, TCSI Dec. 2021 5007-5017*
- Health care**
- Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device. *Lu, J., +, TCSI July 2021 2976-2985*
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- Synthesis of an Equivalent Circuit for Spike-Timing-Dependent Axon Growth: What Fires Together Now Really Wires Together. *Ochs, K., +, TCSI Sept. 2021 3656-3667*

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- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*

HEMT integrated circuits

- Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

Heterojunction bipolar transistors

- Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Heuristic algorithms

- Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence. *Han, T., +, TCSI Oct. 2021 4393-4402*

Hierarchical systems

- Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021 376-386*

High definition video

- Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem. *Shabani, A., +, TCSI March 2021 1259-1268*

High electron mobility transistors

- A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*

- Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*

High energy physics instrumentation computing

- An Optimized Radiation Tolerant Baseline Correction Filter for HEP Using AI Methodologies. *Sanches, B., +, TCSI May 2021 1789-1799*

High-pass filters

- Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

High-temperature electronics

- A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*

Hopfield neural networks

- Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021 4970-4978*

- Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. *Lin, H., +, TCSI Aug. 2021 3397-3410*

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I

IEC Standards

- IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*

IEEE Standards

- Damping Power System Electromechanical Oscillations Using Time Delays. *Tzounas, G., +, TCSI June 2021 2725-2735*

- Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices. *Murad, M.A.A., +, TCSI July 2021 3079-3088*

Image classification

- Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification. *Tannirkulam Chandrasekaran, S., +, TCSI March 2021 1023-1033*

- High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*

- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Image coding

- High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*

Image color analysis

Real-Time Block-Based Embedded CNN for Gesture Classification on an FPGA. *Wang, C., +, TCSI Oct. 2021 4182-4193*

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Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*

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High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*

Image processing

A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning. *Li, S., +, TCSI April 2021 1543-1552*

Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme. *Tsai, K., +, TCSI Aug. 2021 3328-3336*

Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*

Image recognition

A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*

A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*

CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*

Implementation of Ternary Weights With Resistive RAM Using a Single Sense Operation Per Synapse. *Laborieux, A., +, TCSI Jan. 2021 138-147*

Image reconstruction

Generalized Analog-to-Information Converter With Analysis Sparse Prior. *Qian, H., +, TCSI Sept. 2021 3574-3586*

Image resolution

Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification. *Tannirkulam Chandrasekaran, S., +, TCSI March 2021 1023-1033*

Image segmentation

RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Image sensors

RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*

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RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Impedance

A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*

A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021 4556-4565*

Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power. *Guo, L., +, TCSI Dec. 2021 5194-5205*

An Interstage-Reflectionless V-Band Radiometer With Capacitor-Reused Absorptive Matching in 0.13- μ m SiGe BiCMOS. *Bi, X., +, TCSI Nov. 2021 4589-4602*

Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless. *Regev, D., +, TCSI Dec. 2021 5168-5181*

High-Dimensional Extension of the TICER Algorithm. *Hao, L., +, TCSI Nov. 2021 4722-4734*

Impedance converters

Frequency Selective Impedance Transformer With High-Impedance Transforming Ratio and Extremely High/Low Termination Impedances. *Jeong, Y., +, TCSI June 2021 2382-2392*

Impedance matching

A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*

An Interstage-Reflectionless V-Band Radiometer With Capacitor-Reused Absorptive Matching in 0.13- μ m SiGe BiCMOS. *Bi, X., +, TCSI Nov. 2021 4589-4602*

An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

Design of a Quadband Doherty Power Amplifier With Large Power Back-Off Range. *Zhang, Z., +, TCSI Sept. 2021 3598-3610*

Design of Multi-Port With Desired Reference Impedances Using Y-Matrix and Matching Networks. *Sinha, R., TCSI May 2021 2096-2106*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

Impedance matrix

Design of Multi-Port With Desired Reference Impedances Using Y-Matrix and Matching Networks. *Sinha, R., TCSI May 2021 2096-2106*

Indium compounds

High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021 264-274*

Inductive power transmission

3–12-V Wide Input Range Adaptive Delay Compensated Active Rectifier for 6.78-MHz Loosely Coupled Wireless Power Transfer System. *Namgoong, G., +, TCSI June 2021 2702-2713*

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*

A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*

Analysis and Design of EIT-Like Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI July 2021 3103-3113*

Frequency Splitting Elimination and Utilization in Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI Feb. 2021 929-939*

Impedance Shaping Control Strategy for Wireless Power Transfer System Based on Dynamic Small-Signal Analysis. *Tan, T., +, TCSI March 2021 1354-1365*

Multi-Frequency Multi-Amplitude Superposition Modulation Method With Phase Shift Optimization for Single Inverter of Wireless Power Transfer System. *Wu, J., +, TCSI May 2021 2271-2279*

Inductors

A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*

A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021 4556-4565*

An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schroeder, D., +, TCSI May 2021 1800-1813*

Approximate Equivalent Circuits to Understand Tradeoffs in Geometry of On-Chip Inductors. *Leng, W., +, TCSI March 2021 975-988*

Synthesis of Constant Power Loads Using Switching Converters Under Sliding-Mode Control. *Martinez-Trevino, B.A., +, TCSI Jan. 2021 524-535*

Inference mechanisms

A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*

A Real-Time Architecture for Pruning the Effectual Computations in Deep Neural Networks. *Asadikouhanjani, M., +, TCSI May 2021 2030-2041*

RRAM for Compute-in-Memory: From Inference to Training. *Yu, S., +, TCSI July 2021 2753-2765*

Infrared detectors

Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*

Infrared imaging

Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*

Innovation management

Evaluating Performances and Importance of Venture Capitals: A Complex Network Approach. *Liu, J., +, TCSI May 2021 2060-2068*

Instrumentation amplifiers

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A Capacitively Coupled CT Δ Σ M With Chopping Artifacts Rejection for Sensor Readout ICs. *Lim, C., +, TCSI Aug. 2021 3242-3253*

A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhraei, S.S., +, TCSI April 2021 1388-1397*

Instruments

Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021 3991-4000*

Integrated circuit design

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

SymbIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021 2580-2593*

A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*

A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021 1045-1054*

A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*

A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler. *Jiang, W., +, TCSI Feb. 2021 557-568*

A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021 692-703*

A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs. *Malavipathirana, H., +, TCSI Aug. 2021 3363-3376*

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

A Generalization of the Groszkowski's Result in Differential Oscillator Topologies. *Buccolieri, F., +, TCSI July 2021 2800-2812*

A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021 2003-2016*

A Time-Division-Multiplexed Clocked-Analog Low-Dropout Regulator. *Xie, Z., +, TCSI March 2021 1366-1376*

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

An 8-Bit 800 MS/s Loop-Unrolled SAR ADC With Common-Mode Adaptive Background Offset Calibration in 28 nm FDSOI. *Akkaya, A., +, TCSI July 2021 2766-2774*

An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*

An Optimized Radiation Tolerant Baseline Correction Filter for HEP Using AI Methodologies. *Sanches, B., +, TCSI May 2021 1789-1799*

An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021 3668-3681*

Analysis and Design of Lossy Capacitive Over-Neutralization Technique for Amplifiers Operating Near f_{MAX} . *Simic, D., +, TCSI May 2021 1945-1955*

Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*

Applications of Artificial Intelligence on the Modeling and Optimization for Analog and Mixed-Signal Circuits: A Review. *Fayazi, M., +, TCSI June 2021 2418-2431*

Asynchronous Event-Driven Clocking and Control in Pipelined ADCs. *Hershberg, B., +, TCSI July 2021 2813-2826*

Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices. *Jhang, C., +, TCSI May 2021 1773-1786*

Continuous-Time, Configurable Analog Linear System Solutions With Transconductance Amplifiers. *Hasler, J., +, TCSI Feb. 2021 765-775*

Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*

Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*

Design of Soft-Error-Aware SRAM With Multi-Node Upset Recovery for Aerospace Applications. *Pal, S., +, TCSI June 2021 2470-2480*

Design of Three-Stage OTA Based on Settling-Time Requirements Including Large and Small Signal Behavior. *Giustolisi, G., +, TCSI March 2021 998-1011*

Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI March 2021 1171-1182*

From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*

Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification. *Tannirkulam Chandrasekaran, S., +, TCSI March 2021 1023-1033*

Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM. *Kim, S.M., +, TCSI June 2021 2546-2554*

Impact of Analog Non-Idealities on the Design Space of 6T-SRAM Current-Domain Dot-Product Operators for In-Memory Computing. *Kneip, A., +, TCSI May 2021 1931-1944*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*

Low-Complexity High-Precision Method and Architecture for Computing the Logarithm of Complex Numbers. *Chen, H., +, TCSI Aug. 2021 3293-3304*

Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

Magnetoresistive Circuits and Systems: Embedded Non-Volatile Memory to Crossbar Arrays. *Agrawal, A., +, TCSI June 2021 2281-2294*

MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators. *Nasrin, S., +, TCSI May 2021 1966-1978*

Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*

Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*

Power-Speed Trade-Offs in Design of Scaled FET Circuits Using C/I_{DS} Methodology. *Tajalli, A., TCSI Feb. 2021 631-640*

Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*

SRIIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. *Singh, A., +, TCSI May 2021 1917-1930*

Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*

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LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies. *Han, J., +, TCSI March 2021 1012-1022*

Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*

Integrated circuit manufacture

A 0.003-mm² 440f_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

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77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

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A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021 4889-4899*

Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power. *Guo, L., +, TCSI Dec. 2021 5194-5205*

An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. *Donnelly, Y., +, TCSI Oct. 2021 4259-4267*

Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021 5061-5071*

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Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*

Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. *Menkad, T., +, TCSI Dec. 2021 5120-5133*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. *Maheshwari, S., +, TCSI Dec. 2021 4876-4888*

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Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets. *Li, H., +, TCSI Oct. 2021 4170-4181*

Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*

Discrete Memristor Hyperchaotic Maps. *Bao, H., +, TCSI Nov. 2021 4534-4544*

Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI March 2021 1171-1182*

Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor—Part I. *Huang, T., +, TCSI Nov. 2021 4417-4418*

Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter. *Shi, B., +, TCSI Nov. 2021 4746-4759*

Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits. *Khan, R.A., +, TCSI Oct. 2021 4025-4037*

Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*

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How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation. *Kang, S.M., +, TCSI Dec. 2021 4837-4850*

Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021 4970-4978*

Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*

Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021 4900-4909*

Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. *Vahdat, S., +, TCSI Oct. 2021 4310-4323*

Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*

Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. *Fu, H., +, TCSI Nov. 2021 4495-4507*

Integrated circuit noise

A Generalization of the Groszkowski's Result in Differential Oscillator Topologies. *Buccolieri, F., +, TCSI July 2021 2800-2812*

An 8-Bit 800 MS/s Loop-Unrolled SAR ADC With Common-Mode Adaptive Background Offset Calibration in 28 nm FDSOI. *Akkaya, A., +, TCSI July 2021 2766-2774*

Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*

Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*

Integrated circuit packaging

Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*

Integrated circuit reliability

SymbIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021 2580-2593*

Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*

Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets. *Li, H., +, TCSI Oct. 2021 4170-4181*

Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*

NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs. *Wang, A., +, TCSI June 2021 2508-2521*

Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*

SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. *Singh, A., +, TCSI May 2021 1917-1930*

Integrated circuit testing

SymbIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021 2580-2593*

A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*

Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*

Integrated circuit yield

Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI March 2021 1171-1182*

Integrated circuits

Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy. *Wu, Y., +, TCSI Nov. 2021 4735-4745*

Integrated memory circuits

Characterization of Inter-Cell Interference in 3D NAND Flash Memory. *Park, S.K., +, TCSI March 2021 1183-1192*

Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*

Intercell interference

Characterization of Inter-Cell Interference in 3D NAND Flash Memory. *Park, S.K., +, TCSI March 2021 1183-1192*

Interference

Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021 5156-5167*

Interference cancellation

Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless. *Regev, D., +, TCSI Dec. 2021 5168-5181*

Interference suppression

A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. *Zhong, J., +, TCSI May 2021 1869-1880*

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

Balanced and Unbalanced Duplexers Using Common Oval Dielectric Resonators. *Wu, D., +, TCSI Aug. 2021 3211-3221*

CMOS Full-Duplex Mixer-First Receiver With Adaptive Self-Interference Cancellation. *Ayati, S., +, TCSI Feb. 2021 868-878*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

Interleaved codes

Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*

Intermodulation

Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters. *Liu, X., +, TCSI July 2021 2998-3011*

Intermodulation distortion

A 660 MHz-5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*

International trade

Low-Latency Hardware Accelerator for Improved Engle-Granger Cointegration in Pairs Trading. *Liang, S., +, TCSI July 2021 2911-2924*

Internet of Things

A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*

A Real-Time Architecture for Pruning the Effectual Computations in Deep Neural Networks. *Asadikouhanjani, M., +, TCSI May 2021 2030-2041*

Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

Stochastic Dividers for Low Latency Neural Networks. *Liu, S., +, TCSI Oct. 2021 4102-4115*

The Challenges and Emerging Technologies for Low-Power Artificial Intelligence IoT Systems. *Ye, L., +, TCSI Dec. 2021 4821-4834*

Interpolation

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

An Active-Under-Coil RFDAC With Analog Linear Interpolation in 28-nm CMOS. *Zhang, F., +, TCSI May 2021 1855-1868*

Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*

Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures. *Napoli, E., +, TCSI Oct. 2021 4142-4155*

Inverters

A T-Type Switched-Capacitor Multilevel Inverter With Low Voltage Stress and Self-Balancing. *Wang, Y., +, TCSI May 2021 2257-2270*

Adaptive Fast Fault Location for Open-Switch Faults of Voltage Source Inverter. *Yin, H., +, TCSI Sept. 2021 3965-3974*

Distributed Control of Multi-Functional Grid-Tied Inverters for Power Quality Improvement. *Chen, J., +, TCSI Feb. 2021 918-928*

Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021 301-310*

Loading-Aware Reliability Improvement of Ultra-Low Power Memristive Neural Networks. *Vahdat, S., +, TCSI Aug. 2021 3411-3421*

Multi-Frequency Multi-Amplitude Superposition Modulation Method With Phase Shift Optimization for Single Inverter of Wireless Power Transfer System. *Wu, J., +, TCSI May 2021 2271-2279*

Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*

Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. *Vahdat, S., +, TCSI Oct. 2021 4310-4323*

Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021 5156-5167*

Investment

Evaluating Performances and Importance of Venture Capitals: A Complex Network Approach. *Liu, J., +, TCSI May 2021 2060-2068*

Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. *Li, X., +, TCSI Oct. 2021 4268-4277*

Iron

PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits. *Salamin, S., +, TCSI Oct. 2021 4299-4309*

ISO

A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*

Iterative methods

An Energy Efficient Accelerator for Bidirectional Recurrent Neural Networks (BiRNNs) Using Hybrid-Iterative Compression With Error Sensitivity. *Nan, G., +, TCSI Sept. 2021 3707-3718*

Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021 4910-4923*

Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*

Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm. *Wang, Q., +, TCSI Sept. 2021 3794-3807*

Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

Using Strictly Dissipative Impedance Coupling in the Waveform Relaxation Method for the Analysis of Interconnect Circuits. *Menkad, T., +, TCSI March 2021 1283-1296*

J**Jacobian matrices**

A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing. *Younes, H., +, TCSI Oct. 2021 4232-4244*

Jamming

Probabilistic-Constrained H_∞ Tracking Control for a Class of Stochastic Nonlinear Systems Subject to DoS Attacks and Measurement Outliers. *Wei, B., +, TCSI Oct. 2021 4381-4392*

Jitter

A 0.003-mm² 440fs_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. *Zhao, X., +, TCSI Jan. 2021 89-102*

A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator. *Shu, Y., +, TCSI June 2021 2341-2353*

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalone, L., +, TCSI July 2021 2775-2786*

- A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*
- Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021 3668-3681*
- Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021 3485-3494*
- High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*
- Jitter-Power Trade-Offs in PLLs. *Razavi, B., TCSI April 2021 1381-1387*

Junctions

- A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021 4556-4565*
- Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power. *Guo, L., +, TCSI Dec. 2021 5194-5205*

K**Kalman filters**

- Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*

L**Ladder filters**

- An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Laplace transforms

- Experimental Study of Fractional-Order RC Circuit Model Using the Caputo and Caputo-Fabrizio Derivatives. *Lin, D., +, TCSI March 2021 1034-1044*

Large scale integration

- Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021 5061-5071*

Lattice theory

- Lattice Trajectory Piecewise Linear Method for the Simulation of Diode Circuits. *Wang, J., +, TCSI May 2021 2069-2081*

Layout

- A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC. *Park, B., +, TCSI Nov. 2021 4700-4709*

- Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. *Maheshwari, S., +, TCSI Dec. 2021 4876-4888*

- Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*

LC circuits

- A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. *Zhong, J., +, TCSI May 2021 1869-1880*

- A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

Leakage currents

- A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*

Learning (artificial intelligence)

- A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

- A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning. *Li, S., +, TCSI April 2021 1543-1552*

- An 800 nW Switched-Capacitor Feature Extraction Filterbank for Sound Classification. *Villamizar, D.A., +, TCSI April 2021 1578-1588*

- CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*

- Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. *Tao, T., +, TCSI May 2021 1906-1916*

- Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021 2522-2534*

- Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification. *Tannirkulam Chandrasekaran, S., +, TCSI March 2021 1023-1033*

- Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Distortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*

- Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*

- Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

- Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. *Klemme, F., +, TCSI June 2021 2569-2579*

- Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*

- RRAM for Compute-in-Memory: From Inference to Training. *Yu, S., +, TCSI July 2021 2753-2765*

- Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*

- Zero Aware Configurable Data Encoding by Skipping Transfer for Error Resilient Applications. *Jha, C.K., +, TCSI Aug. 2021 3337-3350*

Least mean squares methods

- Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*

- Low Delay Short Word Length Sigma Delta Active Noise Control. *Lopes, P.A.C., +, TCSI Sept. 2021 3746-3757*

Libraries

- Automated Design Approximation to Overcome Circuit Aging. *Balaskas, K., +, TCSI Nov. 2021 4710-4721*

- Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. *Schrage, O., +, TCSI Nov. 2021 4796-4809*

- Multi-Objective Digital Design Optimization via Improved Drive Granularity Standard Cells. *Cao, L., +, TCSI Nov. 2021 4660-4671*

Linear matrix inequalities

- H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021 818-828*

- Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*

- Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021 458-468*

- Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021 2158-2170*

- Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021 776-785*

- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*

- Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*

- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*

- LMI-Based Robust Stability Analysis of Discrete-Time Fractional-Order Systems With Interval Uncertainties. *Zhu, Z., +, TCSI April 2021 1671-1680*

- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*

- Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays.** Yang, Y., +, *TCSI May 2021* 2148-2157
- Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances.** Zhang, X., +, *TCSI March 2021* 1297-1307
- Time Domain Solution Analysis and Novel Admissibility Conditions of Singular Fractional-Order Systems.** Zhang, Q., +, *TCSI Feb. 2021* 842-855
- Linear phase filters**
- An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021* 592-602
- Linear regression**
- A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021* 4889-4899
- Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021* 4900-4909
- Linear systems**
- A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021* 4889-4899
- Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021* 4910-4923
- Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021* 458-468
- Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021* 2171-2182
- Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021* 776-785
- Event-Triggered H_{∞} Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021* 2604-2615
- Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021* 444-457
- Polytopic Event-Triggered Robust Model Predictive Control for Constrained Linear Systems. *Hu, Z., +, TCSI June 2021* 2594-2603
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021* 3436-3448
- State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021* 3846-3856
- Using Strictly Dissipative Impedance Coupling in the Waveform Relaxation Method for the Analysis of Interconnect Circuits. *Menkad, T., +, TCSI March 2021* 1283-1296
- Linearity**
- A 2.1 mW 2 MHz-BW 73.8 dB-SNDR Buffer-Embedded Noise-Shaping SAR ADC. *Kim, T., +, TCSI Dec. 2021* 5029-5037
- Linearization techniques**
- A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalone, L., +, TCSI July 2021* 2775-2786
- Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021* 3508-3519
- Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021* 336-349
- Load flow**
- A New Adaptive Sparse Pseudospectral Approximation Method and its Application for Stochastic Power Flow. *Lin, J., +, TCSI July 2021* 3089-3102
- Load regulation**
- Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021* 1693-1705
- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021* 797-807
- Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021* 3857-3868
- Logic arrays**
- A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021* 4456-4469
- Logic circuits**
- A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021* 2003-2016
- From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021* 114-125
- Radiation Hardened 12T SRAM With Crossbar-Based Peripheral Circuit in 28nm CMOS Technology. *Han, Y., +, TCSI July 2021* 2962-2975
- Re-Assessment of Steep-Slope Device Design From a Circuit-Level Perspective Using Novel Evaluation Criteria and Model-Less Method. *Wang, Z., +, TCSI April 2021* 1624-1635
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021* 1160-1170
- Logic design**
- A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021* 2003-2016
- Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme. *Tsai, K., +, TCSI Aug. 2021* 3328-3336
- An MTJ-Based Asynchronous System With Extremely Fine-Grained Voltage Scaling. *Yin, N., +, TCSI Jan. 2021* 311-321
- Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021* 2522-2534
- Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021* 238-249
- FPGA-Based Relaxation D/A Converters With Parasitics-Induced Error Suppression and Digital Self-Calibration. *Rubino, R., +, TCSI June 2021* 2494-2507
- High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021* 264-274
- High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021* 741-750
- Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021* 301-310
- Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021* 67-76
- Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021* 1217-1230
- Re-Assessment of Steep-Slope Device Design From a Circuit-Level Perspective Using Novel Evaluation Criteria and Model-Less Method. *Wang, Z., +, TCSI April 2021* 1624-1635
- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021* 536-549
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021* 1160-1170
- Logic functions**
- Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021* 4433-4443
- Logic gates**
- A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021* 4626-4635
- A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021* 4456-4469
- A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021* 4603-4615
- Accuracy-Configurable Radix-4 Adder With a Dynamic Output Modification Scheme. *Tsai, K., +, TCSI Aug. 2021* 3328-3336
- Automated Design Approximation to Overcome Circuit Aging. *Balaskas, K., +, TCSI Nov. 2021* 4710-4721
- Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. *Maheshwari, S., +, TCSI Dec. 2021* 4876-4888
- From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021* 114-125
- High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021* 264-274

- Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021 301-310*
- Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021 4970-4978*
- Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*
- Multi-Objective Digital Design Optimization via Improved Drive Granularity Standard Cells. *Cao, L., +, TCSI Nov. 2021 4660-4671*
- Neural Synaptic Plasticity-Inspired Computing: A High Computing Efficient Deep Convolutional Neural Network Accelerator. *Xia, Z., +, TCSI Feb. 2021 728-740*
- Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*
- Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*
- PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits. *Salamin, S., +, TCSI Oct. 2021 4299-4309*
- Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*
- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021 536-549*
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*
- Logic testing**
- Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. *Zhang, J., +, TCSI Jan. 2021 288-300*
- Long Term Evolution**
- A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*
- Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*
- Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*
- Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*
- Low noise amplifiers**
- A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*
- A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*
- A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*
- A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. *Guo, B., +, TCSI Jan. 2021 224-237*
- Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrogendorfer, D., +, TCSI May 2021 1800-1813*
- Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*
- Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*
- Low power electronics**
- A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Content-Aware Router. *Pu, J., +, TCSI Dec. 2021 5081-5094*
- Low-pass filters**
- An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer. *Sethia, G., +, TCSI March 2021 1319-1329*
- Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters. *Sanabria-Borbon, A.C., +, TCSI May 2021 1841-1854*
- Low-power electronics**
- 3–12-V Wide Input Range Adaptive Delay Compensated Active Rectifier for 6.78-MHz Loosely Coupled Wireless Power Transfer System. *Namgoong, G., +, TCSI June 2021 2702-2713*
- A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing. *Zhang, X., +, TCSI Feb. 2021 617-630*
- A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. *Zhao, X., +, TCSI Jan. 2021 89-102*
- A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*
- A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*
- A 197.1-μW Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*
- A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021 1045-1054*
- A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*
- A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*
- A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021 692-703*
- A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*
- A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*
- A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*
- A Fast and Energy-Efficient SNN Processor With Adaptive Clock/Event-Driven Computation Scheme and Online Learning. *Li, S., +, TCSI April 2021 1543-1552*
- A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*
- A Low-Area and Low-Power Comma Detection and Word Alignment Circuits for JESD204B/C Controller. *Yin, P., +, TCSI July 2021 2925-2935*
- A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*
- A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021 2003-2016*
- A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*
- A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*
- Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*
- An Efficient and Flexible Accelerator Design for Sparse Convolutional Neural Networks. *Xie, X., +, TCSI July 2021 2936-2949*
- Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrogendorfer, D., +, TCSI May 2021 1800-1813*
- Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*
- Body Biased Sense Amplifier With Auto-Offset Mitigation for Low-Voltage SRAMs. *Patel, D., +, TCSI Aug. 2021 3265-3278*
- Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*
- Design and Analysis of Approximate Compressors for Balanced Error Accumulation in MAC Operator. *Park, G., +, TCSI July 2021 2950-2961*

- Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*
- Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*
- Hybrid Pass Transistor Logic With Ambipolar Transistors. *Hu, X., +, TCSI Jan. 2021 301-310*
- Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM. *Kim, S.M., +, TCSI June 2021 2546-2554*
- Impact of Analog Non-Idealities on the Design Space of 6T-SRAM Current-Domain Dot-Product Operators for In-Memory Computing. *Kneip, A., +, TCSI May 2021 1931-1944*
- Jitter-Power Trade-Offs in PLLs. *Razavi, B., TCSI April 2021 1381-1387*
- Low-Complexity High-Precision Method and Architecture for Computing the Logarithm of Complex Numbers. *Chen, H., +, TCSI Aug. 2021 3293-3304*
- Low-Voltage Low-Noise High-CMRR Biopotential Integrated Preamplifier. *Cabrera, C., +, TCSI Aug. 2021 3232-3241*
- Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*
- Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*
- Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021 67-76*
- Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021 1217-1230*
- NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021 1892-1905*
- On the Resiliency of NCFET Circuits Against Voltage Over-Scaling. *Paim, G., +, TCSI April 2021 1481-1492*
- Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*
- Power-Speed Trade-Offs in Design of Scaled FET Circuits Using C/I_{D_S} Methodology. *Tajalli, A., TCSI Feb. 2021 631-640*
- Re-Assessment of Steep-Slope Device Design From a Circuit-Level Perspective Using Novel Evaluation Criteria and Model-Less Method. *Wang, Z., +, TCSI April 2021 1624-1635*
- Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*
- Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*
- Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*
- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021 536-549*
- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*
- Ultra-Low-Power FDSOI Neural Circuits for Extreme-Edge Neuromorphic Intelligence. *Rubino, A., +, TCSI Jan. 2021 45-56*
- Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*
- Lumped parameter networks**
- Design of Multi-Port With Desired Reference Impedances Using Y-Matrix and Matching Networks. *Sinha, R., TCSI May 2021 2096-2106*
- Lyapunov methods**
- H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021 818-828*
- Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*
- Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021 2626-2638*
- Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*
- An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer. *Sethia, G., +, TCSI March 2021 1319-1329*
- Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021 458-468*
- Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021 2171-2182*
- Chaos Generation With Impulse Control: Application to Non-Chaotic Systems and Circuit Design. *Tian, K., +, TCSI July 2021 3012-3022*
- Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021 2158-2170*
- Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021 3485-3494*
- Distributed Adaptive Resilient Formation Control of Uncertain Nonholonomic Mobile Robots Under Deception Attacks. *Wang, W., +, TCSI Sept. 2021 3822-3835*
- Distributed Fault Detection and Control for Markov Jump Systems Over Sensor Networks With Round-Robin Protocol. *Gong, C., +, TCSI Aug. 2021 3422-3435*
- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*
- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021 856-867*
- Dynamic Triggering Mechanisms for Distributed Adaptive Synchronization Control and Its Application to Circuit Systems. *Xu, Y., +, TCSI May 2021 2246-2256*
- Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*
- Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*
- Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*
- Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*
- Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*
- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*
- Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. *Xu, Y., +, TCSI April 2021 1589-1598*
- Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms. *Song, X., +, TCSI Jan. 2021 363-375*
- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*
- Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*
- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjugate Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*
- Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. *Lin, H., +, TCSI Aug. 2021 3397-3410*
- Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021* 1599-1609
- Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021* 3901-3912
- Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021* 3069-3078
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021* 3436-3448
- Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays. *Yang, Y., +, TCSI May 2021* 2148-2157
- Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021* 2616-2625
- State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021* 3846-3856
- Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink. *Duan, N., +, TCSI April 2021* 1610-1623

M

Machine control

- Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021* 1308-1318

Macros

- Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices. *Jhang, C., +, TCSI May 2021* 1773-1786

Magnetic anisotropy

- LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications. *Zarei, A., +, TCSI Nov. 2021* 4686-4699

Magnetic resonance

- Frequency Splitting Elimination and Utilization in Magnetic Coupling Wireless Power Transfer Systems. *Liao, Z., +, TCSI Feb. 2021* 929-939

- Robust H_∞ Control for ICPT Process With Coil Misalignment and Time Delay: A Sojourn-Probability-Based Switching Case. *Li, T., +, TCSI Dec. 2021* 5156-5167

Magnetic sensors

- A Galvanic Isolated Amplifier Based on CMOS Integrated Hall-Effect Sensors. *Mirfakhrrei, S.S., +, TCSI April 2021* 1388-1397

Magnetic tunneling

- An MTJ-Based Asynchronous System With Extremely Fine-Grained Voltage Scaling. *Yin, N., +, TCSI Jan. 2021* 311-321

- LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications. *Zarei, A., +, TCSI Nov. 2021* 4686-4699

Magnetoelectronics

- Non-Volatile Approximate Arithmetic Circuits Using Scalable Hybrid Spin-CMOS Majority Gates. *Jiang, H., +, TCSI March 2021* 1217-1230

- Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021* 2481-2493

- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021* 536-549

- Time-Domain Computing in Memory Using Spintronics for Energy-Efficient Convolutional Neural Network. *Zhang, Y., +, TCSI March 2021* 1193-1205

Magnons

- Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021* 536-549

Manganese

- A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021* 4810-4819

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Manipulator dynamics

- Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021* 4403-4412

Market research

- A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021* 4924-4934

Markov processes

- H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021* 818-828

- A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021* 4924-4934

- Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021* 786-796

- Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021* 4910-4923

- Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021* 458-468

- Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021* 2158-2170

- Dynamic Event-Based Non-Fragile Dissipative State Estimation for Quantized Complex Networks With Fading Measurements and Its Application. *Fan, S., +, TCSI Feb. 2021* 856-867

- Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays. *Wang, T., +, TCSI Nov. 2021* 4520-4533

- Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021* 2665-2674

- Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms. *Song, X., +, TCSI Jan. 2021* 363-375

- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021* 3869-3880

- Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm. *Yao, X., +, TCSI Sept. 2021* 3836-3845

Mathematical analysis

- Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. *Wu, L., +, TCSI Sept. 2021* 3668-3681

Mathematical model

- A Self-Matched Multi-Band Rectifier for Efficient Electromagnetic Energy Harvesting. *Wang, S.H., +, TCSI Nov. 2021* 4556-4565

- An Efficient Digital Realization of Retinal Light Adaptation in Cone Photoreceptors. *Ghanbarpour, M., +, TCSI Dec. 2021* 5072-5080

- Discrete Memristor Hyperchaotic Maps. *Bao, H., +, TCSI Nov. 2021* 4534-4544

- Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter. *Shi, B., +, TCSI Nov. 2021* 4746-4759

- Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges. *Kuznetsov, N., +, TCSI Oct. 2021* 4049-4061

- Ripple Suppression in Capacitive-Gain Chopper Instrumentation Amplifier Using Amplifier Slicing. *Lin, T.N., +, TCSI Oct. 2021* 3991-4000

- Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021* 4545-4555

- Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. *Fu, H., +, TCSI Nov. 2021* 4495-4507

Mathematical models

A Universal, Analog, In-Memory Computing Primitive for Linear Algebra Using Memristors. *Mannocci, P., +, TCSI Dec. 2021 4889-4899*

Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021 4910-4923*

Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021 5061-5071*

Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. *Menkad, T., +, TCSI Dec. 2021 5120-5133*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

Generating Any Number of Diversified Hidden Attractors via Memristor Coupling. *Zhang, S., +, TCSI Dec. 2021 4945-4956*

How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation. *Kang, S.M., +, TCSI Dec. 2021 4837-4850*

Leveraging Negative Capacitance CNTFETs for Image Processing: An Ultra-Efficient Ternary Image Edge Detection Hardware. *Behbahani, F., +, TCSI Dec. 2021 5108-5119*

NbO₂-Mott Memristor: A Circuit-Theoretic Investigation. *Messaris, I., +, TCSI Dec. 2021 4979-4992*

Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021 4900-4909*

Mathematics computing

A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs. *Malavipathirana, H., +, TCSI Aug. 2021 3363-3376*

Matrix algebra

An Energy Efficient Accelerator for Bidirectional Recurrent Neural Networks (BiRNNs) Using Hybrid-Iterative Compression With Error Sensitivity. *Nan, G., +, TCSI Sept. 2021 3707-3718*

Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*

Asymptotic Waveform Evaluation With Higher Order Poles. *Jiang, Y., +, TCSI April 2021 1681-1692*

BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*

Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm. *Wang, Q., +, TCSI Sept. 2021 3794-3807*

Design of Multi-Port With Desired Reference Impedances Using Y-Matrix and Matching Networks. *Sinha, R., TCSI May 2021 2096-2106*

Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*

Interconnection, Reciprocity and a Hierarchical Classification of Generalized Multiports. *Recski, A., +, TCSI Sept. 2021 3682-3692*

Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021 444-457*

Matrix multiplication

A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Magnetoresistive Circuits and Systems: Embedded Non-Volatile Memory to Crossbar Arrays. *Agrawal, A., +, TCSI June 2021 2281-2294*

Maximum likelihood detection

Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Maximum power point trackers

A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. *Zeng, Z., +, TCSI Sept. 2021 3587-3597*

A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*

A Rapid Circle Centre-Line Concept-Based MPPT Algorithm for Solar Photovoltaic Energy Conversion Systems. *Saxena, V., +, TCSI Feb. 2021 940-949*

An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*
Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*

Mean square error methods

Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters. *Liu, X., +, TCSI July 2021 2998-3011*

Medical computing

Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*

Medical control systems

Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaifah, A., +, TCSI Aug. 2021 3147-3157*

Medical signal processing

Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*

Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device. *Lu, J., +, TCSI July 2021 2976-2985*

Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems. *Ranjandish, R., +, TCSI April 2021 1740-1749*

Meetings

Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020. *David, J., +, TCSI Aug. 2021 3131-3132*

Guest Editorial Special Issue on the IEEE Latin American Symposium on Circuits and Systems 2020. *Blokhina, E., TCSI May 2021 1787-1788*

Guest Editorial: Special Issue Based on the 12th Edition of the Latin American Symposium on Circuits and Systems. *Rivet, F., +, TCSI Nov. 2021 4760*

Special Issue on the IEEE Asia Pacific Conference of Circuits and Systems 2019 and the IEEE International Conference on Electronics, Circuits and Systems 2019. *Blokhina, E., TCSI Jan. 2021 1-2*

Memory architecture

A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*

Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*

Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices. *Jhang, C., +, TCSI May 2021 1773-1786*

Implementation of Ternary Weights With Resistive RAM Using a Single Sense Operation Per Synapse. *Laborieux, A., +, TCSI Jan. 2021 138-147*

Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*

Spatial-Temporal Hybrid Neural Network With Computing-in-Memory Architecture. *Bai, K., +, TCSI July 2021 2850-2862*

SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. *Singh, A., +, TCSI May 2021 1917-1930*

Memristor circuits

Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays. *Rafiq, S., +, TCSI July 2021 2900-2910*

Unfolding Nonlinear Dynamics in Analogue Systems With Mem-Elements. *Marco, M.D., +, TCSI Jan. 2021 14-24*

Memristors

A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021 4456-4469*

A Compact Memristor Model for Neuromorphic ReRAM Devices in Flux-Charge Space. *Chawa, M.M.A., +, TCSI Sept. 2021 3631-3641*

A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

- A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021 4924-4934*
- Analog Neural Computing With Super-Resolution Memristor Crossbars. *James, A.P., +, TCSI Nov. 2021 4470-4481*
- Analog Solutions of Discrete Markov Chains via Memristor Crossbars. *Zoppo, G., +, TCSI Dec. 2021 4910-4923*
- Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*
- Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. *Maheshwari, S., +, TCSI Dec. 2021 4876-4888*
- Discrete Memristor Hyperchaotic Maps. *Bao, H., +, TCSI Nov. 2021 4534-4544*
- Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor - Part II. *Huang, T., +, TCSI Dec. 2021 4835-4836*
- Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor—Part I. *Huang, T., +, TCSI Nov. 2021 4417-4418*
- Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays. *Wang, T., +, TCSI Nov. 2021 4520-4533*
- Fault Modeling and Efficient Testing of Memristor-Based Memory. *Liu, P., +, TCSI Nov. 2021 4444-4455*
- Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*
- Generating Any Number of Diversified Hidden Attractors via Memristor Coupling. *Zhang, S., +, TCSI Dec. 2021 4945-4956*
- High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021 264-274*
- How to Build a Memristive Integrate-and-Fire Model for Spiking Neuronal Signal Generation. *Kang, S.M., +, TCSI Dec. 2021 4837-4850*
- Improved Vertex Coloring With NbO_x Memristor-Based Oscillatory Networks. *Weiher, M., +, TCSI May 2021 2082-2095*
- Loading-Aware Reliability Improvement of Ultra-Low Power Memristive Neural Networks. *Vahdat, S., +, TCSI Aug. 2021 3411-3421*
- NbO₂-Mott Memristor: A Circuit-Theoretic Investigation. *Messaris, I., +, TCSI Dec. 2021 4979-4992*
- Neuromorphic Dynamics of Chua Corsage Memristor. *Jin, P., +, TCSI Nov. 2021 4419-4432*
- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*
- Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021 4900-4909*
- Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*
- Positivity and Stability of Cohen-Grossberg-Type Memristor Neural Networks With Unbounded Delays. *Wu, A., +, TCSI Nov. 2021 4508-4519*
- QuantBayes: Weight Optimization for Memristive Neural Networks via Quantization-Aware Bayesian Inference. *Zhou, Y., +, TCSI Dec. 2021 4851-4861*
- Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. *Vahdat, S., +, TCSI Oct. 2021 4310-4323*
- Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. *Fu, H., +, TCSI Nov. 2021 4495-4507*
- SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. *Singh, A., +, TCSI May 2021 1917-1930*
- Synthesis of an Equivalent Circuit for Spike-Timing-Dependent Axon Growth: What Fires Together Now Really Wires Together. *Ochs, K., +, TCSI Sept. 2021 3656-3667*
- The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits. *Fahimi, Z., +, TCSI Oct. 2021 4090-4101*

Merging

A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

Message passing

Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

Metals

A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC. *Park, B., +, TCSI Nov. 2021 4700-4709*

Meters

Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021 4324-4336*

Microcontrollers

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Fast and Accurate Inference on Microcontrollers With Boosted Cooperative Convolutional Neural Networks (BC-Net). *Mocerino, L., +, TCSI Jan. 2021 77-88*

Microelectrodes

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

Microfabrication

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Micromagnetics

Spin Wave Normalization Toward All Magnonic Circuits. *Mahmoud, A.N., +, TCSI Jan. 2021 536-549*

Micromechanical devices

A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*

Noise Analysis of Charge-Balanced Readout Circuits for MEMS Accelerometers. *Lanniel, A., +, TCSI Jan. 2021 175-184*

Microprocessor chips

Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021 238-249*

LWRpro: An Energy-Efficient Configurable Crypto-Processor for Module-LWR. *Zhu, Y., +, TCSI March 2021 1146-1159*

Microprocessors

A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. *Eggimann, M., +, TCSI Oct. 2021 4116-4128*

Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. *Schrape, O., +, TCSI Nov. 2021 4796-4809*

Micsensors

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

Noise Analysis of Charge-Balanced Readout Circuits for MEMS Accelerometers. *Lanniel, A., +, TCSI Jan. 2021 175-184*

Microstrip filters

Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

Microstrip lines

Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

Microstrip resonators

Balanced and Unbalanced Duplexers Using Common Oval Dielectric Resonators. *Wu, D., +, TCSI Aug. 2021 3211-3221*

Microwave amplifiers

Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrodendorfer, D., +, TCSI May 2021 1800-1813*

Microwave oscillators

A New Boosted Active-Capacitor With Negative- G_m for Wide Tuning Range VCOs. *Agarwal, P., +, TCSI March 2021 1080-1090*

Microwave power amplifiers

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*
Design of a Quadband Doherty Power Amplifier With Large Power Back-Off Range. *Zhang, Z., +, TCSI Sept. 2021 3598-3610*

Millimeter wave amplifiers

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

Analysis and Design of Lossy Capacitive Over-Neutralization Technique for Amplifiers Operating Near f_{MAX} . *Simic, D., +, TCSI May 2021 1945-1955*

Millimeter wave circuits

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

Millimeter wave communication

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

Millimeter wave integrated circuits

Corrections to “Millimeter-Wave Integrated Phased Arrays” [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. *Zhao, D., +, TCSI Oct. 2021 4413*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Millimeter wave measurement

mm-Wave Through-Load Element for On-Wafer Measurement Applications. *Margalef-Rovira, M., +, TCSI Aug. 2021 3170-3183*

Millimeter wave mixers

A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Millimeter wave oscillators

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator. *Shu, Y., +, TCSI June 2021 2341-2353*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Millimeter wave phase shifters

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Analysis and Design of a CMOS Bidirectional Passive Vector-Modulated Phase Shifter. *Gu, P., +, TCSI April 2021 1398-1408*

Broadband Amplifier Design Technique by Dissipative Matching Networks. *Ciccognani, W., +, TCSI Jan. 2021 148-160*

Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

Millimeter wave radar

A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*

Millimeter wave receivers

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Millimeter wave technology

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MIMIC

Machine Learning for Automating the Design of Millimeter-Wave Baluns. *Nguyen, H.T., +, TCSI June 2021 2329-2340*

MIMO communication

A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*

Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*

Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021 3732-3745*

Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters. *Liu, X., +, TCSI July 2021 2998-3011*

MIMO systems

Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*

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Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*

Mixed analog digital integrated circuits

SymbIST: Symmetry-Based Analog and Mixed-Signal Built-In Self-Test for Functional Safety. *Pavlidis, A., +, TCSI June 2021 2580-2593*

Advanced Mixed Signal Concepts Exploiting the Strong Body-Bias Effect in CMOS 22FDX®. *Wittenhagen, E., +, TCSI Jan. 2021 57-66*

Applications of Artificial Intelligence on the Modeling and Optimization for Analog and Mixed-Signal Circuits: A Review. *Fayazi, M., +, TCSI June 2021 2418-2431*

NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021 1892-1905*

Ultra-Low-Power FDSOI Neural Circuits for Extreme-Edge Neuromorphic Intelligence. *Rubino, A., +, TCSI Jan. 2021 45-56*

Mixers

An Interstage-Reflectionless V-Band Radiometer With Capacitor-Reused Absorptive Matching in 0.13- μm SiGe BiCMOS. *Bi, X., +, TCSI Nov. 2021 4589-4602*

Mixers (circuits)

A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*

A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*

CMOS Full-Duplex Mixer-First Receiver With Adaptive Self-Interference Cancellation. *Ayati, S., +, TCSI Feb. 2021 868-878*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

MMIC amplifiers

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

MMIC mixers

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MMIC oscillators

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MMIC power amplifiers

A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*

A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. *Zhong, J., +, TCSI May 2021 1869-1880*

Mobile communication

Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schroedendorfer, D., +, TCSI May 2021 1800-1813*

Mobile handsets

Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. *Kwon, E., +, TCSI Oct. 2021 4156-4169*

Mobile radio

CARLA: A Convolution Accelerator With a Reconfigurable and Low-Energy Architecture. *Ahmadi, M., +, TCSI Aug. 2021 3184-3196*

Mobile robots

Distributed Adaptive Resilient Formation Control of Uncertain Nonholonomic Mobile Robots Under Deception Attacks. *Wang, W., +, TCSI Sept. 2021 3822-3835*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021 2616-2625*

Modulation

An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. *Donnelly, Y., +, TCSI Oct. 2021 4259-4267*

Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021 2183-2195*

Modulators

A Capacitively Coupled CT Δ Σ M With Chopping Artifacts Rejection for Sensor Readout ICs. *Lim, C., +, TCSI Aug. 2021 3242-3253*

Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback. *Pavan, S., +, TCSI Aug. 2021 3222-3231*

Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

Monte Carlo methods

Dynamic Read V_{MIN} and Yield Estimation for Nanoscale SRAMs. *Gupta, S., +, TCSI March 2021 1171-1182*

Imbalance-Tolerant Bit-Line Sense Amplifier for Dummy-Less Open Bit-Line Scheme in DRAM. *Kim, S.M., +, TCSI June 2021 2546-2554*

Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. *Klemme, F., +, TCSI June 2021 2569-2579*

Sub-ppm/ $^{\circ}\text{C}$ Bandgap References With Natural Basis Expansion for Curvature Cancellation. *Liu, N., +, TCSI Sept. 2021 3551-3561*

MOS capacitors

$A 2e_{\text{rms}}^-$ Temporal Noise CMOS Image Sensor With In-Pixel 1/f Noise Reduction and Conversion Gain Modulation for Low Light Imaging. *Priyadarshini, N., +, TCSI Jan. 2021 185-195*

MOSFET

A $+0.44^{\circ}\text{C}/-0.4^{\circ}\text{C}$ Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021 1102-1113*

A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*

Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Paralleled SiC MOSFETs. *Wang, J., +, TCSI May 2021 2107-2120*

Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. *Butz, N., +, TCSI Oct. 2021 4013-4024*

Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. *Maheshwari, S., +, TCSI Dec. 2021 4876-4888*

From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*

Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. *Klemme, F., +, TCSI June 2021 2569-2579*

Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

MOSFET circuits

A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing. *Zhang, X., +, TCSI Feb. 2021 617-630*

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies. *Han, J., +, TCSI March 2021 1012-1022*
Power-Speed Trade-Offs in Design of Scaled FET Circuits Using C/I_{DS} Methodology. *Tajalli, A., TCSI Feb. 2021 631-640*

MRAM devices

Magnetoresistive Circuits and Systems: Embedded Non-Volatile Memory to Crossbar Arrays. *Agrawal, A., +, TCSI June 2021 2281-2294*

Neural Network Training With Stochastic Hardware Models and Software Abstractions. *Zhang, B., +, TCSI April 2021 1532-1542*

Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*

Time-Domain Computing in Memory Using Spintronics for Energy-Efficient Convolutional Neural Network. *Zhang, Y., +, TCSI March 2021 1193-1205*

Multi-access systems

Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

Multi-agent systems

A New Approach of Formation Control for Multi-Agent Systems With Environmental Changes. *Liu, Y., +, TCSI Aug. 2021 3449-3459*

Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems. *Yang, Y., +, TCSI Jan. 2021 434-443*

Co-Design of Fault Detection and Consensus Control Protocol for Multi-Agent Systems Under Hidden DoS Attack. *Zhang, D., +, TCSI May 2021 2158-2170*

Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021 776-785*

Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*

Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*

Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021 444-457*

Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021 3069-3078*

Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021 3436-3448*

Privacy-Preserving Consensus for Multi-Agent Systems via Node Decomposition Strategy. *Wang, Y., +, TCSI Aug. 2021 3474-3484*

- Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021* 2616-2625
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- NbO₂-Mott Memristor: A Circuit-Theoretic Investigation. *Messaris, I., +, TCSI Dec. 2021 4979-4992*
- Numerical simulation**
- Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy. *Wu, Y., +, TCSI Nov. 2021 4735-4745*
- Object detection**
- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*
- Observers**
- Adaptive Fault Estimation for Unmanned Surface Vessels With a Neural Network Observer Approach. *Chen, L., +, TCSI Jan. 2021 416-425*
- Adaptive Fuzzy Output-Feedback Control Design for a Class of p -Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021 2626-2638*
- An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer. *Sethia, G., +, TCSI March 2021 1319-1329*
- Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence. *Han, T., +, TCSI Oct. 2021 4393-4402*
- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021 1659-1670*
- Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems. *Shu, F., +, TCSI Feb. 2021 808-817*
- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*
- Fractional-Order Sliding Mode Approach of Buck Converters With Mis-matched Disturbances. *Lin, X., +, TCSI Sept. 2021 3890-3900*
- Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*
- Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021 3901-3912*
- Observer-Based Bipartite Containment Control for Singular Multi-Agent Systems Over Signed Digraphs. *Zhu, Z., +, TCSI Jan. 2021 444-457*
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. *Wang, Q., +, TCSI Aug. 2021 3436-3448*
- Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021 2616-2625*
- Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm. *Yao, X., +, TCSI Sept. 2021 3836-3845*
- Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*

Operational amplifiers

A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

A Fast-Transient Low-Dropout Regulator With Current-Efficient Super Transconductance Cell and Dynamic Reference Control. *Ming, X., +, TCSI June 2021 2354-2367*

A Multi-Step Incremental Analog-to-Digital Converter With a Single Opamp and Two-Capacitor SAR Extended Counting. *Kuo, S., +, TCSI July 2021 2890-2899*

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. *Guo, B., +, TCSI Jan. 2021 224-237*

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Continuous-Time, Configurable Analog Linear System Solutions With Transconductance Amplifiers. *Hasler, J., +, TCSI Feb. 2021 765-775*

Design of Digital OTAs With Operation Down to 0.3 V and nW Power for Direct Harvesting. *Toledo, P., +, TCSI Sept. 2021 3693-3706*

Design of Three-Stage OTA Based on Settling-Time Requirements Including Large and Small Signal Behavior. *Giustolisi, G., +, TCSI March 2021 998-1011*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

Gain-Boosted Super Class AB OTAs Based on Nested Local Feedback. *Beloso-Legarra, J., +, TCSI Sept. 2021 3562-3573*

Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

Vector Wave Digital Filters and Their Application to Circuits With Two-Port Elements. *Bernardini, A., +, TCSI March 2021 1269-1282*

Optical amplifiers

A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. *Cavallaro, M., +, TCSI Dec. 2021 5018-5028*

Optical fiber communication

Efficient Implementation of 400 Gbps Optical Communication FEC. *Truhachev, D., +, TCSI Jan. 2021 496-509*

Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*

Optical imaging

Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021 4207-4220*

Optical radar

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

Optical sensors

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Optical transceivers

Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*

Optical transmitters

Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021 4545-4555*

Optimal control

Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021 1760-1768*

Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*

Optimization

A $+0.44^{\circ}\text{C}/-0.4^{\circ}\text{C}$ Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021 1102-1113*

A Real-Time-Link-Adaptive Operation Scheme for Maximum Energy Storage Efficiency in Resonant CM Wireless Power Receivers. *Taghadosi, M., +, TCSI Jan. 2021 510-523*

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021 5061-5071*

Demonstrating Filtered Feedback Control Near a Boundary Crisis. *Meucci, R., +, TCSI July 2021 3023-3030*

Frequency Design of Lossless Passive Electronic Filters: A State-Space Formulation of the Direct Synthesis Approach. *Perodou, A., +, TCSI Jan. 2021 161-174*

Generalized Analog-to-Information Converter With Analysis Sparse Prior. *Qian, H., +, TCSI Sept. 2021 3574-3586*

Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021 3732-3745*

High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*

Impedance Transparency and Performance Metrics of HBT-Based N-Path Mixers for mmWave Applications. *Ying, R., +, TCSI May 2021 2210-2223*

Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021 4970-4978*

Multi-Frequency Multi-Amplitude Superposition Modulation Method With Phase Shift Optimization for Single Inverter of Wireless Power Transfer System. *Wu, J., +, TCSI May 2021 2271-2279*

Multi-Objective Digital Design Optimization via Improved Drive Granularity Standard Cells. *Cao, L., +, TCSI Nov. 2021 4660-4671*

NS-FDN: Near-Sensor Processing Architecture of Feature-Configurable Distributed Network for Beyond-Real-Time Always-on Keyword Spotting. *Li, Q., +, TCSI May 2021 1892-1905*

Optimization Schemes for In-Memory Linear Regression Circuit With Memristor Arrays. *Wang, S., +, TCSI Dec. 2021 4900-4909*

Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*

QuantBayes: Weight Optimization for Memristive Neural Networks via Quantization-Aware Bayesian Inference. *Zhou, Y., +, TCSI Dec. 2021 4851-4861*

Reduced Complexity Optimal Convolution Based on the Discrete Hirschman Transform. *Xue, D., +, TCSI May 2021 2051-2059*

Variation-Aware SRAM Cell Optimization Using Deep Neural Network-Based Sensitivity Analysis. *Kwon, H., +, TCSI April 2021 1567-1577*

Organic light emitting diodes

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Oscillations

Damping Power System Electromechanical Oscillations Using Time Delays. *Tzounas, G., +, TCSI June 2021 2725-2735*

Oscillators

A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing. *Zhang, X., +, TCSI Feb. 2021 617-630*

A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A Double-Memristor Hyperchaotic Oscillator With Complete Amplitude Control. *Jiang, Y., +, TCSI Dec. 2021 4935-4944*

A Novel Topology of Coupled Phase-Locked Loops. *Karman, S., +, TCSI March 2021 989-997*

All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

- Demonstrating Filtered Feedback Control Near a Boundary Crisis. *Meucci, R., +, TCSI July 2021 3023-3030*
- Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020. *David, J., +, TCSI Aug. 2021 3131-3132*
- High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*
- Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. *Mikalic, J., +, TCSI Oct. 2021 4076-4089*
- Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*

Oscilloscopes

- Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures. *Napoli, E., +, TCSI Oct. 2021 4142-4155*

Output feedback

- Global Event-Triggered Output Feedback Stabilization for a Class of Nonlinear Time-Delay Systems. *Shu, F., +, TCSI Oct. 2021 4371-4380*
- Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*
- Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

P

Parabolic equations

- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*

Parallel architectures

- Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem. *Shabani, A., +, TCSI March 2021 1259-1268*
- Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021 238-249*
- High Performance CNN Accelerators Based on Hardware and Algorithm Co-Optimization. *Yuan, T., +, TCSI Jan. 2021 250-263*
- High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Parameter estimation

- Finite-Time and Fixed-Time Bipartite Consensus Tracking of Multi-Agent Systems With Weighted Antagonistic Interactions. *Zhao, M., +, TCSI Jan. 2021 426-433*

Parity check codes

- Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. *Cui, H., +, TCSI Feb. 2021 879-891*
- Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*
- Hardware Implementation for Belief Propagation Flip Decoding of Polar Codes. *Ji, H., +, TCSI March 2021 1330-1341*
- High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Partial differential equations

- A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs. *Malavipathirana, H., +, TCSI Aug. 2021 3363-3376*
- Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*
- Robust PCL Discovery of Data-Driven Mean-Field Game Systems and Control Problems. *Li, C., +, TCSI June 2021 2651-2664*
- Synthesis of an Equivalent Circuit for Spike-Timing-Dependent Axon Growth: What Fires Together Now Really Wires Together. *Ochs, K., +, TCSI Sept. 2021 3656-3667*
- Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink. *Duan, N., +, TCSI April 2021 1610-1623*

Partitioning algorithms

- Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. *Menkad, T., +, TCSI Dec. 2021 5120-5133*

Passive filters

- A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. *Zhong, J., +, TCSI May 2021 1869-1880*

Passive networks

- Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*

- CRADLE: Combined RF/Acoustic Detection and Localization of Passive Tags. *Rekhi, A.S., +, TCSI June 2021 2555-2568*

- High-Dimensional Extension of the TICER Algorithm. *Hao, L., +, TCSI Nov. 2021 4722-4734*

Patient diagnosis

- Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

- Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalafah, A., +, TCSI Aug. 2021 3147-3157*

Patient monitoring

- 22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification. *Vallicelli, E.A., +, TCSI Jan. 2021 3-13*

Patient treatment

- Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalafah, A., +, TCSI Aug. 2021 3147-3157*

Pattern classification

- Fully Integrated Analog Machine Learning Classifier Using Custom Activation Function for Low Resolution Image Classification. *Tannirkulam Chandrasekaran, S., +, TCSI March 2021 1023-1033*

Peer-to-peer computing

- Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. *Liu, Y., +, TCSI Oct. 2021 4194-4206*

Performance evaluation

- A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*

- A Novel Flow for Reducing Dynamic Power and Conditional Performance Improvement. *Mostafa, M., +, TCSI May 2021 2003-2016*

- Applying Lightweight Soft Error Mitigation Techniques to Embedded Mixed Precision Deep Neural Networks. *Abich, G., +, TCSI Nov. 2021 4772-4782*

- Hardware Architecture for Supersingular Isogeny Diffie-Hellman and Key Encapsulation Using a Fast Montgomery Multiplier. *Farzam, M., +, TCSI May 2021 2042-2050*

- Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*

- Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. *Kwon, E., +, TCSI Oct. 2021 4156-4169*

- The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits. *Fahimi, Z., +, TCSI Oct. 2021 4090-4101*

Periodic control

- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*

Peripheral interfaces

- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

Permittivity measurement

- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

Perturbation methods

- Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021 4403-4412*

- Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. *Cui, Y., +, TCSI Oct. 2021 4360-4370*

Perturbation techniques

Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays. *Hua, L., +, TCSI April 2021 1599-1609*

Perturbation theory

Universal Frequency-Domain Analysis of N-Path Networks. *Tymchenko, M., +, TCSI Feb. 2021 569-580*

Phase detectors

A 0.003-mm² 440fs_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalalone, L., +, TCSI July 2021 2775-2786*

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

A Novel Topology of Coupled Phase-Locked Loops. *Karman, S., +, TCSI March 2021 989-997*

The Truth About 2-Level Transition Elimination in Bang-Bang PAM-4 CDRs. *Verbeke, M., +, TCSI Jan. 2021 469-482*

Phase frequency detectors

A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD. *Chen, W., +, TCSI Nov. 2021 4566-4575*

Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges. *Kuznetsov, N., +, TCSI Oct. 2021 4049-4061*

Phase locked loops

A 0.003-mm² 440fs_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator. *Shu, Y., +, TCSI June 2021 2341-2353*

A Novel Topology of Coupled Phase-Locked Loops. *Karman, S., +, TCSI March 2021 989-997*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers. *Mazzaro, V., +, TCSI Oct. 2021 4038-4048*

High-Throughput Portable True Random Number Generator Based on Jitter-Latch Structure. *Wang, X., +, TCSI Feb. 2021 741-750*

Jitter-Power Trade-Offs in PLLs. *Razavi, B., TCSI April 2021 1381-1387*

Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges. *Kuznetsov, N., +, TCSI Oct. 2021 4049-4061*

Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

The Truth About 2-Level Transition Elimination in Bang-Bang PAM-4 CDRs. *Verbeke, M., +, TCSI Jan. 2021 469-482*

Phase measurement

Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*

Phase modulation

Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers. *Mazzaro, V., +, TCSI Oct. 2021 4038-4048*

Nonlinear Analysis of Cross-Coupled Super-Regenerative Oscillators. *Ferischischi, A., +, TCSI June 2021 2368-2381*

Phase noise

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

A 0.003-mm² 440fs_{RMS}-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

A 0.11–0.38 pJ/cycle Differential Ring Oscillator in 65 nm CMOS for Robust Neurocomputing. *Zhang, X., +, TCSI Feb. 2021 617-630*

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

A Cascaded Mode-Switching Sub-Sampling PLL With Quadrature Dual-Mode Voltage Waveform-Shaping Oscillator. *Shu, Y., +, TCSI June 2021 2341-2353*

A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalalone, L., +, TCSI July 2021 2775-2786*

A Fully Synthesizable Fractional-N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. *Liu, B., +, TCSI Feb. 2021 603-616*

A Generalization of the Groszkowski's Result in Differential Oscillator Topologies. *Buccolieri, F., +, TCSI July 2021 2800-2812*

A New Boosted Active-Capacitor With Negative- G_m for Wide Tuning Range VCOs. *Agarwal, P., +, TCSI March 2021 1080-1090*

A Novel Topology of Coupled Phase-Locked Loops. *Karman, S., +, TCSI March 2021 989-997*

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

MASH-Based Divider Controllers for Mitigation of Wandering Spurs in a Fractional-N Frequency Synthesizer. *Mai, D., +, TCSI Jan. 2021 126-137*
Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

Phase shifters

Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems. *Kumar, A., +, TCSI Nov. 2021 4636-4647*

Corrections to "Millimeter-Wave Integrated Phased Arrays" [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. *Zhao, D., +, TCSI Oct. 2021 4413*

High-Resolution Wideband Vector-Sum Digital Phase Shifter With On-Chip Phase Linearity Enhancement Technology. *Zhou, J., +, TCSI June 2021 2457-2469*

Phased arrays

Corrections to "Millimeter-Wave Integrated Phased Arrays" [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. *Zhao, D., +, TCSI Oct. 2021 4413*

Millimeter-Wave Integrated Phased Arrays. *Zhao, D., +, TCSI Oct. 2021 3977-3990*

Photoconductivity

Modeling and Analysis of High-Performance Triple Hole Block Layer Organic LED Based Light Sensor for Detection of Ovarian Cancer. *Negi, S., +, TCSI Aug. 2021 3254-3264*

Photodetectors

Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*

Photodiodes

Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*

Photoreceptors

An Efficient Digital Realization of Retinal Light Adaptation in Cone Photoreceptors. *Ghanbarpour, M., +, TCSI Dec. 2021 5072-5080*

Photovoltaic power systems

A Rapid Circle Centre-Line Concept-Based MPPT Algorithm for Solar Photovoltaic Energy Conversion Systems. *Saxena, V., +, TCSI Feb. 2021 940-949*

An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*
Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*

Physiological models

High Speed and Low Digital Resources Implementation of Hodgkin-Huxley Neuronal Model Using Base-2 Functions. *Haghiri, S., +, TCSI Jan. 2021 275-287*

PI control

Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks. *Dong, W., +, TCSI April 2021 1760-1768*

- Impedance Shaping Control Strategy for Wireless Power Transfer System Based on Dynamic Small-Signal Analysis. *Tan, T., +, TCSI March 2021 1354-1365*
- Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices. *Murad, M.A.A., +, TCSI July 2021 3079-3088*
- Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*
- Piecewise linear techniques**
- Lattice Trajectory Piecewise Linear Method for the Simulation of Diode Circuits. *Wang, J., +, TCSI May 2021 2069-2081*
- Ultralow-Latency VLSI Architecture Based on a Linear Approximation Method for Computing N th Roots of Floating-Point Numbers. *Lyu, F., +, TCSI Feb. 2021 715-727*
- Piezoelectric transducers**
- A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH Rectifier and DC-DC Converters. *Kara, I., +, TCSI Jan. 2021 210-223*
- A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators. *Ciftci, B., +, TCSI April 2021 1458-1471*
- Pins**
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- Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*
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Power consumption

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A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. *Radogna, A.V., +, TCSI March 2021 1123-1133*

A Compact 26.5–29.5-GHz LNA-Phase-Shifter Combo With 360° Continuous Phase Tuning Based on All-Pass Networks for Millimeter-Wave 5G. *Anjos, E.V.P., +, TCSI Sept. 2021 3927-3940*

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Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrogendorfer, D., +, TCSI May 2021 1800-1813*

BitSystolic: A 26.7 TOPS/W 2b–8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*

Design of Low-Voltage Power Efficient Frequency Dividers in Folded MOS Current Mode Logic. *Centurelli, F., +, TCSI Feb. 2021 680-691*

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Jitter-Power Trade-Offs in PLLs. *Razavi, B., TCSI April 2021 1381-1387*

Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021 2183-2195*

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A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. *Zeng, Z., +, TCSI Sept. 2021 3587-3597*

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

An RF Energy Harvesting and Power Management Unit Operating Over –24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach. *Xu, J., +, TCSI Aug. 2021 3520-3533*

Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*

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Power demand

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A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. *Cavallaro, M., +, TCSI Dec. 2021 5018-5028*

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LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications.

Zarei, A., +, TCSI Nov. 2021 4686-4699

Noise-Shaping SAR ADC Using a Two-Capacitor Digitally Calibrated DAC With 82.6-dB SNDR and 90.9-dB SFDR. *Shi, L., +, TCSI Oct. 2021 4001-4012*

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Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021 1693-1705*

Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

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A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. *Zhao, X., +, TCSI Jan. 2021 89-102*
The Truth About 2-Level Transition Elimination in Bang-Bang PAM-4 CDRs. *Verbeke, M., +, TCSI Jan. 2021 469-482*

Pulse width modulation

Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. *Chen, K., +, TCSI Oct. 2021 4062-4075*

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LAYGO: A Template-and-Grid-Based Layout Generation Engine for Advanced CMOS Technologies. *Han, J., +, TCSI March 2021 1012-1022*

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A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*

Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Quality of service

Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*

Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. *Kwon, E., +, TCSI Oct. 2021 4156-4169*

Quantization (signal)

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A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

A Mixed-Pruning Based Framework for Embedded Convolutional Neural Network Acceleration. *Chang, X., +, TCSI April 2021 1706-1715*

A Time-Based Pipelined ADC Using Integrate-and-Fire Multiplying-DAC. *Ryu, S., +, TCSI July 2021 2876-2889*

Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*

Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers. *Preissl, C., +, TCSI May 2021 2234-2245*

Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers. *Mazzaro, V., +, TCSI Oct. 2021 4038-4048*

QuantBayes: Weight Optimization for Memristive Neural Networks via Quantization-Aware Bayesian Inference. *Zhou, Y., +, TCSI Dec. 2021 4851-4861*

Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

Quantum cryptography

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Quantum entanglement

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Quantum Sealed-Bid Auction Without a Trusted Third Party. *Shi, R., TCSI Oct. 2021 4221-4231*

Queueing theory

Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

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A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. *Pan, D., +, TCSI March 2021 1091-1101*

A Transimpedance-to-Noise Optimized Analog Front-End With High PSRR for Pulsed ToF Lidar Receivers. *Khoeini, F., +, TCSI Sept. 2021 3642-3655*

Radiation hardening (electronics)

Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. *Schrape, O., +, TCSI Nov. 2021 4796-4809*

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Design of Soft-Error-Aware SRAM With Multi-Node Upset Recovery for Aerospace Applications. *Pal, S., +, TCSI June 2021 2470-2480*

General Efficient TMR for Combinational Circuit Hardening Against Soft Errors and Improved Multi-Objective Optimization Framework. *Tan, C., +, TCSI July 2021 3044-3057*

Radiation Hardened 12T SRAM With Crossbar-Based Peripheral Circuit in 28nm CMOS Technology. *Han, Y., +, TCSI July 2021 2962-2975*

Soft-Error-Immune Read-Stability-Improved SRAM for Multi-Node Upset Tolerance in Space Applications. *Pal, S., +, TCSI Aug. 2021 3317-3327*

Radiation therapy

22 dB Signal-to-Noise Ratio Real-Time Proton Sound Detector for Experimental Beam Range Verification. *Vallicelli, E.A., +, TCSI Jan. 2021 3-13*

Radio access technologies

Applying Lightweight Soft Error Mitigation Techniques to Embedded Mixed Precision Deep Neural Networks. *Abich, G., +, TCSI Nov. 2021 4772-4782*

Radio frequency

A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*

Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power. *Guo, L., +, TCSI Dec. 2021 5194-5205*

Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems. *Kumar, A., +, TCSI Nov. 2021 4636-4647*

Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless. *Regev, D., +, TCSI Dec. 2021 5168-5181*

Radio links

A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021 692-703*

Radio networks

Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters. *Sanabria-Borbon, A.C., +, TCSI May 2021 1841-1854*

Radio receivers

A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A Reconfigurable Passive Mixer-Based Sub-GHz Receiver Front-End for Fast Spectrum Sensing Functionality. *Bae, S., +, TCSI Feb. 2021 892-903*

Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

Double-Conversion, Noise-Cancelling Receivers Using Modulated LNTAs and Double-Layer Passive Mixers for Concurrent Signal Reception With Tuned RF Interface. *Han, G., +, TCSI Sept. 2021 3913-3926*

Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021 2183-2195*

- Self-Synchronized DS/SS With High Spread Factors for Robust Millimeter-Wave Datalinks. *Tang, A., +, TCSI Sept. 2021 3941-3950*
- Radio spectrum management**
- A Reconfigurable Passive Mixer-Based Sub-GHz Receiver Front-End for Fast Spectrum Sensing Functionality. *Bae, S., +, TCSI Feb. 2021 892-903*
- Radio transceivers**
- A 0.85mm² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. *Sun, Z., +, TCSI Jan. 2021 196-209*
- A Foreground Calibration for M-Channel Time-Interleaved Analog-to-Digital Converters Based on Genetic Algorithm. *Tavares, Y.A., +, TCSI April 2021 1444-1457*
- CMOS Full-Duplex Mixer-First Receiver With Adaptive Self-Interference Cancellation. *Ayati, S., +, TCSI Feb. 2021 868-878*
- Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers. *Preissl, C., +, TCSI May 2021 2234-2245*
- Radio transmitters**
- A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*
- A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*
- Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*
- Coding Efficiency Enhancement Using Time Interleaved Level Splitting and Optimized Multi-Level Delta Sigma Modulation in Digital Transmitter. *Kumar, N., +, TCSI July 2021 2986-2997*
- Radiofrequency filters**
- Adaptive Multi-Band Negative-Group-Delay RF Circuits With Low Reflection. *Gomez-Garcia, R., +, TCSI May 2021 2196-2209*
- Radiofrequency interference**
- A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*
- Radiofrequency oscillators**
- A 660 MHz–5 GHz 6-Phase/3-Phase Transmitter With Cancellation of Counter-Intermodulation Distortion and Improved Image Rejection. *Jiang, H., +, TCSI April 2021 1432-1443*
- Dithering Concepts for Spur-Free Nonlinear DTC-Based Frequency Synthesizers. *Preissl, C., +, TCSI May 2021 2234-2245*
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- CMOS Full-Duplex Mixer-First Receiver With Adaptive Self-Interference Cancellation. *Ayati, S., +, TCSI Feb. 2021 868-878*
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- Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor - Part II. *Huang, T., +, TCSI Dec. 2021 4835-4836*
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- Random processes**
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- Random sequences**
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- Analysis and Comparison of Readout Architectures and Analog-to-Digital Converters for 3D-Stacked CMOS Image Sensors. *Callens, N., +, TCSI Aug. 2021 3117-3130*
- Noise Analysis of Charge-Balanced Readout Circuits for MEMS Accelerometers. *Lanniel, A., +, TCSI Jan. 2021 175-184*
- Signal and Noise Analysis of an Open-Circuit Voltage Pixel for Uncooled Infrared Image Sensors. *Fragasse, R., +, TCSI May 2021 1827-1840*
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- Dadu-Eye: A 5.3 TOPS/W, 30 fps/1080p High Accuracy Stereo Vision Accelerator. *Min, F., +, TCSI Oct. 2021 4207-4220*
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- An Interstage-Reflectionless V-Band Radiometer With Capacitor-Reused Absorptive Matching in 0.13-μm SiGe BiCMOS. *Bi, X., +, TCSI Nov. 2021 4589-4602*
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- A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH1 Rectifier and DC-DC Converters.** Kara, I., +, *TCSI Jan. 2021* 210-223
- A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine.** Zeng, Z., +, *TCSI Sept. 2021* 3587-3597
- A Low-Profile Autonomous Interface Circuit for Piezoelectric Micro-Power Generators.** Ciftci, B., +, *TCSI April 2021* 1458-1471
- An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range.** Martins, G.C., +, *TCSI March 2021* 1342-1353
- Rectifying circuits**
- A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH1 Rectifier and DC-DC Converters.** Kara, I., +, *TCSI Jan. 2021* 210-223
- Recurrent neural networks**
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- An Energy Efficient Accelerator for Bidirectional Recurrent Neural Networks (BiRNNs) Using Hybrid-Iterative Compression With Error Sensitivity.** Nan, G., +, *TCSI Sept. 2021* 3707-3718
- BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices.** Yang, Q., +, *TCSI March 2021* 1134-1145
- Control of a Buck DC/DC Converter Using Approximate Dynamic Programming and Artificial Neural Networks.** Dong, W., +, *TCSI April 2021* 1760-1768
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- Centralized System Identification of Multi-Rail Power Converter Systems Using an Iterative Decimation Approach.** Xu, J., +, *TCSI Aug. 2021* 3520-3533
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- Reference circuits**
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- Broadband Mismatch Calibration for Time-Interleaved ADC Based on Linear Frequency Modulated Signal.** Peng, X., +, *TCSI Sept. 2021* 3621-3630
- Sub-ppm/ $^{\circ}$ C Bandgap References With Natural Basis Expansion for Curvature Cancellation.** Liu, N., +, *TCSI Sept. 2021* 3551-3561
- Registers**
- A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Congestion-Aware Router.** Pu, J., +, *TCSI Dec. 2021* 5081-5094
- Applying Lightweight Soft Error Mitigation Techniques to Embedded Mixed Precision Deep Neural Networks.** Abich, G., +, *TCSI Nov. 2021* 4772-4782
- Quantum Sealed-Bid Auction Without a Trusted Third Party.** Shi, R., *TCSI Oct. 2021* 4221-4231
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Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. Chen, K., +, *TCSI Oct. 2021* 4062-4075

Reinforcement learning

Reinforcement Learning-Based Power Management Policy for Mobile Device Systems. Kwon, E., +, *TCSI Oct. 2021* 4156-4169

Relaxation oscillators

A 296 nJ Energy-per-Measurement Relaxation Oscillator-Based Analog Front-End for Chemiresistive Sensors. Radogna, A.V., +, *TCSI March 2021* 1123-1133

A Generalization of the Groszkowski's Result in Differential Oscillator Topologies. Buccolieri, F., +, *TCSI July 2021* 2800-2812

Unfolding Nonlinear Dynamics in Analogue Systems With Mem-Elements. Marco, M.D., +, *TCSI Jan. 2021* 14-24

Reliability

Applying Lightweight Soft Error Mitigation Techniques to Embedded Mixed Precision Deep Neural Networks. Abich, G., +, *TCSI Nov. 2021* 4772-4782

Loading-Aware Reliability Improvement of Ultra-Low Power Memristive Neural Networks. Vahdat, S., +, *TCSI Aug. 2021* 3411-3421

Reliability theory

Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunction Measurement. Song, X., +, *TCSI Sept. 2021* 3869-3880

Remotely operated vehicles

Adaptive Fault Estimation for Unmanned Surface Vessels With a Neural Network Observer Approach. Chen, L., +, *TCSI Jan. 2021* 416-425

Distributed Adaptive Finite-Time Compensation Control for UAV Swarm With Uncertain Disturbances. Zhang, J., +, *TCSI Feb. 2021* 829-841

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. Cheng, W., +, *TCSI May 2021* 2121-2133

Renewable energy sources

Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. Yildirim, B., +, *TCSI April 2021* 1693-1705

Resistance

A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. Biccario, G.E., +, *TCSI Nov. 2021* 4626-4635

A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. Aljafar, M.J., +, *TCSI Nov. 2021* 4456-4469

A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC. Park, B., +, *TCSI Nov. 2021* 4700-4709

Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. Menkad, T., +, *TCSI Dec. 2021* 5120-5133

Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. Maheshwari, S., +, *TCSI Dec. 2021* 4876-4888

Fault Modeling and Efficient Testing of Memristor-Based Memory. Liu, P., +, *TCSI Nov. 2021* 4444-4455

Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. Fu, H., +, *TCSI Nov. 2021* 4495-4507

Resistive RAM

A Compact Memristor Model for Neuromorphic ReRAM Devices in Flux-Charge Space. Chawa, M.M.A., +, *TCSI Sept. 2021* 3631-3641

Circuit Modeling for RRAM-Based Neuromorphic Chip Crossbar Array With and Without Write-Verify Scheme. Tao, T., +, *TCSI May 2021* 1906-1916

Implementation of Ternary Weights With Resistive RAM Using a Single Sense Operation Per Synapse. Laborieux, A., +, *TCSI Jan. 2021* 138-147

Investigation of ReRAM Variability on Flow-Based Edge Detection Computing Using HfO₂-Based ReRAM Arrays. Rafiq, S., +, *TCSI July 2021* 2900-2910

Resistors

A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC. Park, B., +, *TCSI Nov. 2021* 4700-4709

Active Circuits With Diodes: Topological Conditions Sufficient to Determine the State of a Diode. Ciampa, M., *TCSI Jan. 2021* 35-44

- High-Resolution Wideband Vector-Sum Digital Phase Shifter With On-Chip Phase Linearity Enhancement Technology. Zhou, J., +, *TCSI June 2021* 2457-2469
- Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. Mikulic, J., +, *TCSI Oct. 2021* 4076-4089
- Two- and Three-Way Filtering Power Dividers With Harmonic Suppression Using Triangle Patch Resonator. Zhu, Y., +, *TCSI Dec. 2021* 5007-5017
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- Resonant power converters**
- A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. Wang, C., +, *TCSI June 2021* 2714-2724
- Resonators**
- Frequency Splitting Elimination and Utilization in Magnetic Coupling Wireless Power Transfer Systems. Liao, Z., +, *TCSI Feb. 2021* 929-939
- Parametric and Structural-Parametric Synthesis of Nonuniform Transmission Line Resonators. Zakharov, A., *TCSI March 2021* 1055-1067
- Resource allocation**
- Set-Based Obfuscation for Strong PUFs Against Machine Learning Attacks. Zhang, J., +, *TCSI Jan. 2021* 288-300
- Resource management**
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- RF signals**
- A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. Chen, L., +, *TCSI Nov. 2021* 4616-4625
- Ring oscillators**
- Advanced Mixed Signal Concepts Exploiting the Strong Body-Bias Effect in CMOS 22FDX®. Wittenhagen, E., +, *TCSI Jan. 2021* 57-66
- Failure in Ring Oscillators With Capacitive Load. Ravezzi, L., *TCSI Aug. 2021* 3388-3396
- The Analog Behavior of Pseudo Digital Ring Oscillators Used in VCO ADCs. Borgmans, J., +, *TCSI July 2021* 2827-2840
- Risk management**
- Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. Li, X., +, *TCSI Oct. 2021* 4268-4277
- Road vehicle radar**
- A 76–81-GHz Four-Channel Digitally Controlled CMOS Receiver for Automotive Radars. Pan, D., +, *TCSI March 2021* 1091-1101
- Robots**
- Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. Mobayen, S., +, *TCSI Oct. 2021* 4403-4412
- Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. Du, Y., +, *TCSI Oct. 2021* 4324-4336
- Robust control**
- Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. Qi, W., +, *TCSI Feb. 2021* 786-796
- Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. Yildirim, B., +, *TCSI April 2021* 1693-1705
- Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. Huang, M., +, *TCSI April 2021* 1659-1670
- LMI-Based Robust Stability Analysis of Discrete-Time Fractional-Order Systems With Interval Uncertainties. Zhu, Z., +, *TCSI April 2021* 1671-1680
- Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices. Murad, M.A.A., +, *TCSI July 2021* 3079-3088
- Polytopic Event-Triggered Robust Model Predictive Control for Constrained Linear Systems. Hu, Z., +, *TCSI June 2021* 2594-2603
- Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. Fei, Y., +, *TCSI June 2021* 2616-2625
- Robust H ∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. Zhang, X., +, *TCSI March 2021* 1297-1307
- Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. Alshalaftah, A., +, *TCSI Aug. 2021* 3147-3157
- Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm. Yao, X., +, *TCSI Sept. 2021* 3836-3845
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- Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets. Li, H., +, *TCSI Oct. 2021* 4170-4181
- DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays. Moitra, A., +, *TCSI Nov. 2021* 4482-4494
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- Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. Cui, Y., +, *TCSI Oct. 2021* 4360-4370
- Routh methods**
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- S**
- S-parameters**
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- Safety**
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- Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. Butz, N., +, *TCSI Oct. 2021* 4013-4024
- Sample and hold circuits**
- Advanced Mixed Signal Concepts Exploiting the Strong Body-Bias Effect in CMOS 22FDX®. Wittenhagen, E., +, *TCSI Jan. 2021* 57-66
- Analysis and Design of a Charge Sampler With 70-GHz 1-dB Bandwidth in 130-nm SiGe BiCMOS. Wu, L., +, *TCSI Sept. 2021* 3668-3681
- SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. Singh, A., +, *TCSI May 2021* 1917-1930
- Sampled data systems**
- Containment Control for Networked Fractional-Order Systems With Sampled Position Data. Ye, Y., +, *TCSI Sept. 2021* 3881-3889
- Satellite communication**
- A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. Zhong, J., +, *TCSI May 2021* 1869-1880
- Corrections to “Millimeter-Wave Integrated Phased Arrays” [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. Zhao, D., +, *TCSI Oct. 2021* 4413
- Scattering parameters**
- Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless. Regev, D., +, *TCSI Dec. 2021* 5168-5181
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- Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems. Kumar, A., +, *TCSI Nov. 2021* 4636-4647
- Secondary cells**
- An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer. Sethia, G., +, *TCSI March 2021* 1319-1329
- Security**
- BCA: A 530-mW Multicore Blockchain Accelerator for Power-Constrained Devices in Securing Decentralized Networks. Tran, T.H., +, *TCSI Oct. 2021* 4245-4258
- Fast Strategies for the Implementation of SIKE Round 3 on ARM Cortex-M4. Anastasova, M., +, *TCSI Oct. 2021* 4129-4141

Security of data

Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021 3857-3868*

Self-induced transparency

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Self-organizing feature maps

Hardware Self-Organizing Map Based on Digital Frequency-Locked Loop and Triangular Neighborhood Function. *Hikawa, H., TCSI March 2021 1245-1258*

Semiconductor counters

A 2e_{rms}^- Temporal Noise CMOS Image Sensor With In-Pixel $1/f$ Noise Reduction and Conversion Gain Modulation for Low Light Imaging. *Priyadarshini, N., +, TCSI Jan. 2021 185-195*

Semiconductor device modeling

Annealing Processing Architecture of 28-nm CMOS Chip for Ising Model With 512 Fully Connected Spins. *Iimura, R., +, TCSI Dec. 2021 5061-5071*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

PROTON: Post-Synthesis Ferroelectric Thickness Optimization for NCFET Circuits. *Salamin, S., +, TCSI Oct. 2021 4299-4309*

Semiconductor device models

A Compact Memristor Model for Neuromorphic ReRAM Devices in Flux-Charge Space. *Chawla, M.M.A., +, TCSI Sept. 2021 3631-3641*

A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*

Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Paralleled SiC MOSFETs. *Wang, J., +, TCSI May 2021 2107-2120*

Re-Assessment of Steep-Slope Device Design From a Circuit-Level Perspective Using Novel Evaluation Criteria and Model-Less Method. *Wang, Z., +, TCSI April 2021 1624-1635*

Semiconductor device reliability

Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. *Klemme, F., +, TCSI June 2021 2569-2579*

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Active Circuits With Diodes: Topological Conditions Sufficient to Determine the State of a Diode. *Ciampa, M., TCSI Jan. 2021 35-44*

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NoPUF: A Novel PUF Design Framework Toward Modeling Attack Resistant PUFs. *Wang, A., +, TCSI June 2021 2508-2521*

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A Smoothed LASSO-Based DNN Sparsification Technique. *Koneru, B.N.G., +, TCSI Oct. 2021 4287-4298*

Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. *Vahdat, S., +, TCSI Oct. 2021 4310-4323*

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A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

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A 5\AA Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. *Eggemann, M., +, TCSI Oct. 2021 4116-4128*

Adaptive Fault Estimation for Unmanned Surface Vessels With a Neural Network Observer Approach. *Chen, L., +, TCSI Jan. 2021 416-425*

Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. *Chen, K., +, TCSI Oct. 2021 4062-4075*

Cyber-Physical Systems With Multiple Denial-of-Service Attackers: A Game-Theoretic Framework. *Huang, Y., +, TCSI Oct. 2021 4349-4359*

Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021 1659-1670*

Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

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Variable Cut-Off Frequency Observer-Based Positioning for Ball-Beam Systems Without Velocity and Current Feedback Considering Actuator Dynamics. *Kim, Y., +, TCSI Jan. 2021 396-405*

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Interconnection, Reciprocity and a Hierarchical Classification of Generalized Multiports. *Recski, A., +, TCSI Sept. 2021 3682-3692*

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A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*

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Low Delay Short Word Length Sigma Delta Active Noise Control. *Lopes, P.A.C., +, TCSI Sept. 2021 3746-3757*

Spur Immunity in MASH-Based Fractional-N CP-PLLs With Polynomial Nonlinearities. *Mazzaro, V., +, TCSI June 2021 2295-2306*

Signal classification

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Efficient Hardware Architecture of Convolutional Neural Network for ECG Classification in Wearable Healthcare Device. *Lu, J., +, TCSI July 2021 2976-2985*

Towards Low Latency and Resource-Efficient FPGA Implementations of the MUSIC Algorithm for Direction of Arrival Estimation. *Butt, U.M., +, TCSI Aug. 2021 3351-3362*

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Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021 3732-3745*

Low-Voltage Low-Noise High-CMRR Biopotential Integrated Preamplifier. *Cabrera, C., +, TCSI Aug. 2021 3232-3241*

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High-Resolution Wideband Vector-Sum Digital Phase Shifter With On-Chip Phase Linearity Enhancement Technology. *Zhou, J., +, TCSI June 2021 2457-2469*

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Low-Complexity High-Precision Method and Architecture for Computing the Logarithm of Complex Numbers. *Chen, H., +, TCSI Aug. 2021 3293-3304*

Signal processing algorithms

Instruction-Set Accelerated Implementation of CRYSTALS-Kyber. *Bisheh-Niasar, M., +, TCSI Nov. 2021 4648-4659*

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- Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021* 3537-3550
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- A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021* 581-591
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- Approximate Equivalent Circuits to Understand Tradeoffs in Geometry of On-Chip Inductors. *Leng, W., +, TCSI March 2021* 975-988

Sliding mode control

- Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021* 4403-4412
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Slow wave structures

- 77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021* 3158-3169
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- Intrusion-Detector-Dependent Distributed Economic Model Predictive Control for Load Frequency Regulation With PEVs Under Cyber Attacks. *Hu, Z., +, TCSI Sept. 2021* 3857-3868

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- Editorial Special Issue for 50th Birthday of Memristor Theory and Application of Neuromorphic Computing Based on Memristor—Part I. *Huang, T., +, TCSI Nov. 2021* 4417-4418

- Guest Editorial Special Issue on the IEEE International NEWCAS Conference 2020. *David, J., +, TCSI Aug. 2021* 3131-3132

- Guest Editorial Special Issue on the IEEE Latin American Symposium on Circuits and Systems 2020. *Blokhina, E., TCSI May 2021* 1787-1788

- Guest Editorial: Special Issue Based on the 12th Edition of the Latin American Symposium on Circuits and Systems. *Rivet, F., +, TCSI Nov. 2021* 4760

- Special Issue on the IEEE Asia Pacific Conference of Circuits and Systems 2019 and the IEEE International Conference on Electronics, Circuits and Systems 2019. *Blokhina, E., TCSI Jan. 2021* 1-2

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Speech processing

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Self-Referenced Single-Ended Resistance Monitoring Write Termination Scheme for STT-RAM Write Energy Reduction. *Choi, S., +, TCSI June 2021 2481-2493*

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A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*

Analysis and Optimization Strategies Toward Reliable and High-Speed 6T Compute SRAM. *Chen, J., +, TCSI April 2021 1520-1531*

Body Biased Sense Amplifier With Auto-Offset Mitigation for Low-Voltage SRAMs. *Patel, D., +, TCSI Aug. 2021 3265-3278*

Challenges and Trends of SRAM-Based Computing-In-Memory for AI Edge Devices. *Jhang, C., +, TCSI May 2021 1773-1786*

Design of Soft-Error-Aware SRAM With Multi-Node Upset Recovery for Aerospace Applications. *Pal, S., +, TCSI June 2021 2470-2480*

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Exploring Applications of STT-RAM in GPU Architectures. *Liu, X., +, TCSI Jan. 2021 238-249*

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MF-Net: Compute-In-Memory SRAM for Multibit Precision Inference Using Memory-Immersed Data Conversion and Multiplication-Free Operators. *Nasrin, S., +, TCSI May 2021 1966-1978*

Radiation Hardened 12T SRAM With Crossbar-Based Peripheral Circuit in 28nm CMOS Technology. *Han, Y., +, TCSI July 2021 2962-2975*

Soft-Error-Immune Read-Stability-Improved SRAM for Multi-Node Upset Tolerance in Space Applications. *Pal, S., +, TCSI Aug. 2021 3317-3327*

TD-SRAM: Time-Domain-Based In-Memory Computing Macro for Binary Neural Networks. *Song, J., +, TCSI Aug. 2021 3377-3387*

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All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*

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Delay-Dependent Stability Analysis of Modern Shipboard Microgrids. *Yildirim, B., +, TCSI April 2021 1693-1705*

Distributed Adaptive Finite-Time Compensation Control for UAV Swarm With Uncertain Disturbances. *Zhang, J., +, TCSI Feb. 2021 829-841*

Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems. *Shu, F., +, TCSI Feb. 2021 808-817*

Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*

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- Stock markets**
- Low-Latency Hardware Accelerator for Improved Engle-Granger Cointegration in Pairs Trading. *Liang, S., +, TCSI July 2021 2911-2924*
- Storage management**
- A Hardware-Friendly Approach Towards Sparse Neural Networks Based on LFSR-Generated Pseudo-Random Sequences. *Karimzadeh, F., +, TCSI Feb. 2021 751-764*
- A Mixed-Pruning Based Framework for Embedded Convolutional Neural Network Acceleration. *Chang, X., +, TCSI April 2021 1706-1715*
- Hybrid Convolution Architecture for Energy-Efficient Deep Neural Network Processing. *Kim, S., +, TCSI May 2021 2017-2029*
- Memory Access Optimization for On-Chip Transfer Learning. *Hussain, M.A., +, TCSI April 2021 1507-1519*
- Storage management chips**
- SRIF: Scalable and Reliable Integrate and Fire Circuit ADC for Memristor-Based CIM Architectures. *Singh, A., +, TCSI May 2021 1917-1930*
- Stress**
- Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits. *Khan, R.A., +, TCSI Oct. 2021 4025-4037*

Strip line resonators

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

Submillimeter wave detectors

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

Submillimeter wave integrated circuits

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

Submillimeter wave measurement

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

Substrate integrated waveguides

Mode Composite Waveguide Based on Hybrid Substrate Integrated Waveguide and Spoof Surface Plasmon Polariton Structure. *Yang, Z., +, TCSI April 2021 1472-1480*

Substrates

Two- and Three-Way Filtering Power Dividers With Harmonic Suppression Using Triangle Patch Resonator. *Zhu, Y., +, TCSI Dec. 2021 5007-5017*

Sugar

Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaifah, A., +, TCSI Aug. 2021 3147-3157*

Superconducting logic circuits

Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*

Superresolution

Analog Neural Computing With Super-Resolution Memristor Crossbars. *James, A.P., +, TCSI Nov. 2021 4470-4481*

Support vector machines

A Gait Energy Image-Based System for Brazilian Sign Language Recognition. *Passos, W.L., +, TCSI Nov. 2021 4761-4771*

A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing. *Younes, H., +, TCSI Oct. 2021 4232-4244*

Surface plasmon polaritons

Mode Composite Waveguide Based on Hybrid Substrate Integrated Waveguide and Spoof Surface Plasmon Polariton Structure. *Yang, Z., +, TCSI April 2021 1472-1480*

Switched capacitor filters

A 270 nW Switched-Capacitor Acoustic Feature Extractor for Always-On Voice Activity Detection. *Shi, E., +, TCSI March 2021 1045-1054*

Switched capacitor networks

A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021 2714-2724*

A T-Type Switched-Capacitor Multilevel Inverter With Low Voltage Stress and Self-Balancing. *Wang, Y., +, TCSI May 2021 2257-2270*

An 800 nW Switched-Capacitor Feature Extraction Filterbank for Sound Classification. *Villamizar, D.A., +, TCSI April 2021 1578-1588*

Switched filters

Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*

Universal Frequency-Domain Analysis of N-Path Networks. *Tymchenko, M., +, TCSI Feb. 2021 569-580*

Switched systems

A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021 4924-4934*

Switches

A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021 4626-4635*

A 2.1 mW 2 MHz-BW 73.8 dB-SNDR Buffer-Embedded Noise-Shaping SAR ADC. *Kim, T., +, TCSI Dec. 2021 5029-5037*

A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*

A Dynamic Event-Triggered Approach to State Estimation for Switched Memristive Neural Networks With Nonhomogeneous Sojourn Probabilities. *Cheng, J., +, TCSI Dec. 2021 4924-4934*

A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021 4403-4412*

Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence. *Han, T., +, TCSI Oct. 2021 4393-4402*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

Event-Based Extended Dissipative State Estimation for Memristor-Based Markovian Neural Networks With Hybrid Time-Varying Delays. *Wang, T., +, TCSI Nov. 2021 4520-4533*

Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter. *Shi, B., +, TCSI Nov. 2021 4746-4759*

LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications. *Zarei, A., +, TCSI Nov. 2021 4686-4699*

Millimeter-Wave Integrated Phased Arrays. *Zhao, D., +, TCSI Oct. 2021 3977-3990*

Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021 4324-4336*

Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

Positivity and Stability of Cohen-Grossberg-Type Memristor Neural Networks With Unbounded Delays. *Wu, A., +, TCSI Nov. 2021 4508-4519*

Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. *Liu, Y., +, TCSI Oct. 2021 4194-4206*

The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits. *Fahimi, Z., +, TCSI Oct. 2021 4090-4101*

Switching circuits

A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021 4626-4635*

A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*

A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

Switching converters

A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021 2714-2724*

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

A T-Type Switched-Capacitor Multilevel Inverter With Low Voltage Stress and Self-Balancing. *Wang, Y., +, TCSI May 2021 2257-2270*

Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021 3485-3494*

Synthesis of Constant Power Loads Using Switching Converters Under Sliding-Mode Control. *Martinez-Trevino, B.A., +, TCSI Jan. 2021 524-535*

Switching frequency

Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. *Chen, K., +, TCSI Oct. 2021 4062-4075*

Switching systems

Online Identification of Piecewise Affine Systems Using Integral Concurrent Learning. *Du, Y., +, TCSI Oct. 2021 4324-4336*

Switching systems (control)

Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021 3901-3912*

Symmetric matrices

High-Dimensional Extension of the TICER Algorithm. *Hao, L., +, TCSI Nov. 2021 4722-4734*

Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

Synapses

A 5.28-mm² 4.5-pJ/SOP Energy-Efficient Spiking Neural Network Hardware With Reconfigurable High Processing Speed Neuron Core and Congestion-Aware Router. *Pu, J., +, TCSI Dec. 2021 5081-5094*

Improved Hopfield Network Optimization Using Manufacturable Three-Terminal Electronic Synapses. *Yi, S., +, TCSI Dec. 2021 4970-4978*

Synchronization

All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*

Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*

Asynchronous Event-Driven Clocking and Control in Pipelined ADCs. *Hershberg, B., +, TCSI July 2021 2813-2826*

Dynamic Triggering Mechanisms for Distributed Adaptive Synchronization Control and Its Application to Circuit Systems. *Xu, Y., +, TCSI May 2021 2246-2256*

Exponential Synchronization of Complex Networks: An Intermittent Adaptive Event-Triggered Control Strategy. *Wu, Y., +, TCSI Nov. 2021 4735-4745*

Finite-/Fixed-Time Synchronization of Memristor Chaotic Systems and Image Encryption Application. *Wang, L., +, TCSI Dec. 2021 4957-4969*

Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. *Xu, Y., +, TCSI April 2021 1589-1598*

Finite/Fixed-Time Anti-Synchronization of Inconsistent Markovian Quaternion-Valued Memristive Neural Networks With Reaction-Diffusion Terms. *Song, X., +, TCSI Jan. 2021 363-375*

Finite/Fixed-Time Synchronization of Multi-Layer Networks Based on Energy Consumption Estimation. *Xu, Y., +, TCSI Oct. 2021 4278-4286*

Intermittent Dynamic Event-Triggered Control for Synchronization of Stochastic Complex Networks. *Wu, Y., +, TCSI June 2021 2639-2650*

Metastability in Superconducting Single Flux Quantum (SFQ) Logic. *Datta, G., +, TCSI May 2021 1990-2002*

Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. *Lin, H., +, TCSI Aug. 2021 3397-3410*

Plesiochronous Spread Spectrum Clocking With Guaranteed QoS for In-Band Switching Noise Reduction. *Fan, X., +, TCSI July 2021 3031-3043*

Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays. *Yang, Y., +, TCSI May 2021 2148-2157*

Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. *Kuttappa, R., +, TCSI April 2021 1636-1645*

Self-Synchronized DS/SS With High Spread Factors for Robust Millimeter-Wave Datalinks. *Tang, A., +, TCSI Sept. 2021 3941-3950*

System-on-chip

A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*

All Digital Phase-Locked Loop Networks for Clock Generation and Distribution: Network Stability, Convergence and Performance. *Koskin, E., +, TCSI Jan. 2021 406-415*

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

Dynamic Dataflow Scheduling and Computation Mapping Techniques for Efficient Depthwise Separable Convolution Acceleration. *Li, B., +, TCSI Aug. 2021 3279-3292*

IECA: An In-Execution Configuration CNN Accelerator With 30.55 GOPS/mm² Area Efficiency. *Huang, B., +, TCSI Nov. 2021 4672-4685*

Implementation of an On-Chip Learning Neural Network IC Using Highly Linear Charge Trap Device. *Choi, J., +, TCSI July 2021 2863-2875*

Portable CMOS NMR System With 50-kHz IF, 10- μ s Dead Time, and Frequency Tracking. *Hong, S., +, TCSI Nov. 2021 4576-4588*

RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021 4545-4555*

Systolic arrays

A Real-Time Architecture for Pruning the Effectual Computations in Deep Neural Networks. *Asadikouhanjani, M., +, TCSI May 2021 2030-2041*

BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*

T**Table lookup**

Digital Non-Linearity Calibration for ADCs With Redundancy Using a New LUT Approach. *Gines, A., +, TCSI Aug. 2021 3197-3210*

Gradient-Adaptive Spline-Interpolated LUT Methods for Low-Complexity Digital Predistortion. *Campo, P.P., +, TCSI Jan. 2021 336-349*

Multi-Context TCAM-Based Selective Computing: Design Space Exploration for a Low-Power NN. *Arakawa, R., +, TCSI Jan. 2021 67-76*

Symmetric-Mapping LUT-Based Method and Architecture for Computing XY-Like Functions. *Chen, H., +, TCSI March 2021 1231-1244*

Task analysis

A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. *Eggimann, M., +, TCSI Oct. 2021 4116-4128*

DyGA: A Hardware-Efficient Accelerator With Traffic-Aware Dynamic Scheduling for Graph Convolutional Networks. *Xie, R., +, TCSI Dec. 2021 5095-5107*

Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. *Liu, Y., +, TCSI Oct. 2021 4194-4206*

Technology CAD (electronics)

From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*

Telecommunication computing

A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*

High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Telecommunication congestion control

Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

Telecommunication network management

Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

Telecommunication network reliability

A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. *Cui, H., +, TCSI Feb. 2021 879-891*

Telecommunication network routing

High-Speed LDPC Decoders Towards 1 Tb/s. *Li, M., +, TCSI May 2021 2224-2233*

Telecommunication network topology

Hardware Topologies for Decentralized Large-Scale MIMO Detection Using Newton Method. *Kulkarni, A., +, TCSI Sept. 2021 3732-3745*

Telecommunication power management

A 0.7-V Sub-mW Type-II Phase-Tracking Bluetooth Low Energy Receiver in 28-nm CMOS. *Hu, S., +, TCSI June 2021 2317-2328*

A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*

A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*

Power Scaling Laws for Radio Receiver Front Ends. *Sarajlic, M., +, TCSI May 2021 2183-2195*

Telecommunication security

LWRpro: An Energy-Efficient Configurable Crypto-Processor for Module-LWR. *Zhu, Y., +, TCSI March 2021 1146-1159*

Temperature measurement

Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. *Mikulic, J., +, TCSI Oct. 2021 4076-4089*

Temperature sensors

A +0.44°C/-0.4°C Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021 1102-1113*

Post-Manufacturing Process and Temperature Calibration of a 2-MHz On-Chip Relaxation Oscillator. *Mikulic, J., +, TCSI Oct. 2021 4076-4089*

Tensors

A Shallow Neural Network for Real-Time Embedded Machine Learning for Tensorial Tactile Data Processing. *Younes, H., +, TCSI Oct. 2021 4232-4244*

Terahertz wave detectors

Emerging Terahertz Integrated Systems in Silicon. *Yi, X., +, TCSI Sept. 2021 3537-3550*

Ternary logic

High-Density Memristor-CMOS Ternary Logic Family. *Wang, X., +, TCSI Jan. 2021 264-274*

Thermal noise

Power Bound Analysis of a Two-Step MASH Incremental ADC Based on Noise-Shaping SAR ADCs. *Akbari, M., +, TCSI Aug. 2021 3133-3146*

Thermal stability

A Metal-Via Resistance Based Physically Unclonable Function With Backend Incremental ADC. *Park, B., +, TCSI Nov. 2021 4700-4709*

Generalized Relationship Between Frequency Response and Settling Time of CMOS OTAs: Toward Many-Stage Design. *Mohammed, M.A., +, TCSI Dec. 2021 4993-5006*

Thermoelectric conversion

Power Management IC With a Three-Phase Cold Self-Start for Thermoelectric Generators. *Tran-Dinh, T., +, TCSI Jan. 2021 103-113*

Three-dimensional displays

A 3-D Crossbar Architecture for Both Pipeline and Parallel Computations. *Aljafar, M.J., +, TCSI Nov. 2021 4456-4469*

Three-dimensional integrated circuits

A 96-MB 3D-Stacked SRAM Using Inductive Coupling With 0.4-V Transmitter, Termination Scheme and 12:1 SerDes in 40-nm CMOS. *Shiba, K., +, TCSI Feb. 2021 692-703*

Characterization of Inter-Cell Interference in 3D NAND Flash Memory. *Park, S.K., +, TCSI March 2021 1183-1192*

Three-term control

Towards Safe and Robust Closed-Loop Artificial Pancreas Using Improved PID-Based Control Strategies. *Alshalaifah, A., +, TCSI Aug. 2021 3147-3157*

Threshold voltage

A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD. *Chen, W., +, TCSI Nov. 2021 4566-4575*

A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*

Thyristors

A +0.44°C/-0.4°C Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021 1102-1113*

Time division multiplexing

A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. *Li, C., +, TCSI May 2021 1881-1891*

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

A Time-Division-Multiplexed Clocked-Analog Low-Dropout Regulator. *Xie, Z., +, TCSI March 2021 1366-1376*

Efficient Design of Spiking Neural Network With STDP Learning Based on Fast CORDIC. *Wu, J., +, TCSI June 2021 2522-2534*

Time factors

Generalized Relationship Between Frequency Response and Settling Time of CMOS OTAs: Toward Many-Stage Design. *Mohammed, M.A., +, TCSI Dec. 2021 4993-5006*

Time series

Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm. *Wang, Q., +, TCSI Sept. 2021 3794-3807*

Time-digital conversion

Delta-Sigma FDC Enhancements for FDC-Based Digital Fractional-N PLLs. *Alvarez-Fontecilla, E., +, TCSI March 2021 965-974*

Time-domain analysis

A Comprehensive Phase Noise Analysis of Bang-Bang Digital PLLs. *Avalone, L., +, TCSI July 2021 2775-2786*

An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. *Donnelly, Y., +, TCSI Oct. 2021 4259-4267*

Nonlinear Analysis of Cross-Coupled Super-Regenerative Oscillators. *Ferschischi, A., +, TCSI June 2021 2368-2381*

Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. *Ji, L., +, TCSI July 2021 2841-2849*

Time-frequency analysis

A 10.4–16-Gb/s Reference-Less Baud-Rate Digital CDR With One-Tap DFE Using a Wide-Range FD. *Chen, W., +, TCSI Nov. 2021 4566-4575*

Generalized Relationship Between Frequency Response and Settling Time of CMOS OTAs: Toward Many-Stage Design. *Mohammed, M.A., +, TCSI Dec. 2021 4993-5006*

Real-Time Downsampling in Digital Storage Oscilloscopes With Multichannel Architectures. *Napoli, E., +, TCSI Oct. 2021 4142-4155*

Time-varying systems

H_∞ Stabilization of Discrete-Time Nonlinear Semi-Markov Jump Singularly Perturbed Systems With Partially Known Semi-Markov Kernel Information. *Shen, H., +, TCSI Feb. 2021 818-828*

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*

Almost Sure Synchronization of Multilayer Networks via Intermittent Pinning Noises: A White-Noise-Based Time-Varying Coupling. *Li, S., +, TCSI Aug. 2021 3460-3473*

Bumpless Transfer Control for Switched Linear Systems and its Application to Aero-Engines. *Shi, Y., +, TCSI May 2021 2171-2182*

Data-Driven Resilient Control for Linear Discrete-Time Multi-Agent Networks Under Unconfined Cyber-Attacks. *Zhang, W., +, TCSI Feb. 2021 776-785*

Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. *Liu, C., +, TCSI April 2021 1646-1658*

Event-Triggered H_∞ Filtering for Discrete-Time Switched Systems Under Denial-of-Service. *Qu, H., +, TCSI June 2021 2604-2615*

Finite-Time Event-Triggered Control for Semi-Markovian Switching Cyber-Physical Systems With FDI Attacks and Applications. *Qi, W., +, TCSI June 2021 2665-2674*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Global Event-Triggered Output Feedback Stabilization for a Class of Nonlinear Time-Delay Systems. *Shu, F., +, TCSI Oct. 2021 4371-4380*

Joint State and Fault Estimation for Networked Interconnected PDE Systems With Semi-Markov Fault Coefficient via Conjunct Measurement. *Song, X., +, TCSI Sept. 2021 3869-3880*

- MASH-Based Divider Controllers for Mitigation of Wandering Spurs in a Fractional- N Frequency Synthesizer.** Mai, D., +, TCSI Jan. 2021 126-137
- Novel Finite-Time Reliable Control Design for Memristor-Based Inertial Neural Networks With Mixed Time-Varying Delays.** Hua, L., +, TCSI April 2021 1599-1609
- Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems.** Li, K., +, TCSI July 2021 3069-3078
- Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs.** Wang, Q., +, TCSI Aug. 2021 3436-3448
- Probabilistic-Constrained H_∞ Tracking Control for a Class of Stochastic Nonlinear Systems Subject to DoS Attacks and Measurement Outliers.** Wei, B., +, TCSI Oct. 2021 4381-4392
- Quasi-Synchronization of Heterogeneous LC Circuits in Grid-Connected Systems With Intentionally Time-Varying Lumped Delays.** Yang, Y., +, TCSI May 2021 2148-2157
- State Bumpless Transfer Control for a Class of Switched Descriptor Systems.** Shi, J., +, TCSI Sept. 2021 3846-3856
- Timing**
- Machine Learning for On-the-Fly Reliability-Aware Cell Library Characterization. Klemme, F., +, TCSI June 2021 2569-2579
 - Portable CMOS NMR System With 50-kHz IF, 10- μ s Dead Time, and Frequency Tracking. Hong, S., +, TCSI Nov. 2021 4576-4588
- Timing jitter**
- Continuous-Time Incremental Delta-Sigma Modulators With FIR Feedback. Pavan, S., +, TCSI Aug. 2021 3222-3231
- Tissue damage**
- Active Charge Balancer With Adaptive 3.3 V to 38 V Supply Compliance for Neural Stimulators. Butz, N., +, TCSI Oct. 2021 4013-4024
- Topology**
- Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. Qiu, H., +, TCSI Dec. 2021 5182-5193
 - Bipartite Average Tracking for Multi-Agent Systems With Disturbances: Finite-Time and Fixed-Time Convergence. Han, T., +, TCSI Oct. 2021 4393-4402
 - Constructing Higher-Dimensional Digital Chaotic Systems via Loop-State Contraction Algorithm. Wang, Q., +, TCSI Sept. 2021 3794-3807
 - Corrections to “Millimeter-Wave Integrated Phased Arrays” [early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. Zhao, D., +, TCSI Oct. 2021 4413
 - Design Flow for Hybrid CMOS/Memristor Systems—Part II: Circuit Schematics and Layout. Maheshwari, S., +, TCSI Dec. 2021 4876-4888
 - Finite-Time Intra-Layer and Inter-Layer Quasi-Synchronization of Two-Layer Multi-Weighted Networks. Xu, Y., +, TCSI April 2021 1589-1598
 - Millimeter-Wave Integrated Phased Arrays. Zhao, D., +, TCSI Oct. 2021 3977-3990
 - Predefined Finite-Time Output Containment of Nonlinear Multi-Agent Systems With Leaders of Unknown Inputs. Wang, Q., +, TCSI Aug. 2021 3436-3448
 - Scalable Fully Pipelined Hardware Architecture for In-Network Aggregated AllReduce Communication. Liu, Y., +, TCSI Oct. 2021 4194-4206
- Torque**
- Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. Cui, Y., +, TCSI Oct. 2021 4360-4370
 - Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. Naseri, F., +, TCSI March 2021 1308-1318
- Tracking**
- Distributed Fault-Tolerant Consensus Tracking Control of Multi-Agent Systems Under Fixed and Switching Topologies. Liu, C., +, TCSI April 2021 1646-1658
- Traffic engineering computing**
- RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. Bai, L., +, TCSI Feb. 2021 704-714
- Training data**
- A 5 μ W Standard Cell Memory-Based Configurable Hyperdimensional Computing Accelerator for Always-on Smart Sensing. Eggimann, M., +, TCSI Oct. 2021 4116-4128
 - A Gait Energy Image-Based System for Brazilian Sign Language Recognition. Passos, W.L., +, TCSI Nov. 2021 4761-4771
 - A High-Level Modeling Framework for Estimating Hardware Metrics of CNN Accelerators. Juracy, L.R., +, TCSI Nov. 2021 4783-4795
 - A Smoothed LASSO-Based DNN Sparsification Technique. Koneru, B.N.G., +, TCSI Oct. 2021 4287-4298
 - DetectX—Adversarial Input Detection Using Current Signatures in Memristive XBar Arrays. Moitra, A., +, TCSI Nov. 2021 4482-4494
 - Reliability Enhancement of Inverter-Based Memristor Crossbar Neural Networks Using Mathematical Analysis of Circuit Non-Idealities. Vahdat, S., +, TCSI Oct. 2021 4310-4323
- Trajectory control**
- Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. Cheng, W., +, TCSI May 2021 2121-2133
- Transceivers**
- A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. Cavallaro, M., +, TCSI Dec. 2021 5018-5028
 - A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. Salem, J.M., +, TCSI Feb. 2021 581-591
 - A Low-Area and Low-Power Comma Detection and Word Alignment Circuits for JESD204B/C Controller. Yin, P., +, TCSI July 2021 2925-2935
 - Millimeter-Wave Integrated Phased Arrays. Zhao, D., +, TCSI Oct. 2021 3977-3990
 - Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. Azmat, R., +, TCSI Nov. 2021 4545-4555
- Transfer functions**
- A Complex Band-Pass Filter for Low-Power and High-Performance Transceivers. Cavallaro, M., +, TCSI Dec. 2021 5018-5028
 - A Novel Topology of Coupled Phase-Locked Loops. Karman, S., +, TCSI March 2021 989-997
 - An Algorithm for Implementing a Modulator Whose Output is Spur-Free After Nonlinear Distortion. Donnelly, Y., +, TCSI Oct. 2021 4259-4267
 - An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. Hasler, J., +, TCSI Feb. 2021 592-602
 - Digital Non-Linearity Calibration for ADCs With Redundancy Using a New LUT Approach. Gines, A., +, TCSI Aug. 2021 3197-3210
 - Folded Noise Prediction in Nonlinear Fractional-N Frequency Synthesizers. Mazzaro, V., +, TCSI Oct. 2021 4038-4048
 - High-Dimensional Extension of the TICER Algorithm. Hao, L., +, TCSI Nov. 2021 4722-4734
- Transformers**
- A Compact Transformer-Based Fractional-N ADPLL in 10-nm FinFET CMOS. Li, C., +, TCSI May 2021 1881-1891
- Transient analysis**
- Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. Chen, K., +, TCSI Oct. 2021 4062-4075
 - Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets. Li, H., +, TCSI Oct. 2021 4170-4181
 - Event-Driven Approach With Time-Scale Hierarchical Automaton for Switching Transient Simulation of SiC-Based High-Frequency Converter. Shi, B., +, TCSI Nov. 2021 4746-4759
- Transient response**
- A Fast-Transient Low-Dropout Regulator With Current-Efficient Super Transconductance Cell and Dynamic Reference Control. Ming, X., +, TCSI June 2021 2354-2367
 - A Time-Division-Multiplexed Clocked-Analog Low-Dropout Regulator. Xie, Z., +, TCSI March 2021 1366-1376
 - Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. Naseri, F., +, TCSI March 2021 1308-1318

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Transistors

- A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021 4626-4635*
- A 90-GHz Asymmetrical Single-Pole Double-Throw Switch With >19.5-dBm 1-dB Compression Point in Transmission Mode Using 55-nm Bulk CMOS Technology. *Chen, L., +, TCSI Nov. 2021 4616-4625*
- A Highly-Efficient RF Energy Harvester Using Passively-Produced Adaptive Threshold Voltage Compensation. *Karami, M.A., +, TCSI Nov. 2021 4603-4615*
- Analog Neural Computing With Super-Resolution Memristor Crossbars. *James, A.P., +, TCSI Nov. 2021 4470-4481*
- Automated Design Approximation to Overcome Circuit Aging. *Balaskas, K., +, TCSI Nov. 2021 4710-4721*
- Design and Evaluation of Radiation-Hardened Standard Cell Flip-Flops. *Schrape, O., +, TCSI Nov. 2021 4796-4809*
- Design of High-Reliability Memory Cell to Mitigate Single Event Multiple Node Upsets. *Li, H., +, TCSI Oct. 2021 4170-4181*
- From MOSFETs to Ambipolar Transistors: Standard Cell Synthesis for the Planar RFET Technology. *Reuter, M., +, TCSI Jan. 2021 114-125*
- LIMITA: Logic-in-Memory Primitives for Imprecise Tolerant Applications. *Zarei, A., +, TCSI Nov. 2021 4686-4699*
- Optimized Synthesis Method for Ultra-Low Power Multi-Input Material Implication Logic With Emerging Non-Volatile Memories. *Puglisi, F.M., +, TCSI Nov. 2021 4433-4443*
- Sensing and Cancellation Circuits for Mitigating EMI-Related Common Mode Noise in High-Speed PAM-4 Transmitter. *Azmat, R., +, TCSI Nov. 2021 4545-4555*

Transmission line matrix methods

- Analysis and Design of Quasi-Circulating Quadrature Hybrid for Full-Duplex Wireless. *Regev, D., +, TCSI Dec. 2021 5168-5181*
- Convergence of the Resistive Coupling-Based Waveform Relaxation Method for Chains of Identical and Symmetric Circuits. *Menkad, T., +, TCSI Dec. 2021 5120-5133*

Transmission line measurements

- Extracting RLC Parasitics From a Flexible Electronic Hybrid Assembly Using On-Chip ESD Protection Circuits. *Khan, R.A., +, TCSI Oct. 2021 4025-4037*

Transmission line theory

- Using Strictly Dissipative Impedance Coupling in the Waveform Relaxation Method for the Analysis of Interconnect Circuits. *Menkad, T., +, TCSI March 2021 1283-1296*

Transmission lines

- Frequency Selective Impedance Transformer With High-Impedance Transforming Ratio and Extremely High/Low Termination Impedances. *Jeong, Y., +, TCSI June 2021 2382-2392*
- Highly Sensitive Phase-Variation Dielectric Constant Sensor Based on a Capacitively-Loaded Slow-Wave Transmission Line. *Ebrahimi, A., +, TCSI July 2021 2787-2799*
- Parametric and Structural-Parametric Synthesis of Nonuniform Transmission Line Resonators. *Zakharov, A., TCSI March 2021 1055-1067*

Transmitters

- Multi-Stream Spatial Digital Predistortion for Fully-Connected Hybrid Beamforming Massive MIMO Transmitters. *Liu, X., +, TCSI July 2021 2998-3011*

Transmitting antennas

- Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Transport protocols

- Neural Network-Based Distributed Adaptive Pre-Assigned Finite-Time Consensus of Multiple TCP/AQM Networks. *Wang, C., +, TCSI Jan. 2021 387-395*

Tree searching

- Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Trees (mathematics)

- Asynchronous Event-Driven Clocking and Control in Pipelined ADCs. *Hershberg, B., +, TCSI July 2021 2813-2826*

Triboelectricity

- A 70-to-2 V Triboelectric Energy Harvesting System Utilizing Parallel-SSH Rectifier and DC-DC Converters. *Kara, I., +, TCSI Jan. 2021 210-223*

Tuning

- The Impact of Device Uniformity on Functionality of Analog Passively-Integrated Memristive Circuits. *Fahimi, Z., +, TCSI Oct. 2021 4090-4101*

Tunnel transistors

- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

Tunneling

- Ultra-Low-Power and Performance-Improved Logic Circuit Using Hybrid TFET-MOSFET Standard Cells Topologies and Optimized Digital Front-End Process. *Wang, Z., +, TCSI March 2021 1160-1170*

Turbo codes

- Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

- Reverse Calculation-Based Low Memory Turbo Decoder for Power Constrained Applications. *Zhan, M., +, TCSI June 2021 2688-2701*

Two-port networks

- Design of Multi-Port With Desired Reference Impedances Using Y-Matrix and Matching Networks. *Sinha, R., TCSI May 2021 2096-2106*

U**UHF amplifiers**

- A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. *Guo, B., +, TCSI Jan. 2021 224-237*

- Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrogendorfer, D., +, TCSI May 2021 1800-1813*

UHF integrated circuits

- A 0.59-mW 78.7-dB SNDR 2-MHz Bandwidth Active-RC Delta-Sigma Modulator With Relaxed and Reduced Amplifiers. *Wang, H., +, TCSI March 2021 1114-1122*

- A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

- An Active-Under-Coil RFDAC With Analog Linear Interpolation in 28-nm CMOS. *Zhang, F., +, TCSI May 2021 1855-1868*

- An RF Energy Harvesting and Power Management Unit Operating Over -24 to +15 dBm Input Range. *Martins, G.C., +, TCSI March 2021 1342-1353*

- Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schrogendorfer, D., +, TCSI May 2021 1800-1813*

UHF mixers

- A Reconfigurable Passive Mixer-Based Sub-GHz Receiver Front-End for Fast Spectrum Sensing Functionality. *Bae, S., +, TCSI Feb. 2021 892-903*

UHF oscillators

- A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

- A Reconfigurable Passive Mixer-Based Sub-GHz Receiver Front-End for Fast Spectrum Sensing Functionality. *Bae, S., +, TCSI Feb. 2021 892-903*

- Nonlinear Analysis of Cross-Coupled Super-Regenerative Oscillators. *Ferschischi, A., +, TCSI June 2021 2368-2381*

UHF power amplifiers

- A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*

- Design of a Quadband Doherty Power Amplifier With Large Power Back-Off Range. *Zhang, Z., +, TCSI Sept. 2021 3598-3610*

- Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*

Ultrasonic measurement

A 1.25 μ J per Measurement Ultrasound Rangefinder System in 65 nm CMOS for Explorations With a Swarm of Sensor Nodes. *Berkol, G., +, TCSI April 2021 1409-1420*

Ultrasonic transducers

A 1.25 μ J per Measurement Ultrasound Rangefinder System in 65 nm CMOS for Explorations With a Swarm of Sensor Nodes. *Berkol, G., +, TCSI April 2021 1409-1420*

CRADLE: Combined RF/Acoustic Detection and Localization of Passive Tags. *Rekhi, A.S., +, TCSI June 2021 2555-2568*

Uncertain systems

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*

Adaptive Fuzzy Output-Feedback Control Design for a Class of p-Norm Stochastic Nonlinear Systems With Output Constraints. *Fang, L., +, TCSI June 2021 2626-2638*

Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021 458-468*

Distributed Adaptive Finite-Time Compensation Control for UAV Swarm With Uncertain Disturbances. *Zhang, J., +, TCSI Feb. 2021 829-841*

Distributed Adaptive Resilient Formation Control of Uncertain Nonholonomic Mobile Robots Under Deception Attacks. *Wang, W., +, TCSI Sept. 2021 3822-3835*

Dynamic Event-Triggered Tracking Control for a Class of p -Normal Nonlinear Systems. *Shu, F., +, TCSI Feb. 2021 808-817*

Event-Triggered Adaptive Fuzzy Fixed-Time Tracking Control for a Class of Nonstrict-Feedback Nonlinear Systems. *Wang, H., +, TCSI July 2021 3058-3068*

Event-Triggered Optimized Control for Nonlinear Delayed Stochastic Systems. *Zhang, G., +, TCSI Sept. 2021 3808-3821*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

LMI-Based Robust Stability Analysis of Discrete-Time Fractional-Order Systems With Interval Uncertainties. *Zhu, Z., +, TCSI April 2021 1671-1680*

Observer-Based Adaptive Neural Output Feedback Constraint Controller Design for Switched Systems Under Average Dwell Time. *Liu, L., +, TCSI Sept. 2021 3901-3912*

Output Feedback Predefined-Time Bipartite Consensus Control for High-Order Nonlinear Multiagent Systems. *Li, K., +, TCSI July 2021 3069-3078*

Polytopic Event-Triggered Robust Model Predictive Control for Constrained Linear Systems. *Hu, Z., +, TCSI June 2021 2594-2603*

Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021 1297-1307*

State Bumpless Transfer Control for a Class of Switched Descriptor Systems. *Shi, J., +, TCSI Sept. 2021 3846-3856*

Uncertain Disturbance Rejection and Attenuation for Semi-Markov Jump Systems With Application to 2-Degree-Freedom Robot Arm. *Yao, X., +, TCSI Sept. 2021 3836-3845*

Uncertainty

Interval Observer-Based Robust Coordination Control of Multi-Agent Systems Over Directed Networks. *Wang, X., +, TCSI Dec. 2021 5145-5155*

Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

Unsupervised learning

Hardware Self-Organizing Map Based on Digital Frequency-Locked Loop and Triangular Neighborhood Function. *Hikawa, H., TCSI March 2021 1245-1258*

Uplink

Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. *Qiu, H., +, TCSI Dec. 2021 5182-5193*

V**Varactors**

77.3-GHz Standing-Wave Oscillator Based on an Asymmetrical Tunable Slow-Wave Coplanar Stripline Resonator. *Gomes, L., +, TCSI Aug. 2021 3158-3169*

Variable structure systems

Adaptive Cooperative Terminal Sliding Mode Control for Distributed Energy Storage Systems. *Yang, Y., +, TCSI Jan. 2021 434-443*

Adaptive Event-Triggered SMC for Stochastic Switching Systems With Semi-Markov Process and Application to Boost Converter Circuit Model. *Qi, W., +, TCSI Feb. 2021 786-796*

An Approach to Estimate Lithium-Ion Battery State of Charge Based on Adaptive Lyapunov Super Twisting Observer. *Sethia, G., +, TCSI March 2021 1319-1329*

Asynchronous Event-Triggered Sliding Mode Control for Semi-Markov Jump Systems Within a Finite-Time Interval. *Wang, J., +, TCSI Jan. 2021 458-468*

Control of Power Converters With Hybrid Affine Models and Pulse-Width Modulated Inputs. *Albea, C., +, TCSI Aug. 2021 3485-3494*

Event-Triggered Sliding Mode Control of Power Systems With Communication Delay and Sensor Faults. *Chen, P., +, TCSI Feb. 2021 797-807*

Fixed-Time Fault-Tolerant Formation Control for Heterogeneous Multi-Agent Systems With Parameter Uncertainties and Disturbances. *Cheng, W., +, TCSI May 2021 2121-2133*

Fractional-Order Sliding Mode Approach of Buck Converters With Mismatched Disturbances. *Lin, X., +, TCSI Sept. 2021 3890-3900*

Robust Formation Control for Multi-Agent Systems: A Reference Correction Based Approach. *Fei, Y., +, TCSI June 2021 2616-2625*

Robust H_∞ Adaptive Sliding Mode Fault Tolerant Control for T-S Fuzzy Fractional Order Systems With Mismatched Disturbances. *Zhang, X., +, TCSI March 2021 1297-1307*

Synthesis of Constant Power Loads Using Switching Converters Under Sliding-Mode Control. *Martinez-Trevino, B.A., +, TCSI Jan. 2021 524-535*

Vectors

A Logic-Compatible eDRAM Compute-In-Memory With Embedded ADCs for Processing Neural Networks. *Yu, C., +, TCSI Feb. 2021 667-679*

An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. *Hasler, J., +, TCSI Feb. 2021 592-602*

BitSystolic: A 26.7 TOPS/W 2b~8b NPU With Configurable Data Flows for Edge Devices. *Yang, Q., +, TCSI March 2021 1134-1145*

Magnetoresistive Circuits and Systems: Embedded Non-Volatile Memory to Crossbar Arrays. *Agrawal, A., +, TCSI June 2021 2281-2294*

Vehicle dynamics

Adaptive Continuous Barrier Function Terminal Sliding Mode Control Technique for Disturbed Robotic Manipulator. *Mobayen, S., +, TCSI Oct. 2021 4403-4412*

Output Feedback Sliding Mode Control of Markovian Jump Systems and Its Application to Switched Boost Converter. *Wang, C., +, TCSI Dec. 2021 5134-5144*

Velocity control

Predictive Control of Low-Cost Three-Phase Four-Switch Inverter-Fed Drives for Brushless DC Motor Applications. *Naseri, F., +, TCSI March 2021 1308-1318*

Venture capital

Evaluating Performances and Importance of Venture Capitals: A Complex Network Approach. *Liu, J., +, TCSI May 2021 2060-2068*

Exploring Impact Factors of Risk Contagion in Venture Capital Markets: A Complex Network Approach. *Li, X., +, TCSI Oct. 2021 4268-4277*

Very large scale integration

Efficient Soft-Output Gauss-Seidel Data Detector for Massive MIMO Systems. *Zhang, C., +, TCSI Dec. 2021 5049-5060*

Vibration control

Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink. *Duan, N., +, TCSI April 2021 1610-1623*

Vibrations

Composite Velocity-Tracking Control for Flexible Gimbal System With Multi-Frequency-Band Disturbances. *Cui, Y., +, TCSI Oct. 2021 4360-4370*

Vibration Control of Conveying Fluid Pipe Based on Inerter Enhanced Nonlinear Energy Sink. *Duan, N., +, TCSI April 2021 1610-1623*

Video coding

Area and Power-Efficient Variable-Sized DCT Architecture for HEVC Using Muxed-MCM Problem. *Shabani, A., +, TCSI March 2021 1259-1268*

Video signal processing

RoadNet-RT: High Throughput CNN Architecture and SoC Design for Real-Time Road Segmentation. *Bai, L., +, TCSI Feb. 2021 704-714*

VLSI

Approximate Pruned and Truncated Haar Discrete Wavelet Transform VLSI Hardware for Energy-Efficient ECG Signal Processing. *Seidel, H.B., +, TCSI May 2021 1814-1826*

Efficient Row-Layered Decoder for Sparse Code Multiple Access. *Pang, X., +, TCSI Aug. 2021 3495-3507*

Fixed-Complexity Tree Search Schemes for Detecting Generalized Spatially Modulated Signals: Algorithms and Hardware Architectures. *Liu, T., +, TCSI Feb. 2021 904-917*

Ultra-low-Latency VLSI Architecture Based on a Linear Approximation Method for Computing N th Roots of Floating-Point Numbers. *Lyu, F., +, TCSI Feb. 2021 715-727*

Variable-Rate VLSI Architecture for 400-Gb/s Hard-Decision Product Decoder. *Jain, V., +, TCSI Jan. 2021 25-34*

Voltage

A 1.6-V Tolerant Multiplexer Switch With 0.96-V Core Devices in 28-nm CMOS Technology. *Biccario, G.E., +, TCSI Nov. 2021 4626-4635*

A 2.1 mW 2 MHz-BW 73.8 dB-SNDR Buffer-Embedded Noise-Shaping SAR ADC. *Kim, T., +, TCSI Dec. 2021 5029-5037*

A Three-Stage Charge Pump With Forward Body Biasing in 28 nm UTBB FD-SOI CMOS. *Pinheiro, C.A., +, TCSI Nov. 2021 4810-4819*

Accurately Modeling Zero-Bias Diode-Based RF Power Harvesters With Wide Adaptability to Frequency and Power. *Guo, L., +, TCSI Dec. 2021 5194-5205*

Voltage control

Buck Circuit Design With Pseudo-Constant Frequency and Constant On-Time for High Current Point-of-Load Regulation. *Chen, K., +, TCSI Oct. 2021 4062-4075*

Design Flow for Hybrid CMOS/Memristor Systems—Part I: Modeling and Verification Steps. *Maheshwari, S., +, TCSI Dec. 2021 4862-4875*

Distributed Observer-Based H_∞ Fault-Tolerant Control for DC Microgrids With Sensor Fault. *Huang, M., +, TCSI April 2021 1659-1670*

Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021 376-386*

The Analog Behavior of Pseudo Digital Ring Oscillators Used in VCO ADCs. *Borgmans, J., +, TCSI July 2021 2827-2840*

Voltage regulators

A 3-Phase Resonant Switched-Capacitor Converter for Data Center 48-V Rack Power Distribution. *Wang, C., +, TCSI June 2021 2714-2724*

A Fast-Transient Low-Dropout Regulator With Current-Efficient Super Transconductance Cell and Dynamic Reference Control. *Ming, X., +, TCSI June 2021 2354-2367*

A Time-Division-Multiplexed Clocked-Analog Low-Dropout Regulator. *Xie, Z., +, TCSI March 2021 1366-1376*

Modeling and Control of Islanded DC Microgrid Clusters With Hierarchical Event-Triggered Consensus Algorithm. *Chen, Z., +, TCSI Jan. 2021 376-386*

Voltage-controlled oscillators

A $+0.44^\circ\text{C}/-0.4^\circ\text{C}$ Inaccuracy Temperature Sensor With Multi-Threshold MOSFET-Based Sensing Element and CMOS Thyristor-Based VCO. *Li, J., +, TCSI March 2021 1102-1113*

A 0.003-mm^2 $440\text{fs}_{\text{RMS}}$ -Jitter and -64dBc -Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. *Yang, Z., +, TCSI June 2021 2307-2316*

A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation. *Xu, X., +, TCSI Sept. 2021 3611-3620*

A New Boosted Active-Capacitor With Negative- G_m for Wide Tuning Range VCOs. *Agarwal, P., +, TCSI March 2021 1080-1090*

Nonlinear Analysis of Charge-Pump Phase-Locked Loop: The Hold-In and Pull-In Ranges. *Kuznetsov, N., +, TCSI Oct. 2021 4049-4061*

The Analog Behavior of Pseudo Digital Ring Oscillators Used in VCO ADCs. *Borgmans, J., +, TCSI July 2021 2827-2840*

Voltage-source converters

Modeling and Simulation of Variable Limits on Conditional Anti-Windup PI Controllers for VSC-Based Devices. *Murad, M.A.A., +, TCSI July 2021 3079-3088*

Stability Assessment for Multi-Infeed Grid-Connected VSCs Modeled in the Admittance Matrix Form. *Orellana, L., +, TCSI Sept. 2021 3758-3771*

Voltammetry (chemical analysis)

A Scalable 128-Channel, Time-Multiplexed Potentiostat for Parallel Electrochemical Experiments. *Molderez, T.R., +, TCSI March 2021 1068-1079*

W**Walsh functions**

Walsh-Hadamard-Based Orthogonal Sampling Technique for Parallel Neural Recording Systems. *Ranjandish, R., +, TCSI April 2021 1740-1749*

Water pollution measurement

A Wide Dynamic Range Multi-Sensor ROIC for Portable Environmental Monitoring Systems With Two-Step Self-Optimization Schemes. *Choi, S., +, TCSI June 2021 2432-2443*

Wave digital filters

Vector Wave Digital Filters and Their Application to Circuits With Two-Port Elements. *Bernardini, A., +, TCSI March 2021 1269-1282*

Wave equations

A Fast and Fully Parallel Analog CMOS Solver for Nonlinear PDEs. *Malavipathirana, H., +, TCSI Aug. 2021 3363-3376*

Waveform analysis

Asymptotic Waveform Evaluation With Higher Order Poles. *Jiang, Y., +, TCSI April 2021 1681-1692*

Wide band gap semiconductors

A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*

A High-Temperature Model for GaN-HEMT Transistors and its Application to Resistive Mixer Design. *Salem, J.M., +, TCSI Feb. 2021 581-591*

A Novel Digital Control Method of Primary-Side Regulated Flyback With Active Clamping Technique. *Chen, M., +, TCSI Feb. 2021 950-962*

Accurate Modeling of the Effective Parasitic Parameters for the Laminated Busbar Connected With Paralleled SiC MOSFETs. *Wang, J., +, TCSI May 2021 2107-2120*

Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*

Wideband

Adaptive Dual-Input Analog RF Predistorter for Wideband 5G Communication Systems. *Kumar, A., +, TCSI Nov. 2021 4636-4647*

Wideband amplifiers

A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*

A Wideband Differential Linear Low-Noise Transconductance Amplifier With Active-Combiner Feedback in Complementary MGTR Configurations. *Guo, B., +, TCSI Jan. 2021 224-237*

Analysis and Design of a Broadband Output Stage With Current-Reuse and a Low Insertion-Loss Bypass Mode for CMOS RF Front-End LNAs. *Schroeder, D., +, TCSI May 2021 1800-1813*

Analysis and Design of a Broadband Receiver Front End for 0.1-to-40-GHz Application. *Hu, J., +, TCSI June 2021 2393-2403*

Baseband Fusion Technique for Filter-Less Wideband Transmitters. *Tripathi, G.C., +, TCSI Aug. 2021 3508-3519*

Broadband Amplifier Design Technique by Dissipative Matching Networks. *Ciccognani, W., +, TCSI Jan. 2021 148-160*

- Dual Input Digitally Controlled Broadband Three-Stage Doherty Power Amplifier With Back-Off Reconfigurability. *Barthwal, A., +, TCSI April 2021 1421-1431*
- Wind turbines**
A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. *Zeng, Z., +, TCSI Sept. 2021 3587-3597*
- Wireless channels**
A 7.8–13.6 pJ/b Ultra-Low Latency and Reconfigurable Neural Network-Assisted Polar Decoder With Multi-Code Length Support. *Teng, C., +, TCSI May 2021 1956-1965*
- A Real-Time Hardware Emulator for 3D Non-Stationary U2V Channels. *Zhu, Q., +, TCSI Sept. 2021 3951-3964*
- Balanced and Unbalanced Duplexers Using Common Oval Dielectric Resonators. *Wu, D., +, TCSI Aug. 2021 3211-3221*
- Wireless communication**
Analysis and Mitigation of Coupling-Dependent Data Flipping in Wireless Power and Data Transfer System. *Qiu, H., +, TCSI Dec. 2021 5182-5193*
- Wireless LAN**
A 2.4–6 GHz Broadband GaN Power Amplifier for 802.11ax Application. *Liu, B., +, TCSI June 2021 2404-2417*
- Configurable Quasi-Optimal Sphere Decoding for Scalable MIMO Communications. *Wu, Y., +, TCSI June 2021 2675-2687*
- Wireless sensor networks**
A 197.1- μ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. *Hu, H., +, TCSI June 2021 2444-2456*

Z**Zero current switching**

A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine.
Zeng, Z., +, TCSI Sept. 2021 3587-3597

Zero voltage switching

A 6.78 MHz Single-Stage Wireless Power Transmitter Using a 3-Mode Zero-Voltage Switching Class-D PA. *Ge, X., +, TCSI June 2021 2736-2748*