

Keyword	Paper
2	
28fd-soi	Influence of Calibration Methods and RF Probes on the RF Characterization of 28FD-SOI MOSFET
2D materials	Six Decades of Research on 2D Materials: Progress, Dead Ends, and New Horizons
2D numerical simulation	Effect of Heat Sink Configuration Influence of Dielectrics and Channel Defects on the Electrical Performance of Oxide-based p-Channel TFTs for CMOS Applications on the Performance of Thermoelectric Refrigeration
A	
A-HIZO tfts	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
Acc	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Aging	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Al2O3/sio2	Determination of Carrier Lifetime in Silicon Using an Ultra-thin Al ₂ O ₃ /SiO ₂ Dielectric Stack
AM 1.5G illumination	Analysing the Efficiency Enhancement of Indoor Organic Photovoltaic using Impedance Spectroscopy Technique
Amorphous	High conductivity intrinsic a-SiGe films deposited at low-temperature
Amorphous oxide semiconductor	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
Analog circuits	Strategy for Simulation of Analog Circuits with GCSOI MOSFET using BSIM SOI model
Analytical model	Simple Analytical Modelling of an Electronically Tunable Potentiometer and Body Factor Influence
Analytical model	Analysis of the ZTC-Point for Vertically Stacked Nanosheet pMOS Devices
Ar ion implant edge termination	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode
Architecture	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Asymmetric spacers	Physical Modeling of Asymmetric Spacers Resonant Tunneling Diodes (RTDs)
B	
Ballistic	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?
Bio-TFET	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Biosensor	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Body factor	Simple Analytical Modelling of an Electronically Tunable Potentiometer and Body Factor Influence

Bsim soi	Strategy for Simulation of Analog Circuits with GCSOI MOSFET using BSIM SOI model
Bti	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
C	
Calibration	Influence of Calibration Methods and RF Probes on the RF Characterization of 28FD-SOI MOSFET
Capacitive sensor	Flexible Electrode Capacitive Sensors System for Human Fluid Detection
Channel defects	Effect of Heat Sink Configuration Influence of Dielectrics and Channel Defects on the Electrical Performance of Oxide-based p-Channel TFTs for CMOS Applications on the Performance of Thermoelectric Refrigeration
Characterization	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Charge sensitivity	A low power, charge-sensitive preamplifier integrated with a silicon nanowire biosensor
Charge trapping	Towards Unifying the Statistical Modeling of Charge Trapping in Time and Frequency Domain
Chemical sensor	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
Closed-form	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Cmos	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
CMOS technology	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Compact model	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation? Analytical Compact Model for Transcapacitances of Junctionless Nanowire Transistors
Compact model	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
Compact modeling	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Controlled dielectric breakdown	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Cop	Effect of Heat Sink Configuration on the Performance of Thermoelectric Refrigeration
D	
Daq	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
De-embedding	Influence of Calibration Methods and RF Probes on the RF Characterization of 28FD-SOI MOSFET
Demos	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
Dielectrics	Effect of Heat Sink Configuration Influence of Dielectrics and Channel Defects on the Electrical Performance of Oxide-based

	p-Channel TFTs for CMOS Applications on the Performance of Thermoelectric Refrigeration
DNA & RNA sensor	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
E	
EDA tools	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Effective carrier lifetime	Determination of Carrier Lifetime in Silicon Using an Ultra-thin Al ₂ O ₃ /SiO ₂ Dielectric Stack
Electrical coupling	2D array microelectrodes for sensing the action potential of the sinoatrial node
Electromagnetic fields	Electromagnetic Coherent Electron Control
Electron quantum optics	Electromagnetic Coherent Electron Control
Electron quantum transport	Electromagnetic Coherent Electron Control
Electron transport layer	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
Entangletronics	Electromagnetic Coherent Electron Control
Environmental applications	Semiconductor materials and devices for medical and environmental applications
Etp	Simple Analytical Modelling of an Electronically Tunable Potentiometer and Body Factor Influence
Extinction efficiency	Development of Multi-physics Modeling of Plasmonics in the UV Region Using Transition Metals
F	
Fdsoi	Assessment of RF compact modelling of FD SOI transistors
Ferroelectric	Ultrascaled Multidomain P(VDF-TrFE) Organic Ferroelectric Gate Stack to the Rescue Improved Device Performance of Polarity Controllable-Ferroelectric-Field Effect Transistor Under the Influence of Fixed Trap Charges Fully-Coupled Simulation of the Temperature Effect on Negative Capacitance Ferroelectric Devices
Ferroelectrics	Steep Subthreshold Swing in Double Gate NCFET:A Simulation Study
Fet	Steep Subthreshold Swing in Double Gate NCFET:A Simulation Study
Field emission	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Fitting	Direct extraction of solar cell model parameters using optimization methods
Fixed trap charge	Improved Device Performance of Polarity Controllable-Ferroelectric-Field Effect Transistor Under the Influence of Fixed Trap Charges
Flexible electrode	Flexible Electrode Capacitive Sensors System for Human Fluid Detection
Flexible substrate	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Flicker noise	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation

Fluid detection	Flexible Electrode Capacitive Sensors System for Human Fluid Detection
Fluidic cell	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Fpga	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Fringing field	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Fully Depleted (FD) SOI transistor	SOI technologies for RF and millimeter-wave integrated circuits
G	
GAA-nanosheet pmos	Analysis of the ZTC-Point for Vertically Stacked Nanosheet pMOS Devices
Gas sensor	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
Graded-Channel transistors	Strategy for Simulation of Analog Circuits with GCSOI MOSFET using BSIM SOI model
H	
Halo	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
HCI degradation	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Heat sink	Effect of Heat Sink Configuration on the Performance of Thermoelectric Refrigeration
Heterojunction	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode
High mobility tfts	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
High voltage	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
High-conductivity	High conductivity intrinsic a-SiGe films deposited at low-temperature
High-k insulator	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
Horizontal current bipolar transistor	Versatile BiCMOS Technology Platform for the Low-cost Integration of Multi-purpose Applications
Hot carrier degradation	Impact of Hot Carrier Degradation on DC and RF Performance of 45-nm Power Amplifier Cell
I	
I/O protocols	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Impedance spectroscopy	Analysing the Efficiency Enhancement of Indoor Organic Photovoltaic using Impedance Spectroscopy Technique
Interface	2D array microelectrodes for sensing the action potential of the sinoatrial node
Interface	FULLY/PARTIALLY SUSPENDED GATE SiC-BASED FET FOR POWER APPLICATIONS

Ion sensor	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Isfet	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Isoborneol	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
Istft	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
J	
Jitter	Towards Unifying the Statistical Modeling of Charge Trapping in Time and Frequency Domain
Junctioless nanowire transistor	Analytical Compact Model for Transcapacitances of Junctionless Nanowire Transistors
Junctionless FET	Fully-Coupled Simulation of the Temperature Effect on Negative Capacitance Ferroelectric Devices
L	
Lambert W function	Direct extraction of solar cell model parameters using optimization methods
Landau-khalatnikov	Ultrascaled Multidomain P(VDF-TrFE) Organic Ferroelectric Gate Stack to the Rescue
Lateral bipolar transistors	Versatile BiCMOS Technology Platform for the Low-cost Integration of Multi-purpose Applications
Ldmos	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
Ldmosfet	Hybrid gate dielectric with Si ₃ N ₄ stressor for LD MOSFET
Leakage current	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Led 2700 k	Analysing the Efficiency Enhancement of Indoor Organic Photovoltaic using Impedance Spectroscopy Technique
Low noise amplifier	Versatile BiCMOS Technology Platform for the Low-cost Integration of Multi-purpose Applications
Low temperature	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Low temperature fabrication process	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
Low-frequency noise	Towards Unifying the Statistical Modeling of Charge Trapping in Time and Frequency Domain
Low-temperature	High conductivity intrinsic a-SiGe films deposited at low-temperature
M	
Medical applications	Semiconductor materials and devices for medical and environmental applications
Memristor	Six Decades of Research on 2D Materials: Progress, Dead Ends, and New Horizons
Microcontroller	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Microelectrode array	2D array microelectrodes for sensing the action potential of the sinoatrial node

Microfluidics	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
Microsystems	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
Mobility	TCAD Evaluation of the Substrate Bias Influence on the Carrier Transport of Ω -Gate Nanowire MOS Transistors with Ultra-Thin BOX
Mosfet	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation? Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Multi-scale simulation	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?
Multidomain	Ultrascaled Multidomain P(VDF-TrFE) Organic Ferroelectric Gate Stack to the Rescue
N	
Nand flash memory	High-Density Solid-State Storage: A Long Path to Success
Nanoparticles	Development of Multi-physics Modeling of Plasmonics in the UV Region Using Transition Metals
Nanosheet transistor	Six Decades of Research on 2D Materials: Progress, Dead Ends, and New Horizons
Nanowire MOS transistors	TCAD Evaluation of the Substrate Bias Influence on the Carrier Transport of Ω -Gate Nanowire MOS Transistors with Ultra-Thin BOX
Ncfet	Steep Subthreshold Swing in Double Gate NCFET:A Simulation Study
Negative capacitance	Fully-Coupled Simulation of the Temperature Effect on Negative Capacitance Ferroelectric Devices
Negf	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?
Nemfet	FULLY/PARTIALLY SUSPENDED GATE SiC-BASED FET FOR POWER APPLICATIONS
Nio	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode
O	
On-wafer TRL	Influence of Calibration Methods and RF Probes on the RF Characterization of 28FD-SOI MOSFET
Optimization	Direct extraction of solar cell model parameters using optimization methods
Organic photovoltaic	Analysing the Efficiency Enhancement of Indoor Organic Photovoltaic using Impedance Spectroscopy Technique
Organic solar cells	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
Organic thin-film transistor	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
P	

P-DTS(fbtth2)2:PC70BM solar cells	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
P-type TFT	Effect of Heat Sink Configuration Influence of Dielectrics and Channel Defects on the Electrical Performance of Oxide-based p-Channel TFTs for CMOS Applications on the Performance of Thermoelectric Refrigeration
Packaging methods	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
Parameter extraction	Direct extraction of solar cell model parameters using optimization methods
Pbttt	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
Pecvd	High conductivity intrinsic a-SiGe films deposited at low-temperature
Permittivity	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Pfn etl	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
Ph	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Phase-change memory	High-Density Solid-State Storage: A Long Path to Success
Photovoltaic panels	Direct extraction of solar cell model parameters using optimization methods
Physical modelling	Physical Modeling of Asymmetric Spacers Resonant Tunneling Diodes (RTDs)
Polarisation	Steep Subthreshold Swing in Double Gate NCFET:A Simulation Study
Polarity controllable	Improved Device Performance of Polarity Controllable-Ferroelectric-Field Effect Transistor Under the Influence of Fixed Trap Charges
Post-silicon validation	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Power	Hybrid gate dielectric with Si ₃ N ₄ stressor for LDMOSFET
Power amplifier (pa)	Impact of Hot Carrier Degradation on DC and RF Performance of 45-nm Power Amplifier Cell
Power spectral density	Compact Modeling of Flicker Noise in High Voltage MOSFETs and Experimental Validation
Preamplifier	A low power, charge-sensitive preamplifier integrated with a silicon nanowire biosensor
Process	Hybrid gate dielectric with Si ₃ N ₄ stressor for LDMOSFET
Pseudo-resistance	A low power, charge-sensitive preamplifier integrated with a silicon nanowire biosensor
Pvp	Cross-linked poly(4-vinylphenol) in thin-film transistors for water analysis
Q	
Quantum interference	Electromagnetic Coherent Electron Control
Quantum transport	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?
R	

Radio frequency magnetron sputtering	High Mobility Hf-In-ZnO TFTs, with HfO ₂ as Dielectric for Low Voltage Operation Range
Radio-frequency power amplifier	Versatile BiCMOS Technology Platform for the Low-cost Integration of Multi-purpose Applications
Random telegraph noise (rtn)	Towards Unifying the Statistical Modeling of Charge Trapping in Time and Frequency Domain
Reliability	Improved Device Performance of Polarity Controllable-Ferroelectric-Field Effect Transistor Under the Influence of Fixed Trap Charges Impact of Hot Carrier Degradation on DC and RF Performance of 45-nm Power Amplifier Cell
Resistance	2D array microelectrodes for sensing the action potential of the sinoatrial node
Resonant Tunneling Diodes (rtds)	Physical Modeling of Asymmetric Spacers Resonant Tunneling Diodes (RTDs)
RF and millimeter-wave performance	SOI technologies for RF and millimeter-wave integrated circuits
Rf cmos	SOI technologies for RF and millimeter-wave integrated circuits
RF measurements	Influence of Calibration Methods and RF Probes on the RF Characterization of 28FD-SOI MOSFET
RF switches	SOI technologies for RF and millimeter-wave integrated circuits
Rfet	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Risc-v	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Robustness	A low power, charge-sensitive preamplifier integrated with a silicon nanowire biosensor
Rtn	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
S	
Sbfet	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Schottky barrier	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Schottky diode	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode
Self-heating	Assessment of RF compact modelling of FD SOI transistors
Semiconductor	High conductivity intrinsic a-SiGe films deposited at low-temperature
Semiconductor materials and devices	Semiconductor materials and devices for medical and environmental applications
Sensitivity	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Sensor systems	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
Short channel	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?

Sic	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode FULLY/PARTIALLY SUSPENDED GATE SiC-BASED FET FOR POWER APPLICATIONS
Silicon	Hybrid gate dielectric with Si ₃ N ₄ stressor for LDMOSFET
Silicon nanowire biosensor	A low power, charge-sensitive preamplifier integrated with a silicon nanowire biosensor
Silicon nitride	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Silicon-based substrate	SOI technologies for RF and millimeter-wave integrated circuits
Silicon-germanium	High conductivity intrinsic a-SiGe films deposited at low-temperature
Silicon-germanium heterojunction bipolar transistor	Versatile BiCMOS Technology Platform for the Low-cost Integration of Multi-purpose Applications
Silicon-on-Insulator (SOI) technology	SOI technologies for RF and millimeter-wave integrated circuits
Silvaco atlas	Physical Modeling of Asymmetric Spacers Resonant Tunneling Diodes (RTDs)
Single-electron electronics	Electromagnetic Coherent Electron Control
Sno	Effect of Heat Sink Configuration Influence of Dielectrics and Channel Defects on the Electrical Performance of Oxide-based p-Channel TFTs for CMOS Applications on the Performance of Thermoelectric Refrigeration
Soi	Strategy for Simulation of Analog Circuits with GCSOI MOSFET using BSIM SOI model
Solar cell	Direct extraction of solar cell model parameters using optimization methods
Solid-state nanopores	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique
Solid-state storage	High-Density Solid-State Storage: A Long Path to Success
Solution-processed small molecule	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
Source underlap	Impact of Positive Charges in a Fringing Field Bio-Tunnel-FET Device with Source Underlap
Spectre simulations	Assessment of RF compact modelling of FD SOI transistors
Spi	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Spice simulation	Strategy for Simulation of Analog Circuits with GCSOI MOSFET using BSIM SOI model
Spray pyrolysis technique	ZnO Thin Film Deposited by Spray Pyrolysis for Long-Term Stable Organic Solar Cells
Stability of organic solar cells	ZnO Thin Film Deposited by Spray Pyrolysis for Long-Term Stable Organic Solar Cells
Storage-class memory	High-Density Solid-State Storage: A Long Path to Success
Stress	Hybrid gate dielectric with Si ₃ N ₄ stressor for LDMOSFET
Substrate bias	TCAD Evaluation of the Substrate Bias Influence on the Carrier Transport of Ω -Gate Nanowire MOS Transistors with Ultra-Thin BOX

Substrate effect	Assessment of RF compact modelling of FD SOI transistors
Surface recombination velocity	Determination of Carrier Lifetime in Silicon Using an Ultra-thin Al ₂ O ₃ /SiO ₂ Dielectric Stack
Suspended	FULLY/PARTIALLY SUSPENDED GATE SiC-BASED FET FOR POWER APPLICATIONS
System integration	A Simple Method for Seamless Integration of CMOS Chips with Microfluidics
T	
Tcad	Comprehensive Comparison of Fabricated 1.6-kV Punch-Through Design Ni/n-SiC Schottky Barrier Diode with Ar+ Implant Edge Termination and Heterojunction p-NiO/n-SiC Diode
Tcad simulation	Fully-Coupled Simulation of the Temperature Effect on Negative Capacitance Ferroelectric Devices
Technological nodes	Simple Analytical Modelling of an Electronically Tunable Potentiometer and Body Factor Influence
Temperature effect	Fully-Coupled Simulation of the Temperature Effect on Negative Capacitance Ferroelectric Devices
Test generation	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Testing	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Testing-platforms	An affordable post-silicon testing framework applied to a RISC-V based microcontroller
Tft	Recent advances of Ion Sensing based on Flexible Low Temperature Thin Film Transistors
Thermal impedance	Assessment of RF compact modelling of FD SOI transistors
Thermionic emission	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
Thermoelectric module	Effect of Heat Sink Configuration on the Performance of Thermoelectric Refrigeration
Thin BOX	TCAD Evaluation of the Substrate Bias Influence on the Carrier Transport of Ω -Gate Nanowire MOS Transistors with Ultra-Thin BOX
Thin films	High conductivity intrinsic a-SiGe films deposited at low-temperature
Threshold voltage	Improved Device Performance of Polarity Controllable-Ferroelectric-Field Effect Transistor Under the Influence of Fixed Trap Charges
Time dependent variability	Towards Unifying the Statistical Modeling of Charge Trapping in Time and Frequency Domain
Time-dependent variability	Circuit reliability prediction: challenges and solutions for the device time-dependent variability characterization roadblock
Transcapacitances	Analytical Compact Model for Transcapacitances of Junctionless Nanowire Transistors
Transition metals	Development of Multi-physics Modeling of Plasmonics in the UV Region Using Transition Metals
Transmembrane	Fabrication of Nanopores Using the Controlled Dielectric Breakdown Technique

Traps	FULLY/PARTIALLY SUSPENDED GATE SiC-BASED FET FOR POWER APPLICATIONS
Tunneling	Multiscale simulation: Can compact models be more than a one-way bridge between TCAD and circuit simulation?
Tunneling current	Uniform DC Compact Model for Schottky Barrier and Reconfigurable Field-Effect Transistors
U	
Ultra-thin dielectric stack	Determination of Carrier Lifetime in Silicon Using an Ultra-thin Al ₂ O ₃ /SiO ₂ Dielectric Stack
Utbb	Assessment of RF compact modelling of FD SOI transistors
Uv plasmonics	Development of Multi-physics Modeling of Plasmonics in the UV Region Using Transition Metals
W	
Wearables	Flexible Electrode Capacitive Sensors System for Human Fluid Detection
Wigner transport equation	Electromagnetic Coherent Electron Control
Z	
Zno electron interfacial layer	ZnO Thin Film Deposited by Spray Pyrolysis for Long-Term Stable Organic Solar Cells
Zno ETL	Impact of Hole Blocking Layer on the Performance of Solution-Processed Small Molecule Solar Cells
Ztc point	Analysis of the ZTC-Point for Vertically Stacked Nanosheet pMOS Devices