ISOTDAQ Poster award

ISOTDAQ 2019 - International School of Trigger and Data AcQuisition

ISOTDAQ Poster award

ISOTDAQ 2019 - International School of Trigger and Data AcQuisition

- **The following posters are specially commended.**
  - Simone Bologna (University of Bristol, CMS) Development of a jet finding algorithm for the CMS Phase-2 trigger upgrade
  - Lukas Gerritzen (ETH Zurich, Mu3e) The Mu3e experiment
  - Giulia Tuci (University of Pisa, LHCb) Reconstruction of track candidates at the LHC crossing rate using FPGAs
  - They all had a good design, clear message and pitched their content at the right level.

ISOTDAQ Poster award

ISOTDAQ 2019 - International School of Trigger and Data AcQuisition

- **The following posters are specially commended.**
  - Simone Bologna (University of Bristol, CMS) Development of a jet finding algorithm for the CMS Phase-2 trigger upgrade
  - Lukas Gerritzen (ETH Zurich, Mu3e) The Mu3e experiment
  - Giulia Tuci (University of Pisa, LHCb) Reconstruction of track candidates at the LHC crossing rate using FPGAs
  - They all had a good design, clear message and pitched their content at the right level.
- **The runner up is**
  - Markus Preston (Universtory of Stockholm, PANDA) Predicting the rate of single event upsets in a 28-nm FPGA in the PANDA experiment
  - This is an interesting project in which test beam data is used to validate simulation which is then used to make a prediction for a forthcoming experiment. The poster has an attractive design, clearly indicated route, good mix of text and figures, good text size and use of colour and shape. It sets up interesting problem which can be understood at a quick glance, and it makes sense whether skim reading or going through all the details, with a clear takeaway message.

ISOTDAQ Poster award

ISOTDAQ 2019 - International School of Trigger and Data AcQuisition

- **The following posters are specially commended.**
  - Simone Bologna (University of Bristol, CMS) Development of a jet finding algorithm for the CMS Phase-2 trigger upgrade
  - Lukas Gerritzen (ETH Zurich, Mu3e) The Mu3e experiment
  - Giulia Tuci (University of Pisa, LHCb) Reconstruction of track candidates at the LHC crossing rate using FPGAs
  - They all had a good design, clear message and pitched their content at the right level.
- **The runner up is**
  - Markus Preston (Universtory of Stockholm, PANDA) Predicting the rate of single event upsets in a 28-nm FPGA in the PANDA experiment
  - This is an interesting project in which test beam data is used to validate simulation which is then used to make a prediction for a forthcoming experiment. The poster has an attractive design, clearly indicated route, good mix of text and figures, good text size and use of colour and shape. It sets up interesting problem which can be understood at a quick glance, and it makes sense whether skim reading or going through all the details, with a clear takeaway message.
- **The winner is**
  - Giovanni Bassi (Scuola Normale Superiore Pisa, LHCb) Implementation of a clustering algorithm on FPGA for the LHCb silicon detector
  - This is one of several posters on the hot topic of implementing trigger algorithms on FPGAs. This one really stood out because of its exceptionally clear design and layout, clearly indicated route, and attractive use of colour and diagrams, all of which drew us in to learn more. Text and diagrams are used to great effect, giving a very clear explanation which can be understood quickly. The aim of the poster and take away message are very clear.