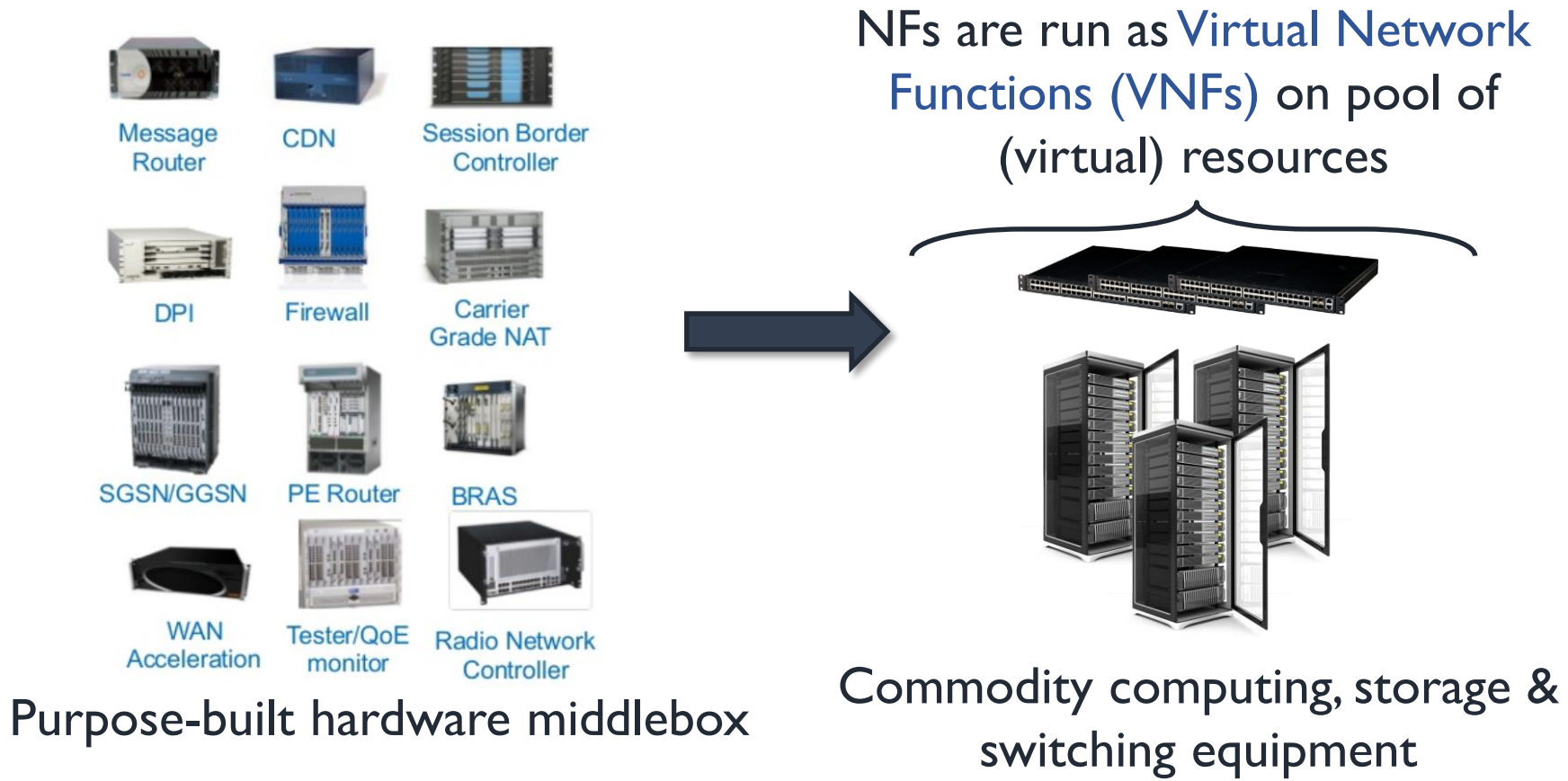


μ NF: A Disaggregated Packet Processing Architecture

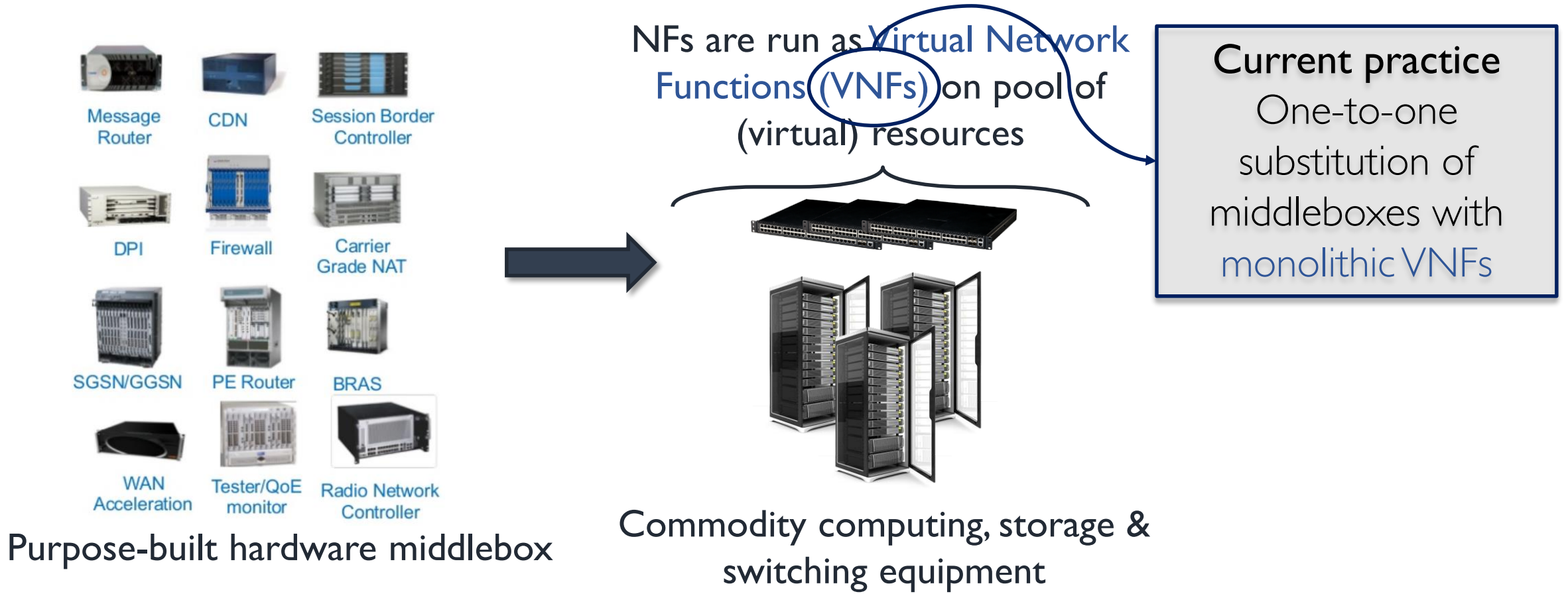
Shihabur Chowdhury, Anthony, Haibo Bian, Tim Bai, and Raouf Boutaba
David R. Cheriton School of Computer Science, University of Waterloo

IEEE NetSoft 2019, Paris, France, June 26, 2019

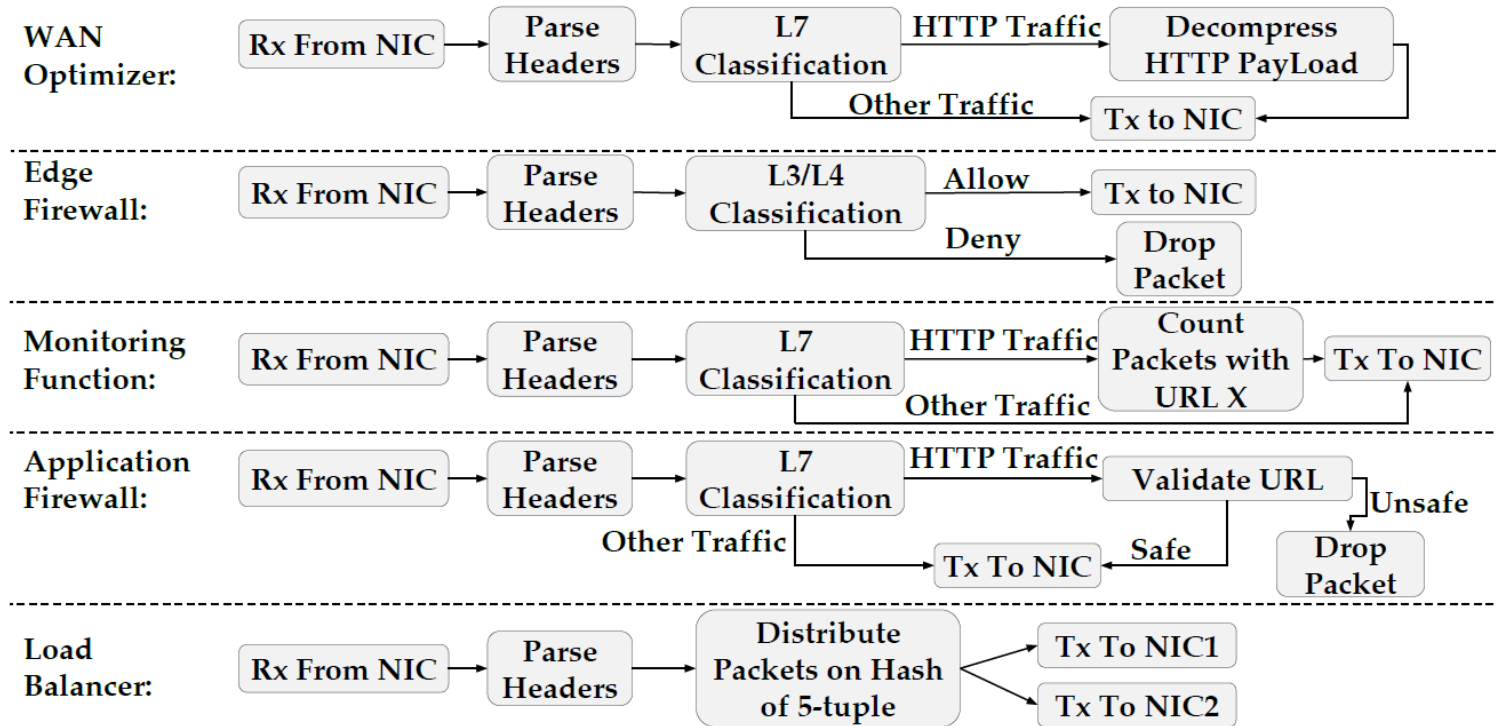
Transition from middleboxes to VNFs



Transition from middleboxes to VNFs



Monolithic VNF Limitations



Functional decomposition of commonly found NFs in Data Centers¹

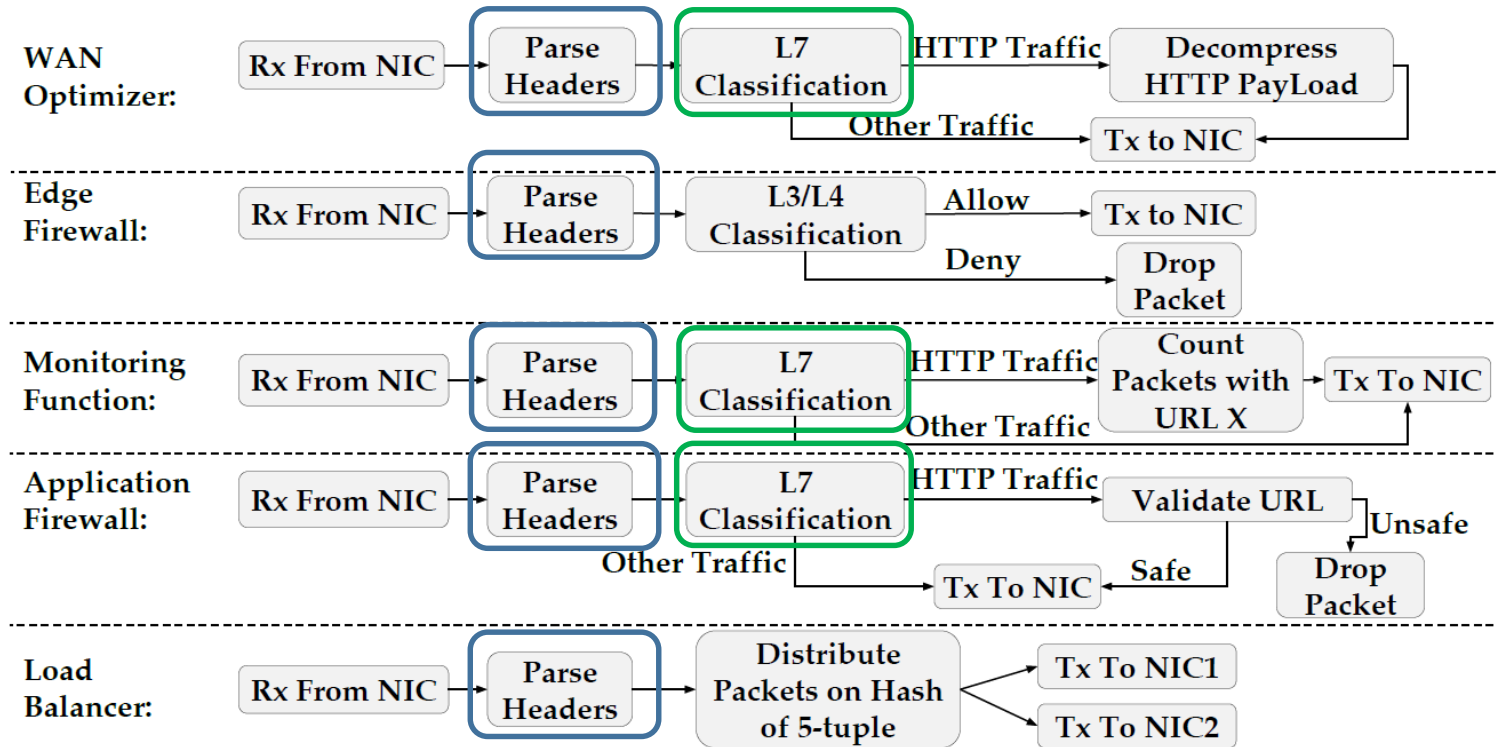
Monolithic VNF Limitations

Redundant development of common tasks

Coarse-grained resource allocation & scaling

1

2



Functional decomposition of commonly found NFs in Data Centers¹

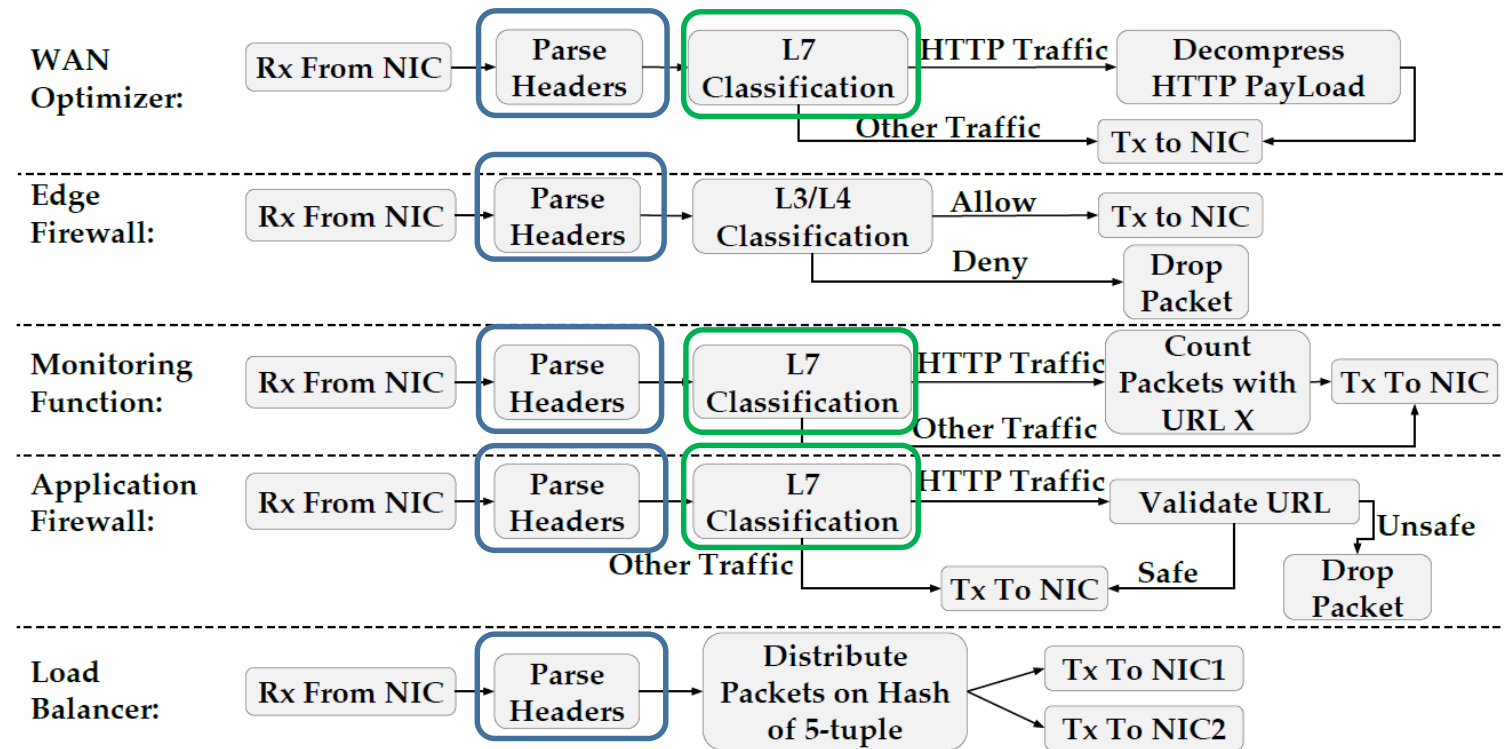
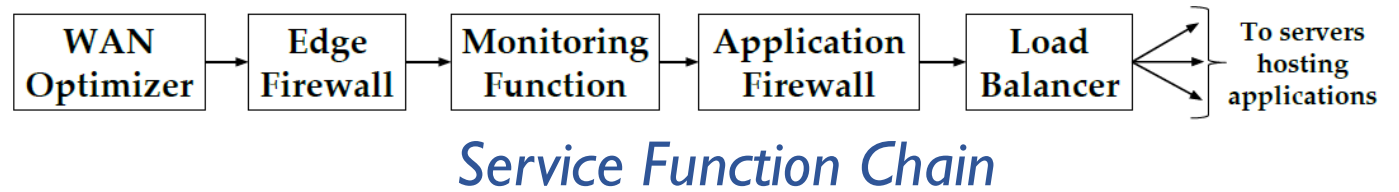
¹S.R. Chowdhury, *et al.* Re-architecting NFV Ecosystem with Microservices: State-of-the-art and Research Challenges. IEEE Network, 33(3): 168-176, May 2019

Monolithic VNF Limitations

1 Redundant development of common tasks

2 Coarse-grained resource allocation & scaling

3 Wasted CPU cycles when VNFs are chained



Functional decomposition of commonly found NFs in Data Centers¹

Monolithic VNFs: Impact on CPU usage

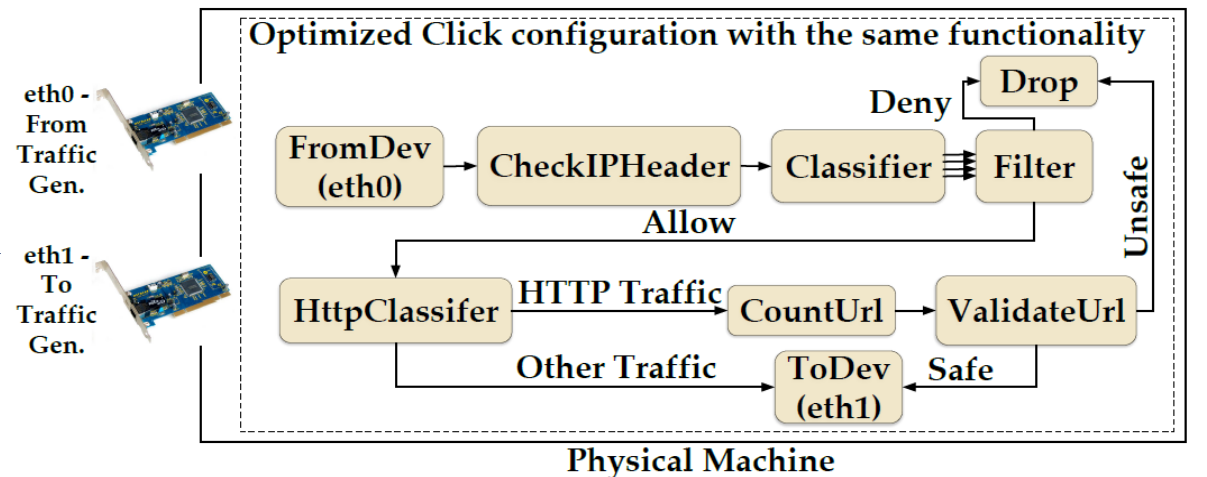
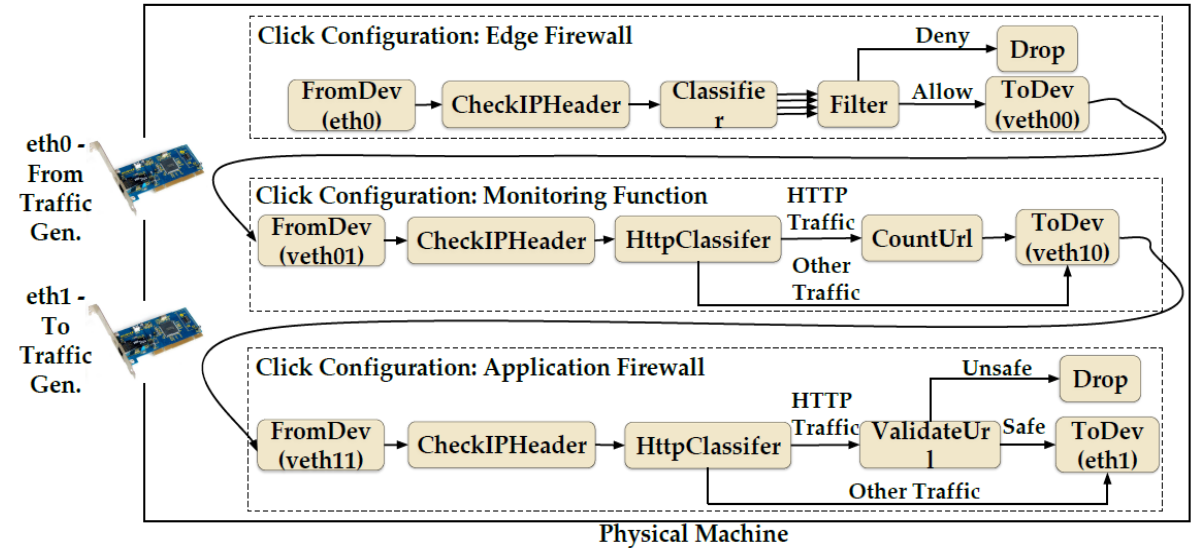
Edge Fw. → Monitoring → App. Fw

(C1) Click-based monolithic VNFs chained with veth pairs

Traffic

HTTP trace derived from a web-service (~15k hits/mo)

(C2) Optimized Click pipeline



Monolithic VNFs: Impact on CPU usage

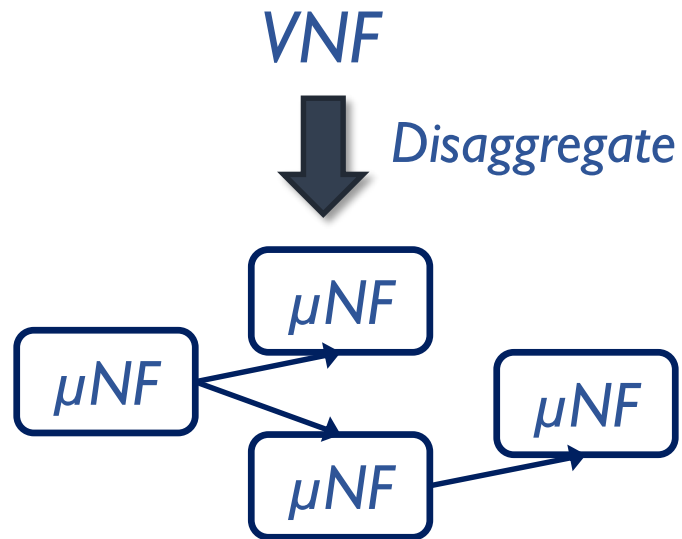
Click Element	CPU Cycles/packet saved in C2	Element weight in C1
FromDevice	71.9%	0.22%
ToDevice	67.1%	0.25%
CheckIPHeader	65.1%	0.44%
HTTPClassifier	48.28%	47.8%
Overall	29.5%	-

How can we engineer VNFs to better consolidate functions on the same hardware, enabling finer-grained resource allocation while maintaining the same level of performance as the state-of-the-art approaches?

How can we engineer VNFs to better consolidate functions on the same hardware, enabling finer-grained resource allocation while maintaining the same level of performance as the state-of-the-art approaches?

Microservices approach: Decompose VNFs into independently deployable and loosely-coupled packet processing entities.

Micro Network Functions (μ NFs)



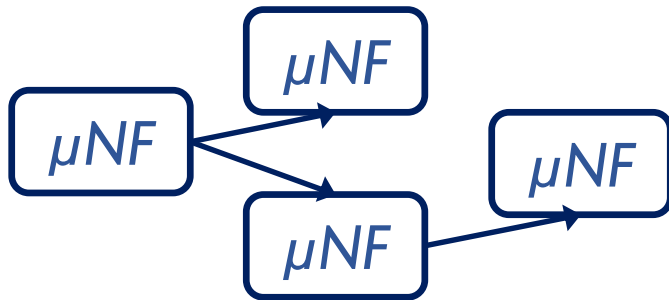
*μ NF Processing Graph:
Pipelined execution of μ NFs*

*μ NFs are:
reusable, loosely-coupled,
independently deployable*

Micro Network Functions (μ NFs)

VNF

Disaggregate



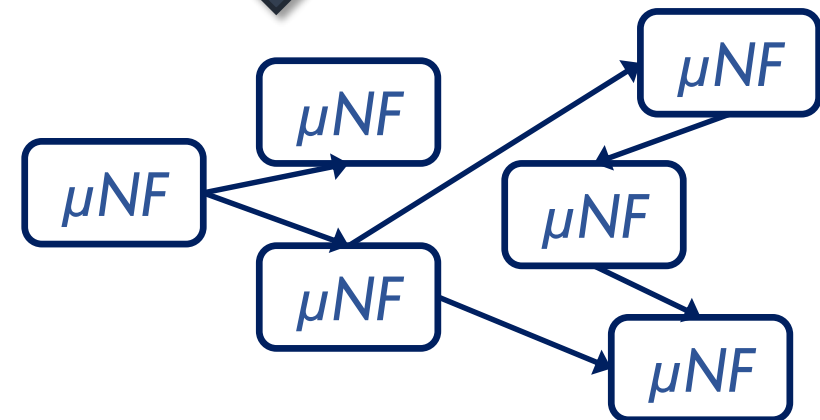
VNF templates (μ NF Processing Graph):
Pipelined execution of μ NFs

μ NFs are:
reusable, loosely-coupled,
independently deployable

SFC

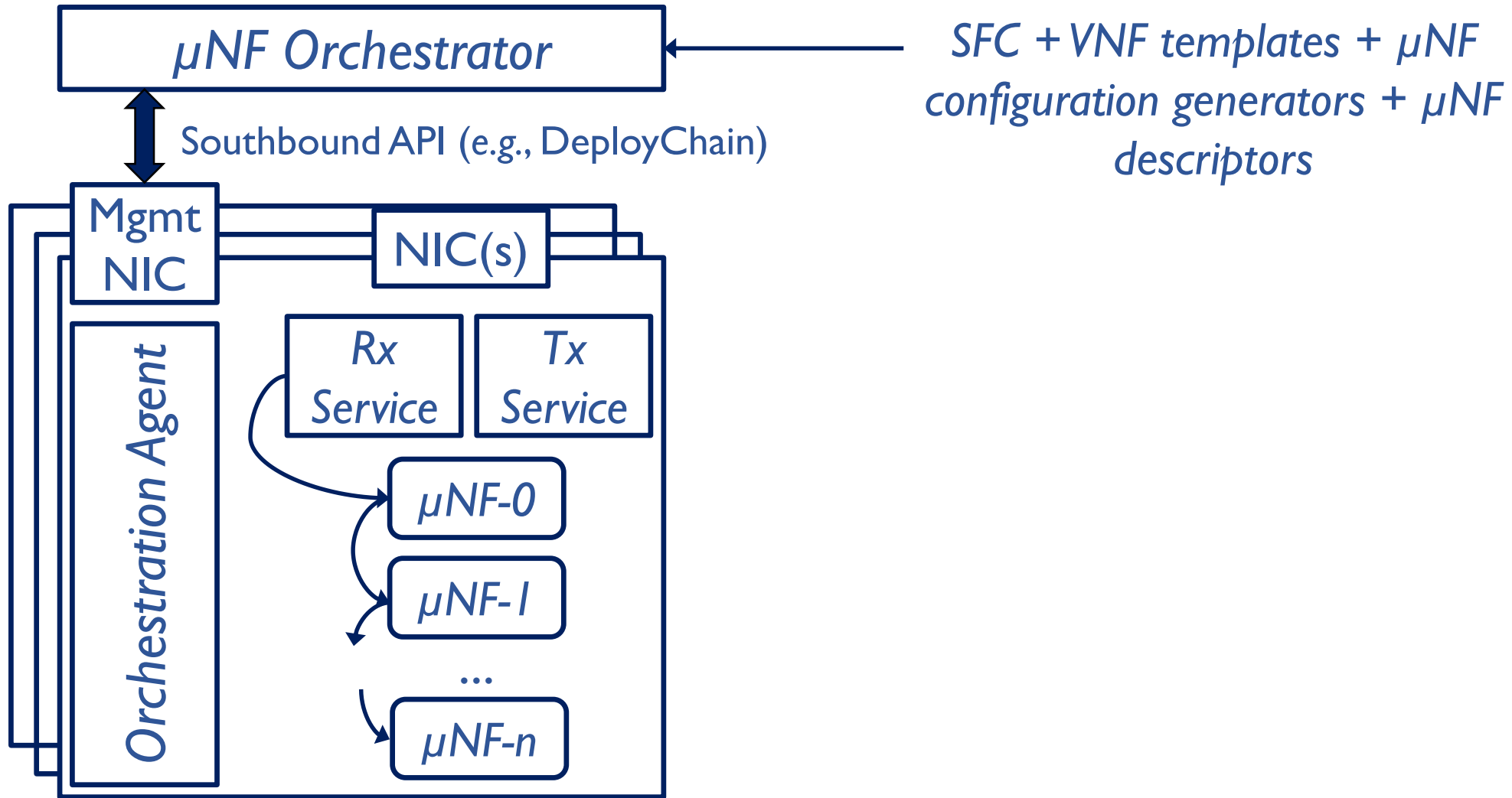


Repeated μ NFs removed
Similar μ NFs consolidated
 μ NF processing graphs merged

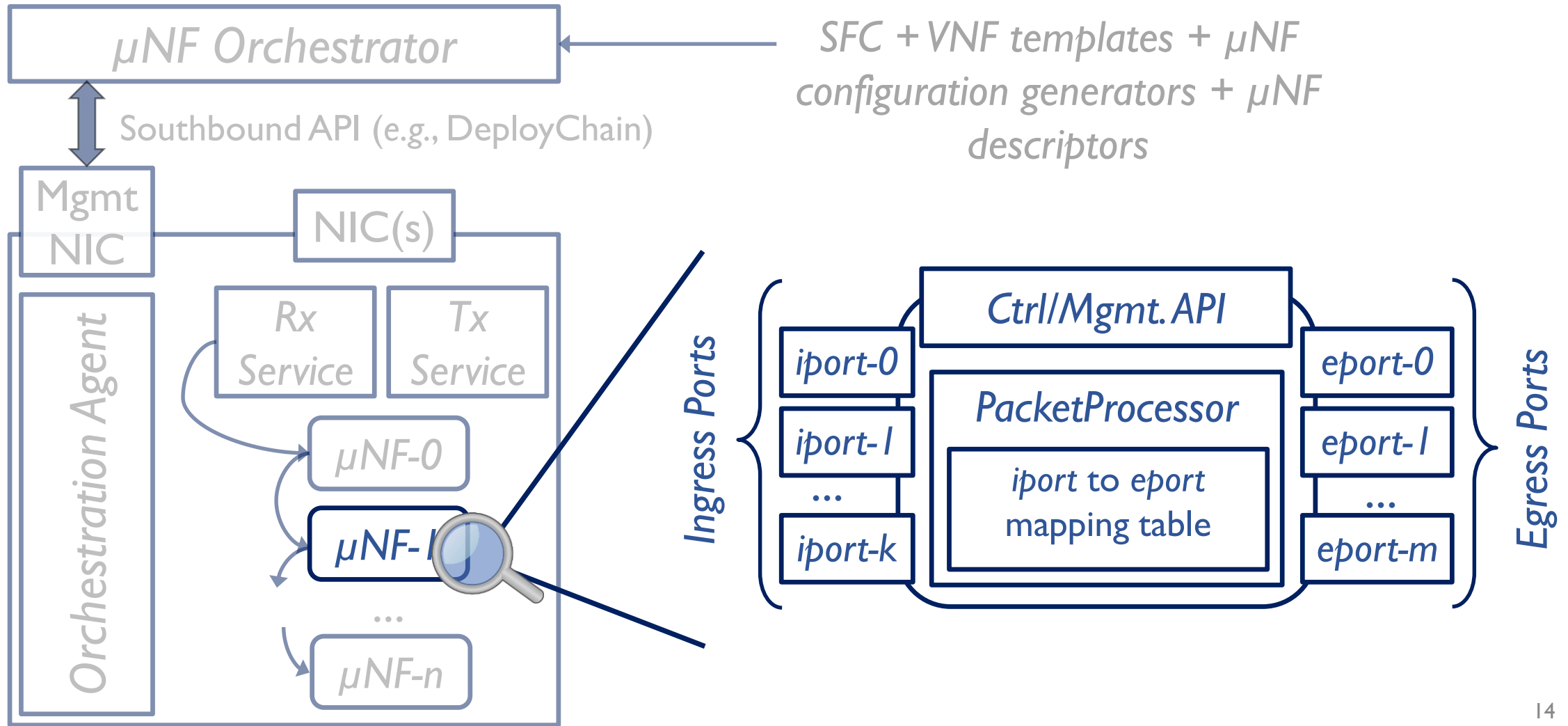


Optimized μ NF Processing Graph

System Overview



μ NF Components



Implementation

Agent

Primary DPDK process. Responsible for bootstrapping (initialize NIC, pre-allocate objects in memory, *etc.*)

Rx/
Tx

Implemented using **DPDK Poll Mode Driver** to bypass kernel. Implements packet classifier to distribute packets to μ NFs

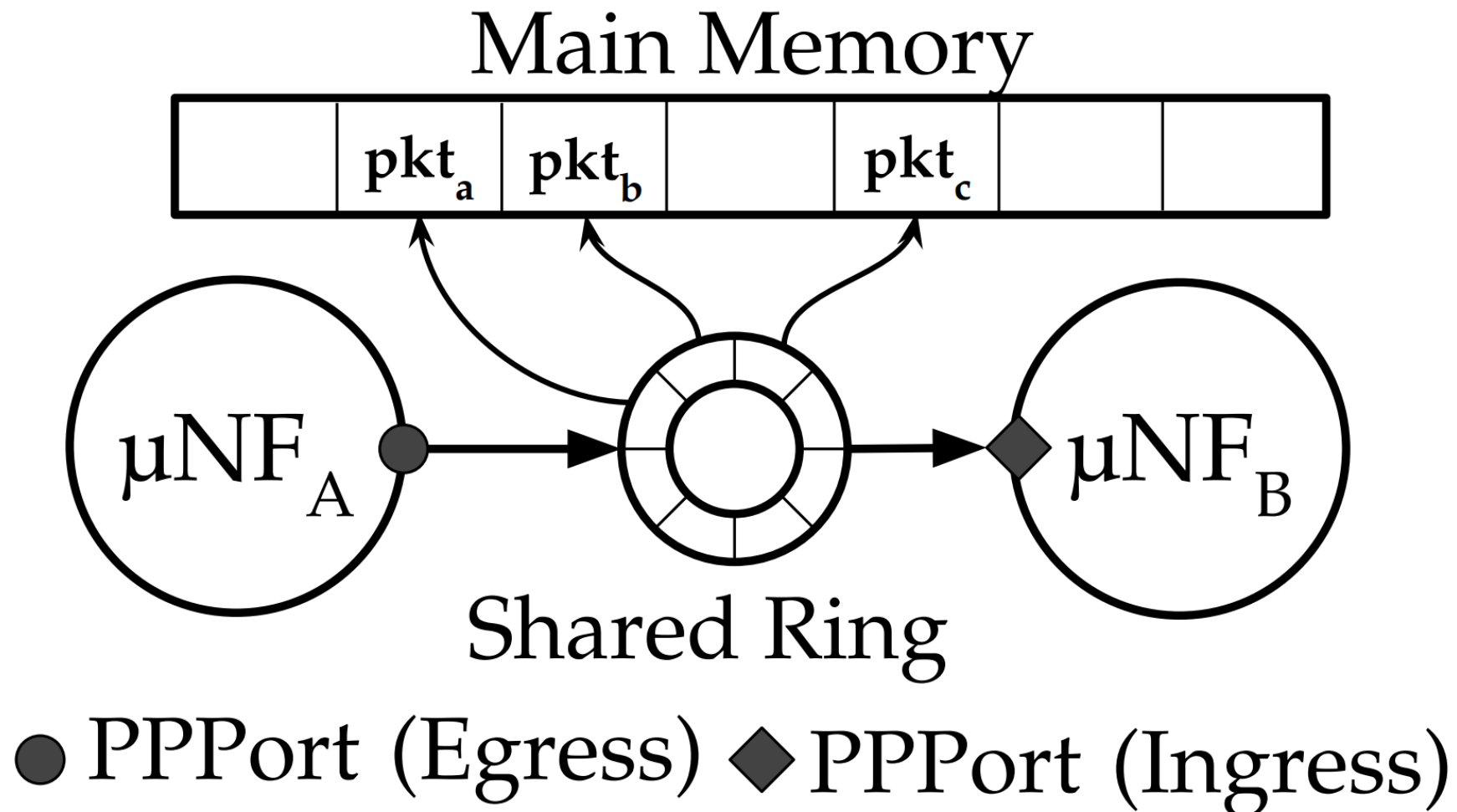
μ NF

Secondary DPDK processes. Obtains pre-allocated memory objects from the agent; works in polling mode.

Port

Implemented using **lockless multi-producer multi-consumer circular queue**. Holds packet references for **zero-copy packet exchange**.

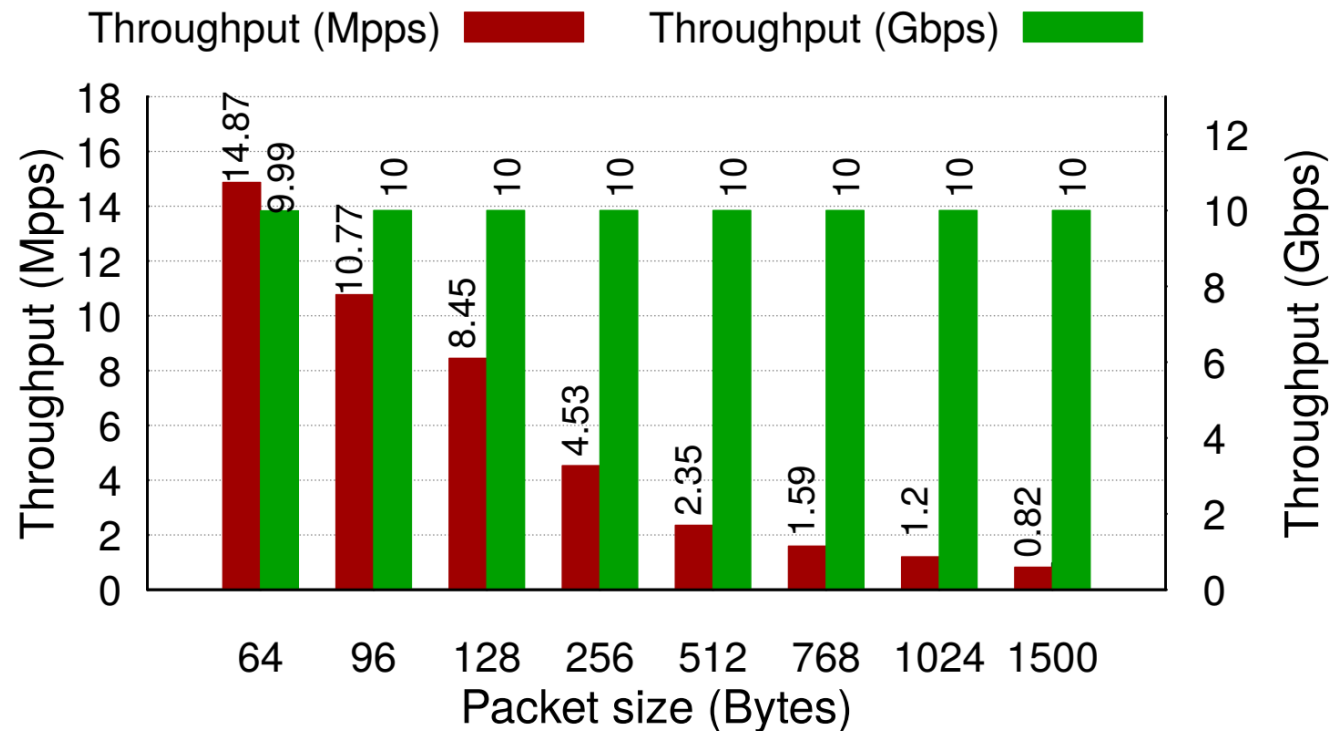
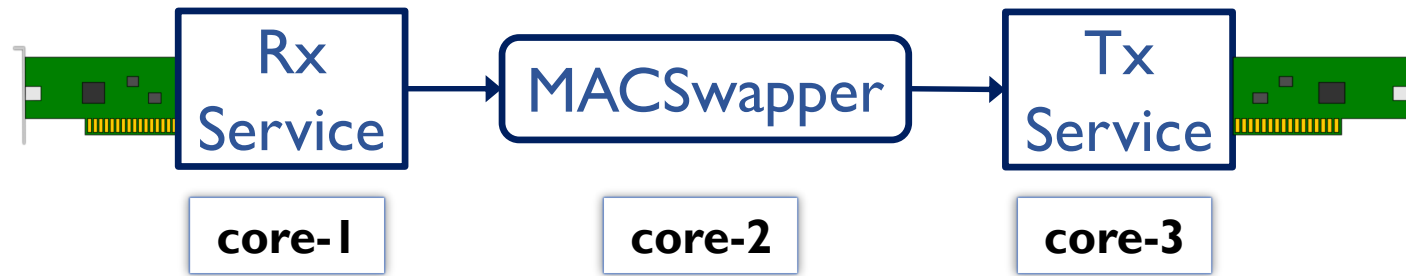
Point-to-Point Ingress/Egress Ports



Experiment Setup

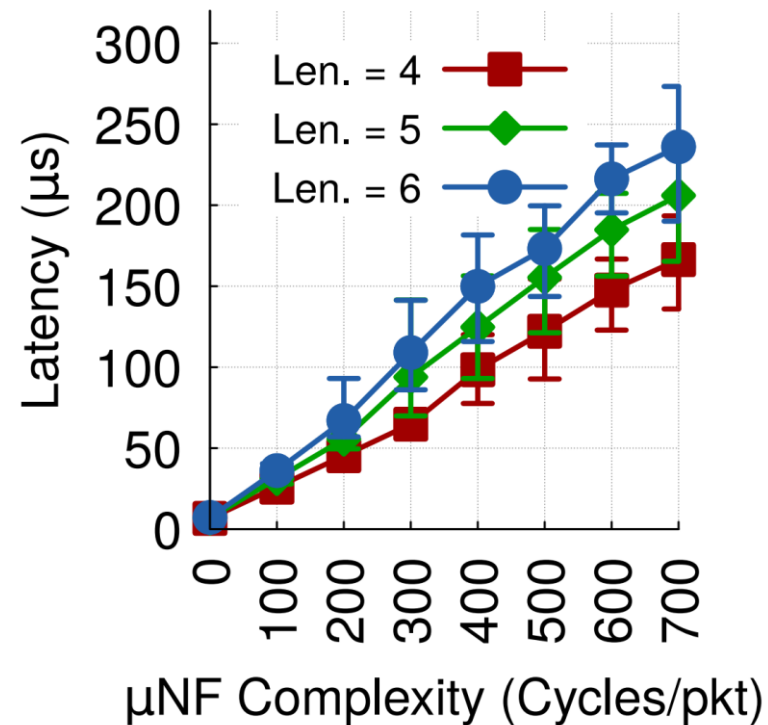
- Two machines connected back-to-back without a switch
- 2x6 core 2.1 GHz Intel Xeon E5 CPUs, 32GB RAM, Intel 10G NIC
- Hyper-threading disabled; All but cpu-0 isolated from kernel scheduler; μ NFs pinned to CPU cores
- Traffic generators: *pktgen-dpdk* (throughput) and *Moongen* (latency)

Microbenchmark: Throughput



Microbenchmark: Latency

Longer chain \rightarrow Higher Latency



Can we improve latency?

Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

The μ NFs do not modify
the same headers

1

The μ NFs do not modify
the packet stream

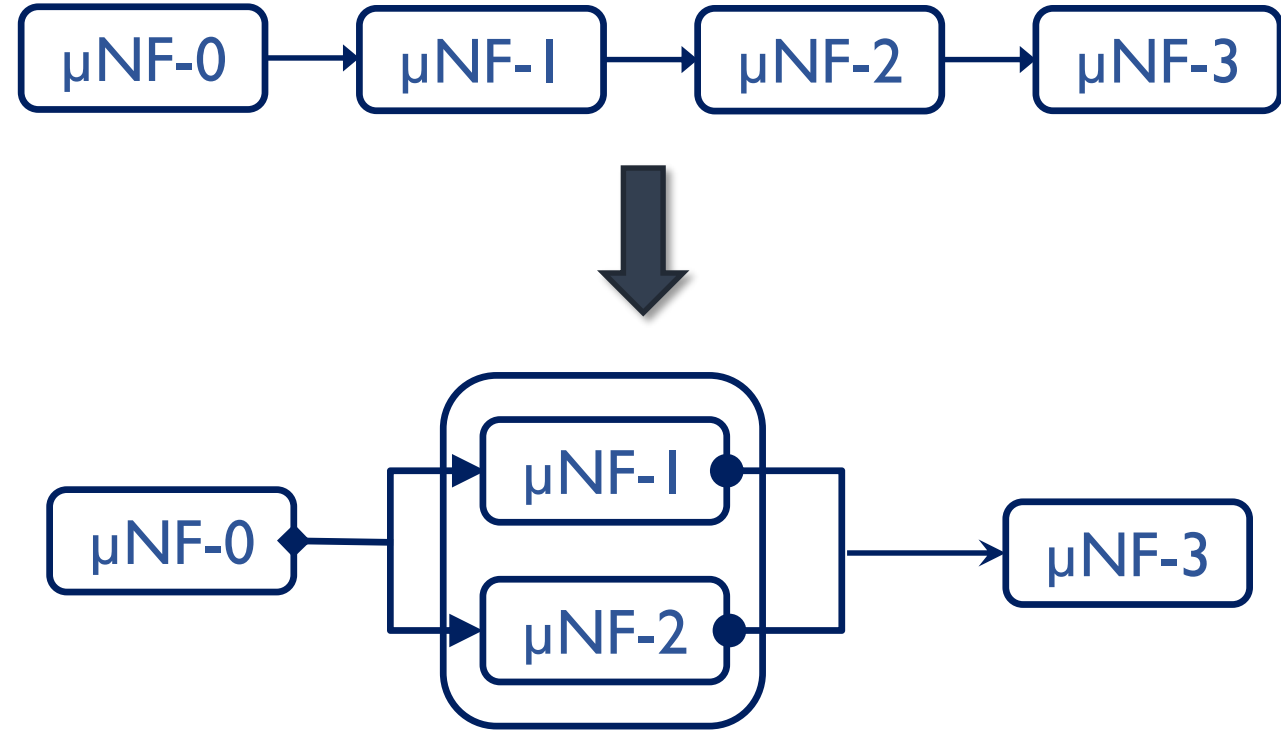
2

Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

1 The μ NFs do not modify
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2 The μ NFs do not modify
the packet stream

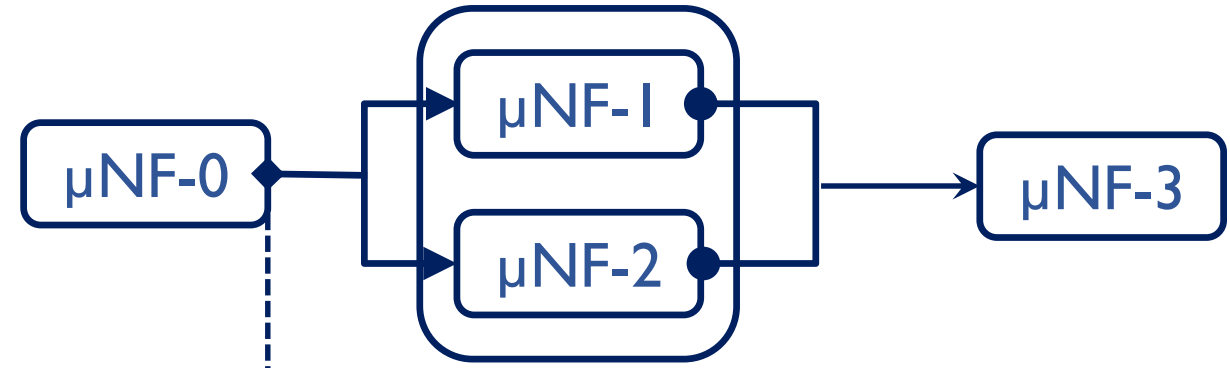
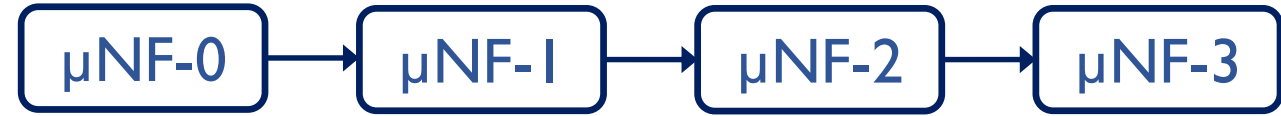


Optimization: Parallel execution of μ NFs

Parallelize sequential
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2 The μ NFs do not modify
the packet stream



BranchEgressPort

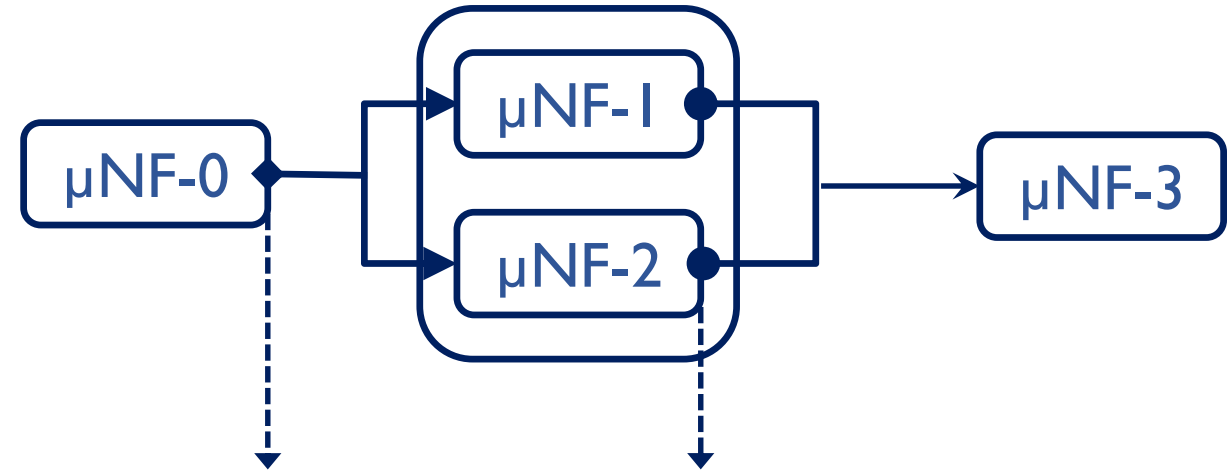
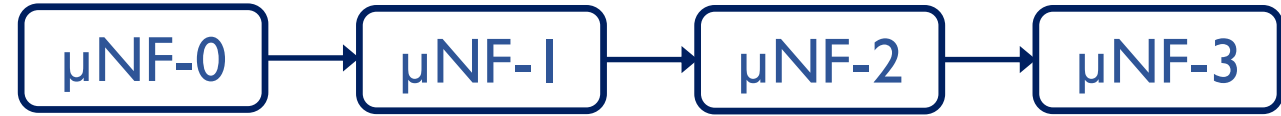
*Embeds an atomic
counter in packets*

Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

1 The μ NFs do not modify
the same headers

2 The μ NFs do not modify
the packet stream



BranchEgressPort *MarkerEgressPort*

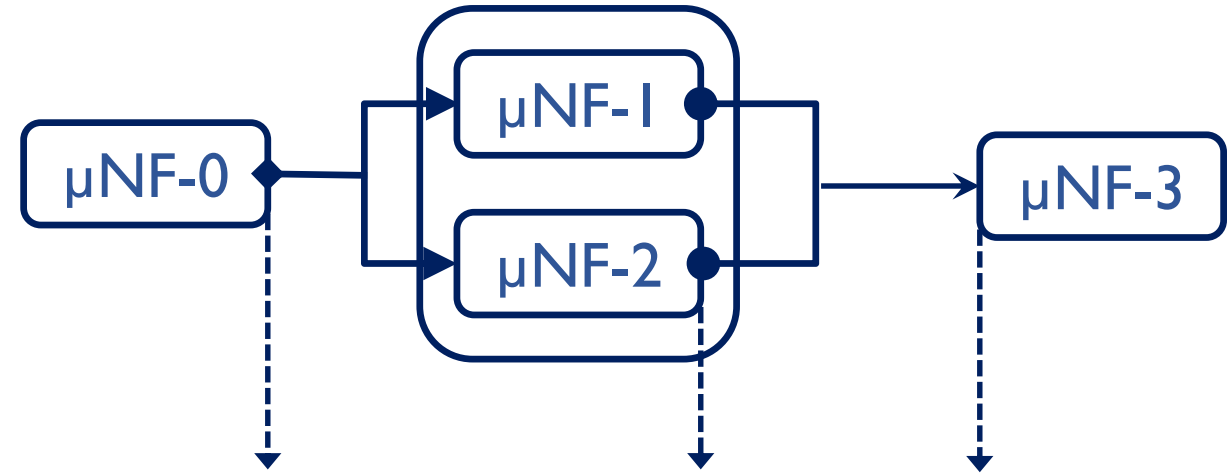
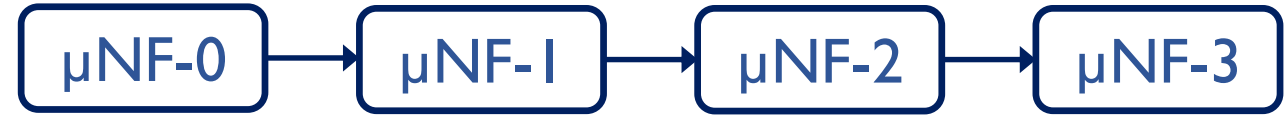
Embeds an atomic counter in packets *Increases atomic counter in packets*

Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

1 The μ NFs do not modify
the same headers

2 The μ NFs do not modify
the packet stream



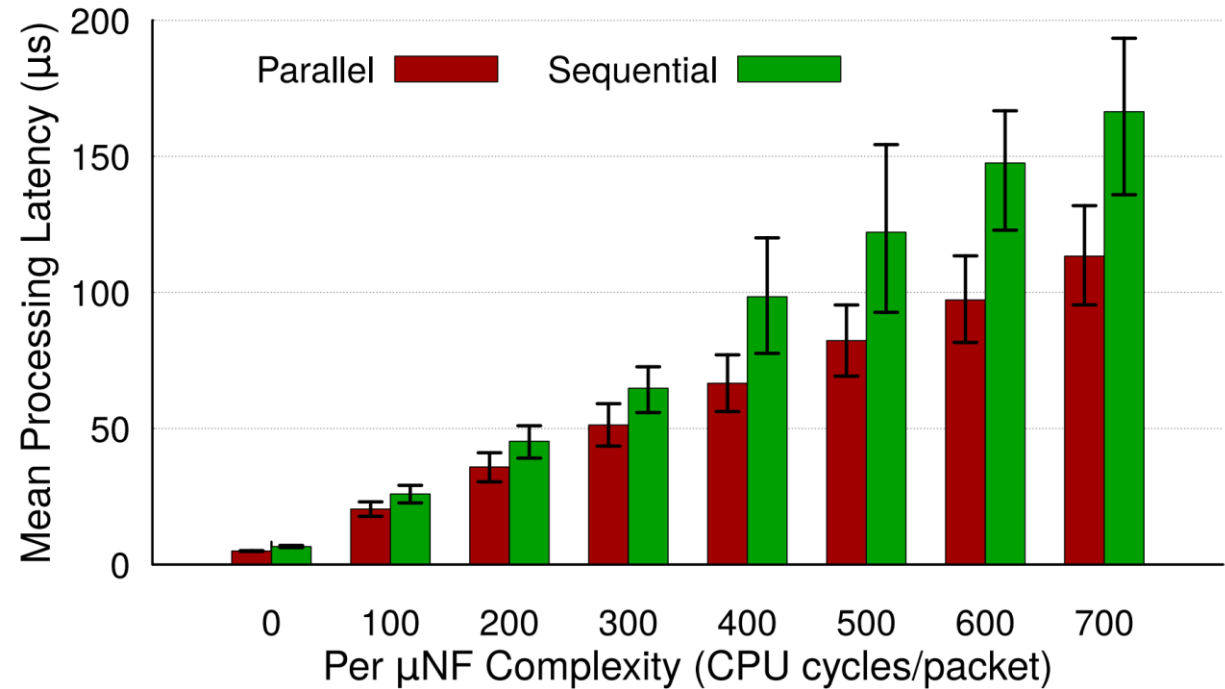
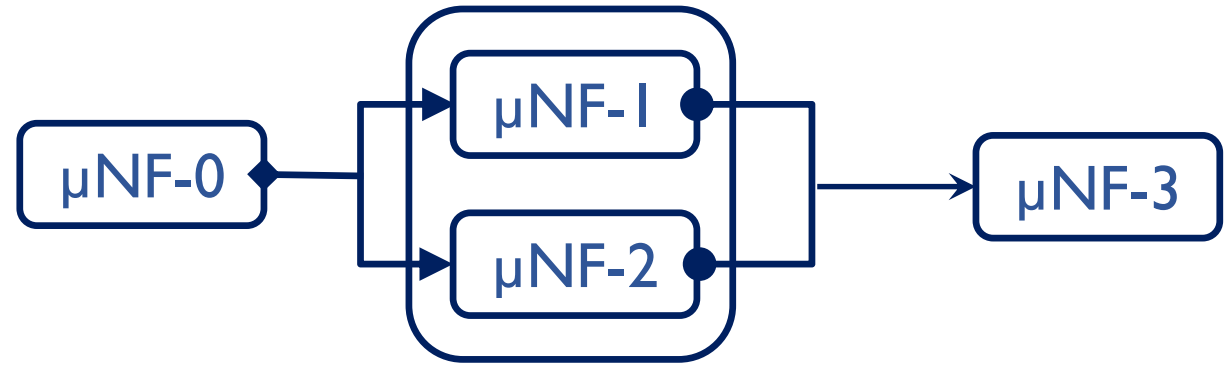
<i>BranchEgressPort</i>	<i>MarkerEgressPort</i>	<i>SyncIngressPort</i>
<i>Embeds an atomic counter in packets</i>	<i>Increases atomic counter in packets</i>	<i>Releases packets after all the μNFs have incremented the atomic counter</i> ²⁴

Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

The μ NFs do not modify
the same headers

The μ NFs do not modify
the packet stream

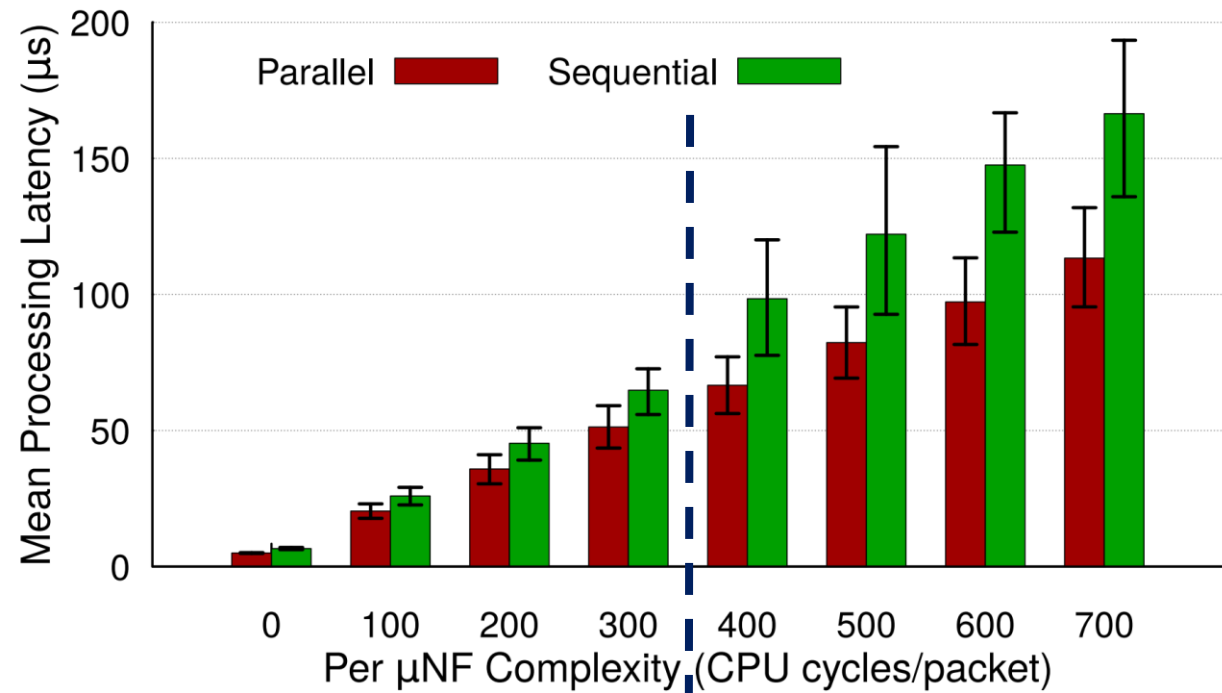
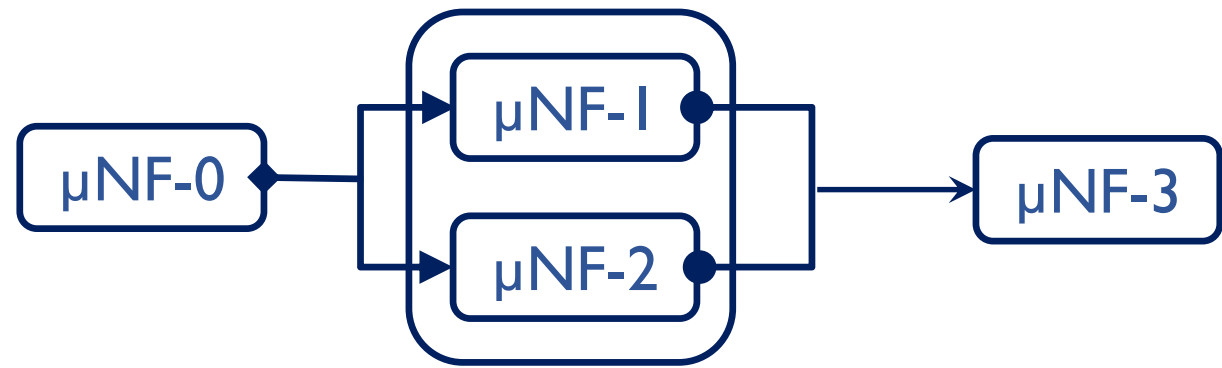


Optimization: Parallel execution of μ NFs

Parallelize sequential
blocks of μ NFs if:

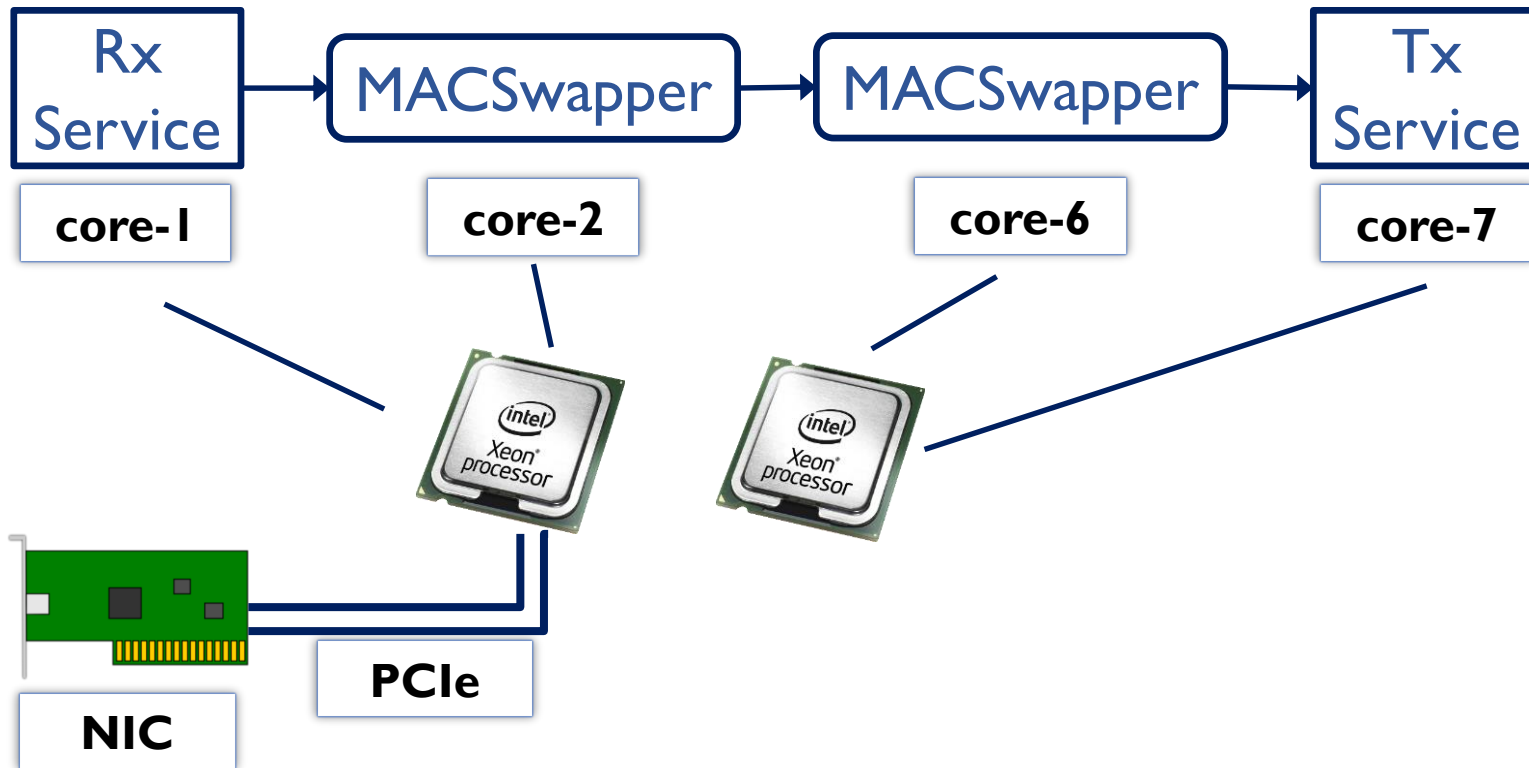
The μ NFs do not modify
the same headers

The μ NFs do not modify
the packet stream

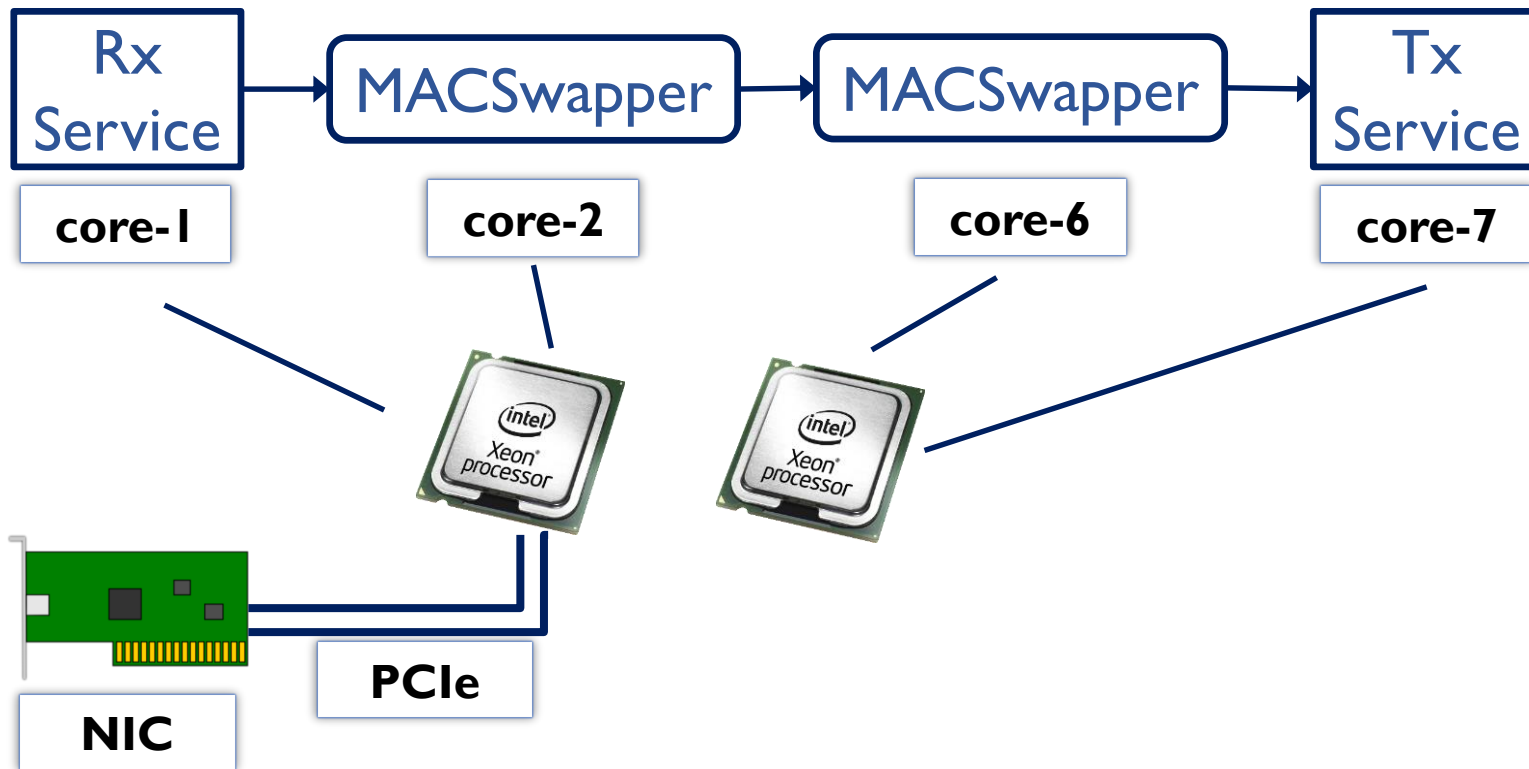


Not so significant gain | More gain

Impact of NUMA configuration



Impact of NUMA configuration



~3x drop in throughput

Optimization: Pipelined Cache- prefetching

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Before processing starts:
Prefetch a cacheline from
first k packets

|

Optimization: Pipelined Cache- prefetching

Before processing starts:

1

Prefetch a cacheline from
first k packets

While processing packet i :

2

Prefetch a cacheline from
packet $(i + k)$

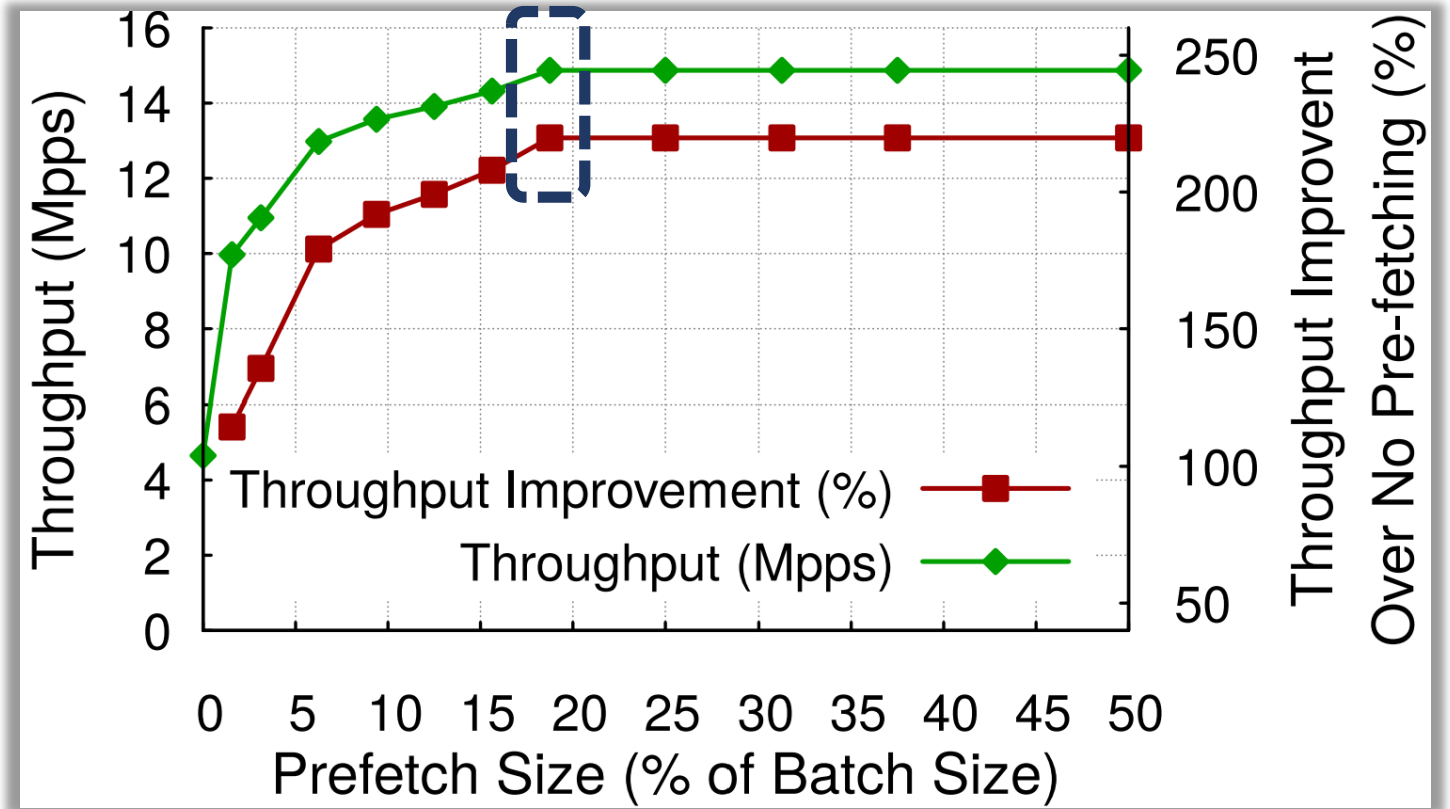
Optimization: Pipelined Cache- prefetching

Before processing starts:
Prefetch a cacheline from
first k packets

1

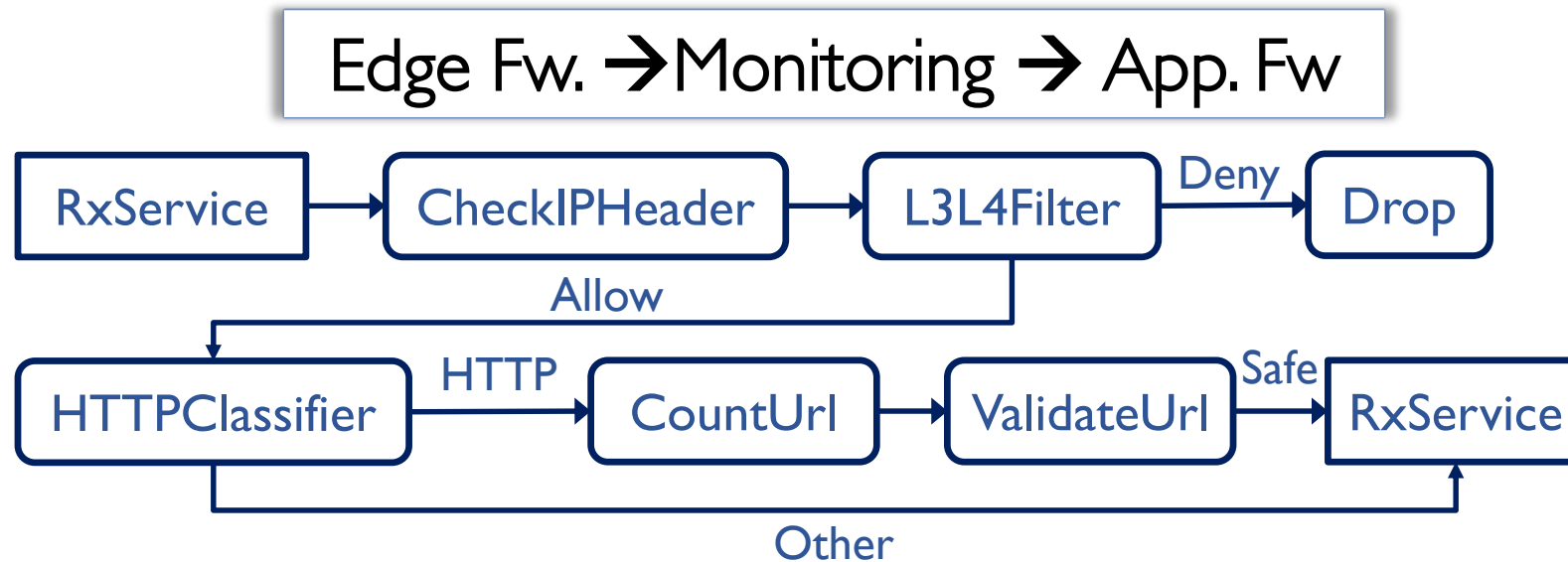
While processing packet i :
Prefetch a cacheline from
packet $(i + k)$

2



Prefetching **~20%** packets in a batch improves
throughput by **~3x**

Performance of μ NF-based SFC



Click Element	Saved cycles/packet	Element weight in CI
CheckIPHeader	27.8%	0.44%
HTTPClassifier	28.9%	47.8%
Overall	16.8%	-

What's Next?

- Disaggregated & pipelined-packet processing for 25/40/100G line rate

- End-to-end aspects of the system: e.g., optimized μ NF processing pipeline deployment with specific SLOs



<https://github.com/micronf>

Questions?