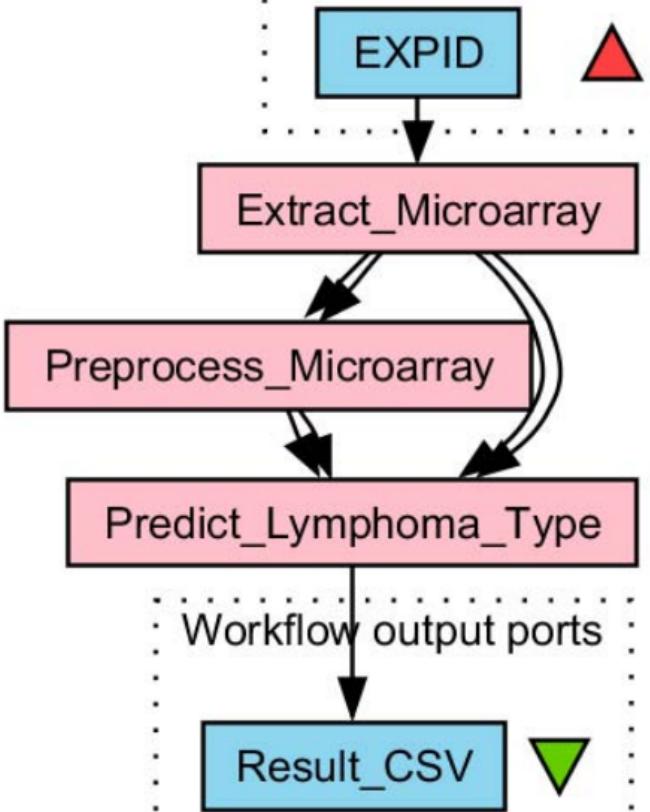


## Workflow input ports



SampleName	TrueClass	SVMPredClass	KNNPredClass
testSample_0	DLBCL	DLBCL	DLBCL
testSample_1	FL	FL	FL
testSample_2	DLBCL	DLBCL	DLBCL
testSample_3	FL	FL	FL
testSample_4	DLBCL	FL	DLBCL
testSample_5	DLBCL	DLBCL	DLBCL
testSample_6	FL	FL	FL
testSample_7	DLBCL	DLBCL	DLBCL
testSample_8	DLBCL	DLBCL	FL
testSample_9	DLBCL	DLBCL	DLBCL
testSample_10	DLBCL	DLBCL	DLBCL
testSample_11	FL	FL	FL
testSample_12	DLBCL	DLBCL	DLBCL
testSample_13	DLBCL	DLBCL	DLBCL
testSample_14	DLBCL	DLBCL	DLBCL
testSample_15	DLBCL	DLBCL	DLBCL
testSample_16	DLBCL	DLBCL	DLBCL
testSample_17	DLBCL	DLBCL	FL
testSample_18	DLBCL	DLBCL	DLBCL
testSample_19	FL	FL	FL
testSample_20	DLBCL	DLBCL	DLBCL
testSample_21	DLBCL	DLBCL	DLBCL
testSample_22	DLBCL	DLBCL	DLBCL
testSample_23	DLBCL	DLBCL	DLBCL
testSample_24	DLBCL	DLBCL	DLBCL