

Software Defined Radio Architectures

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graph TD; A[Software Defined Radio Architectures] --> B[Processor Centered Architectures]; A --> C[Reconfigurable Coarse Grained Architectures]; B --> D["ASIP/DSP (Leocore, Sandblaster, ConnXBBE, EVP etc.)"]; B --> E["Many-Core (SODA, tomahawk, Infineon etc.)"]; C --> F["Montium, BUTTER, CREMA, HERS, ADRES etc."];
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Processor Centered Architectures

ASIP/DSP (Leocore, Sandblaster, ConnXBBE, EVP etc.)

Many-Core (SODA, tomahawk, Infineon etc.)

Reconfigurable Coarse Grained Architectures

Montium, BUTTER, CREMA, HERS, ADRES etc.