

Efficient circuit-level modelling of ballistic CNT using piecewise non-linear approximation of mobile charge density

Tom J Kazmierski, Dafeng Zhou and Bashir M Al-Hashimi

School of Electronics and Computer Science, University of Southampton, Southampton, SO17 1BJ, UK
tjk,dz05r,bmah@ecs.soton.ac.uk

Abstract—This paper presents a new carbon nanotube transistor (CNT) modelling technique which is based on an efficient numerical piece-wise non-linear approximation of the non-equilibrium mobile charge density. The technique facilitates the solution of the self-consistent voltage equation in a carbon nanotube such that the CNT drain-source current evaluation is accelerated by more than three orders of magnitude while maintaining high modelling accuracy. The model is currently limited to ballistic transport but can be extended to non-ballistic modes of transport when a suitable theory is developed while researchers study phenomena that sometimes prevent electrons in a carbon nanotube from going ballistic. Our results show that while the accuracy and speed of the proposed model vary with the number of piece-wise segments in the mobile charge approximation, it is possible to obtain a speed-up of more than 1000 times while maintaining the accuracy within less than 2% in terms of average RMS error compared with the state of the art theoretical reference CNT model implemented in FETToy. This numerical efficiency makes our model particularly suitable for implementation in circuit-level, eg. SPICE-like, simulators where large numbers of such devices may be used to build complex circuits.

I. INTRODUCTION

Following the recent advancements in the understanding of Carbon Nanotube Transistor (CNT) operation [1], [2], several models have been developed [3], [4], [5], [6], [7], [8] for implementation in SPICE-like simulators to enable simulations of future analog and digital systems built with CNT devices. The standard modelling technique is to derive the total CNT drain-source current from some approximation of the non-equilibrium mobile charge injected into the nanotube when a voltage is applied between the drain and the source [1]. The main stumbling block in the development of a circuit-level model is the fact that accurate calculation of the mobile charge involves numerical integration of the densities of states over the number of allowed energy levels using the Fermi probability distribution. In addition, as the total drain current is affected not only by the non-equilibrium mobile charge in the nanotube but also by the charges present at terminal capacitances, the solution of an implicit non-linear algebraic equation is necessary using some iterative approach, such as the Newton-Raphson method, to calculate the drain current. For example, the MATLAB script named FETToy [9] available on line as a reference implementation of the state-of-the art

ballistic CNT theory, requires more than 12 seconds of the CPU time on a Pentium IV PC to calculate a family of current drain characteristics for a single transistor [10]. The numerical complexity inherent in the physical model is impractical in simulations of circuits that might involve very large numbers of CNT devices. Very recently techniques have been proposed to eliminate the need for costly Newton-Raphson iterations and the numerical evaluation of the Fermi-Dirac integral while still maintaining a good agreement with the physical theory [8], [10]. They are based on piece-wise approximation of the charge density profiles, either linear [8] or non-linear [10] to simplify calculations. In this paper we generalise the piece-wise non-linear approach we proposed earlier [10] and develop a more accurate technique where linear, quadratic and third-order polynomials are used to maintain an accuracy better than 2% in terms of average RMS error. This compares favourably with the 5% error that was reported for the simple linear approximation [8], and we still achieve a speed up of more than three orders of magnitude compared with FETToy. The salient feature of the presented approximation technique is the high modelling accuracy while enabling a fast numerical solution of the self-consistent voltage equation using a simple algorithm, without the need for CPU intensive Newton-Raphson iterations and evaluation of state densities from Fermi-Dirac probability distribution integrals.

II. MOBILE CHARGE DENSITY AND SELF-CONSISTENT VOLTAGE

When an electric field is applied between the drain and the source of a CNT, a non-equilibrium mobile charge is induced in the nanotube [1], [11], [12]:

$$\Delta Q = q(N_S + N_D - N_0) \quad (1)$$

where N_S is the density of positive velocity states filled by the source, N_D is the density of negative velocity states filled by the drain and N_0 is the equilibrium electron density. These densities are determined by the Fermi-Dirac probability distribution as follows:

$$N_S = \frac{1}{2} \int_{-\infty}^{+\infty} D(E) f(E - U_{SF}) dE \quad (2)$$

$$N_D = \frac{1}{2} \int_{-\infty}^{+\infty} D(E) f(E - U_{DF}) dE \quad (3)$$

$$N_0 = \int_{-\infty}^{+\infty} D(E) f(E - E_F) dE \quad (4)$$

where U_{SF} and U_{DF} are defined as

$$U_{SF} = E_F - qV_{SC} \quad (5)$$

$$U_{DF} = E_F - qV_{SC} - qV_{DS} \quad (6)$$

V_{SC} is the self-consistent voltage, a recently introduced concept [1] which illustrates that the CNT energy band is affected by external terminal voltages, $D(E)$ is the density of states, E_F is the Fermi level, f is the Fermi probability distribution, q is the electronic charge and E represents the energy levels per nanotube unit length. The self-consistent voltage V_{SC} is implicitly related to the device terminal voltages and charges at terminal capacitances by the following non-linear algebraic equation [1], [8]:

$$V_{SC} = -\frac{Q_t + qN_S(V_{SC}) + qN_D(V_{SC}) + qN_0}{C_\Sigma} \quad (7)$$

where Q_t represents the charge stored in terminal capacitances and is defined as

$$Q_t = V_G C_G + V_D C_D + V_S C_S \quad (8)$$

where C_G, C_D, C_S are the gate, drain, and source capacitances correspondingly and the total terminal capacitance C_Σ is

$$C_\Sigma = C_G + C_D + C_S \quad (9)$$

The standard approach to the solution of equation (7) is to use the Newton-Raphson iterative method and in each iteration evaluate the integrals in equations (3) and (4) to obtain the state densities N_D and N_S .

III. CIRCUIT MODEL

In the earlier work [10] we proposed to apportion equal parts of the equilibrium mobile charge density N_0 to the drain and source. This facilitates circuit implementation of the model because now the corresponding non-equilibrium mobile charge densities Q_S and Q_D can be modelled as non-linear circuit capacitances connected between a conceptual inner node, which represents the self-consistent potential, and CNT terminal nodes:

$$Q_S(V_{SC}) = q(N_S - \frac{1}{2}N_0) \quad (10)$$

and

$$Q_D(V_{SC}) = q(N_D - \frac{1}{2}N_0) \quad (11)$$

The resulting equivalent circuit is shown in figure 1 where Σ is the hypothetical inner node described above, which comprises all the CNT charges.

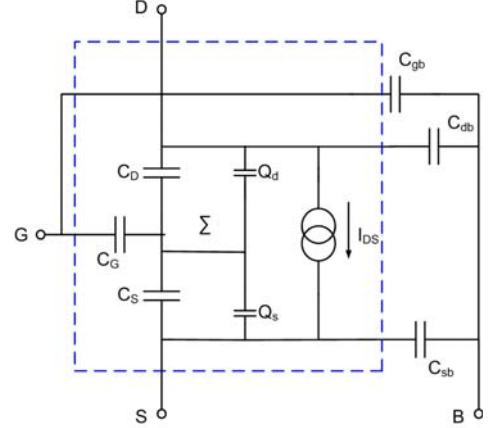


Fig. 1. Equivalent circuit for the proposed CNT model.

According to the ballistic CNT transport theory [1], [9] the drain current caused by the transport of the non-equilibrium charge across the nanotube can be calculated using the Fermi-Dirac statistics as follows:

$$I_{DS} = \frac{2qkT}{\pi\hbar} \left[\mathcal{F}_0\left(\frac{U_{SF}}{kT}\right) - \mathcal{F}_0\left(\frac{U_{DF}}{kT}\right) \right] \quad (12)$$

where \mathcal{F}_0 represents the Fermi-Dirac integral of order 0, k is Boltzmann's constant, T is the temperature and \hbar is reduced Planck's constant.

If the self-consistent voltage V_{SC} is known, the evaluation of the drain current poses no numerical difficulty as energy levels U_{SF}, U_{DF} can be found quickly from equations (5), (6). Then I_{DS} can be calculated directly from equation (12) which uses the closed-form analytical solution of the Fermi-Dirac integral of order 0 [8]:

$$\mathcal{F}_0(\eta) = \log(1 + e^\eta) \quad (13)$$

However, as it has already been highlighted above, the solution of the self-consistent voltage equation (7) is very time consuming because it involves a Newton-Raphson iterative process in which each iteration requires numerical integration to obtain state densities $N_D(V_{SC})$ and $N_S(V_{SC})$. The next section outlines a piece-wise non-linear approximation technique that eliminates the need for these complex calculations while maintaining a high modelling accuracy.

IV. PROPOSED PIECEWISE NON-LINEAR APPROXIMATION OF MOBILE CHARGE DENSITY

We have developed and investigated two accurate piece-wise approximations of the non-equilibrium mobile charge density dependence on the self-consistent voltage V_{SC} at the

source and drain. The boundaries between regions could be determined symbolically from physical parameters [8]. Here we employ a purely numerical, rather than symbolic, approach where the boundaries are calculated to minimise the RMS deviation from the theoretical curves. Polynomial equations for regions were then obtained according to the same rule while assuring the continuity of the first derivative, over the temperature range $150K \leq T \leq 450K$ and Fermi level range $-0.5eV \leq E_F \leq 0V$. The approximations were compared with the theoretical curves calculated from equations (3), (10) and (4), (11) correspondingly. Model 1, illustrated in figure 2 uses three piecewise regions: 1) linear, when $V_{SC} \leq \frac{E_F}{q} - 0.08V$, 2) quadratic, when $\frac{E_F}{q} - 0.08V < V_{SC} < \frac{E_F}{q} + 0.08V$ and 3) zero, when $V_{SC} \geq \frac{E_F}{q} + 0.08V$. The more accurate Model 2, uses four piecewise regions as shown in figure 3: 1) linear, when $V_{SC} \leq \frac{E_F}{q} - 0.28V$, 2) quadratic, when $\frac{E_F}{q} - 0.28V < V_{SC} \leq \frac{E_F}{q} - 0.03V$, 3) 3rd order, when $\frac{E_F}{q} - 0.03V < V_{SC} \leq \frac{E_F}{q} + 0.12V$ and 3) zero, when $V_{SC} > \frac{E_F}{q} + 0.12V$.

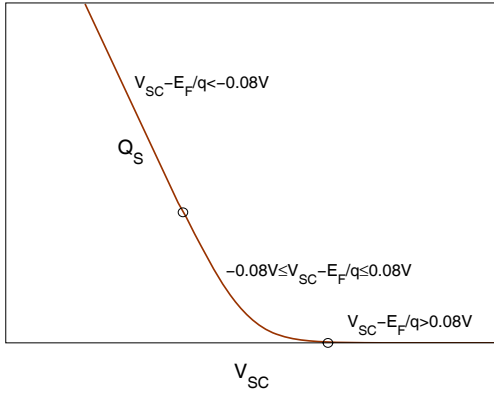


Fig. 2. Model 1: three-piece non-linear approximation of mobile charge.

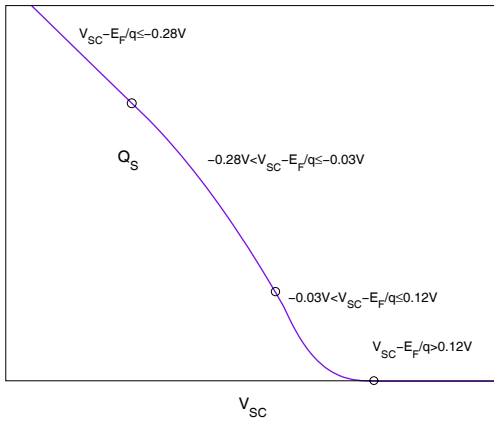


Fig. 3. Model 2: four-piece non-linear approximation of mobile charge.

The piece-wise non-linear approximations for the drain and source charge densities calculated using Model 1 for the

typical temperature and Fermi level values of $T = 300K$ and $E_F = -0.32eV$ correspondingly are illustrated and compared with the theoretical graphs in figure 4. A similar illustration and comparison for Model 2 is shown in figure 5. It is clear, from figures 4 and 5, that Model 2 reflects more accurately the mobile charge densities especially in the ranges representing larger values of the charge. It is possible to use more sections for an even higher accuracy but at some computational expense. We are currently investigating in more detail how the number of sections affects the trade-off between accuracy and speed.

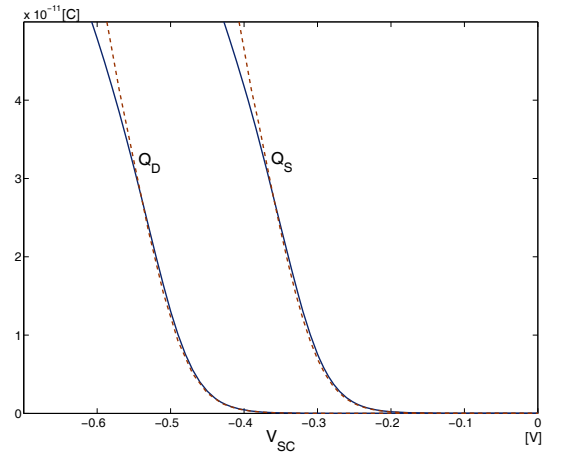


Fig. 4. Mobile charge densities at drain and source for $T = 300K$ and $E_F = -0.32eV$ (solid lines) and their piece-wise approximations using Model 1 (dashed lines).

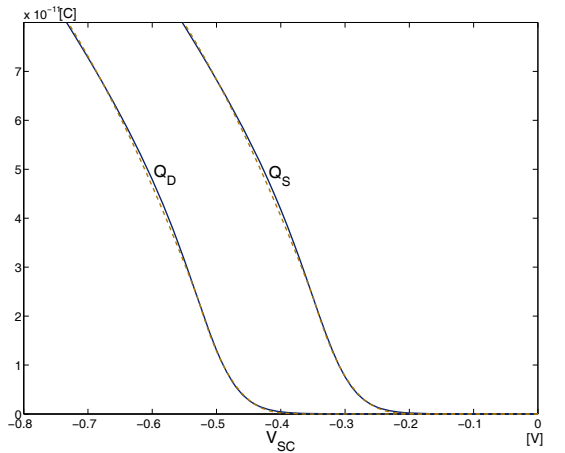


Fig. 5. Mobile charge densities at drain and source for $T = 300K$ and $E_F = -0.32eV$ (solid lines) and their piece-wise approximations using Model 2 (dashed lines).

Both models use polynomials of order not greater than 3 and hence allow a closed-form solution of the self-consistent voltage equation (7) thus completely eliminating the need for Newton-Raphson iterations and numerical evaluation of state density integrals. The optimised non-linear approximation

outlined above ensures an accuracy of about 2% for the four-piece model compared with the theoretical model of CNT ballistic transport [1] and represents an improvement over the piecewise linear symbolic approximation [8]. This purely numerical approach not only gives the model developer control over the approximation accuracy but also leads to a significant saving in the processing time as shown in the next section.

V. DRAIN CURRENT CALCULATION

Once the self-consistent voltage V_{SC} is calculated from the closed-form solutions of equation (7), whose approximation contains only linear, quadratic or 3rd order polynomial terms, the total drain current can be directly obtained from equations (5), (6), (12) and (13):

$$I_{DS} = \frac{2qkT}{\pi\hbar} \left[\log\left(1 + e^{\frac{E_F - qV_{SC}}{kT}}\right) - \log\left(1 + e^{\frac{E_F - q(V_{SC} - V_{DS})}{kT}}\right) \right] \quad (14)$$

These calculations are extremely fast, as Newton-Raphson iterations and integration of the Fermi-Dirac probability distribution are now eliminated. CPU times and I_{DS} prediction accuracy for both models were measured against the theoretical model [1] implemented in FETToy [9]. Table I shows average CPU times for both models and those for FETToy. For accurate measurement, a number of calculations were carried out by invoking all models 5, 10, 50 and 100 times. Results show that both models are more than three orders of magnitude faster than FETToy: Model 1 is about 3400 times faster and Model 2 - more than 1100 times. The extent to which the modelling accuracy was compromised by numerical approximation was also measured by calculating average RMS errors in the simulations. Figures 6 and 7 show the I_{DS} characteristics calculated by FETToy compared with both Model 1 and Model 2 respectively. Table II shows average RMS error for both models. As expected, Model 2 is slightly more accurate with errors not exceeding 2% at $T = 300K$ and $E_F = -0.32eV$ throughout the typical ranges of drain voltages V_{DS} and gate bias V_G .

TABLE I
AVERAGE CPU TIME COMPARISON

Loops	FETToy	Model 1	Model 2
5	64.43Sec	0.02Sec	0.06Sec
10	128.78Sec	0.04Sec	0.12Sec
50	642.44Sec	0.19Sec	0.56Sec
100	1287.45Sec	0.38Sec	1.12Sec

Figures 6 and 7 show the I_{DS} characteristics calculated by FETToy compared with both Model 1 and Model 2 respectively. As shown in Table II, both models maintain a high accuracy in terms of the average RMS error. As expected, Model 2 is slightly more accurate with errors not exceeding 2% at $T = 300K$ and $E_F = -0.32eV$ throughout the typical ranges of drain voltages V_{DS} and gate bias V_G .

In addition, drain current characteristics were calculated and compared for accuracy with the theoretical CNT model in

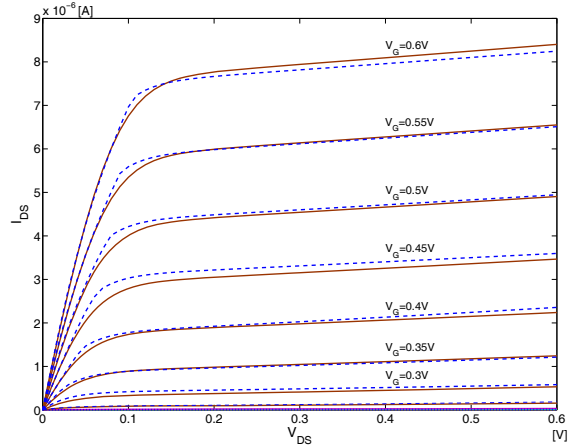


Fig. 6. Drain current characteristics at $T = 300K$ and $E_F = -0.32eV$ for FETToy (solid lines) and piece-wise approximation using Model 1 (dashed lines).

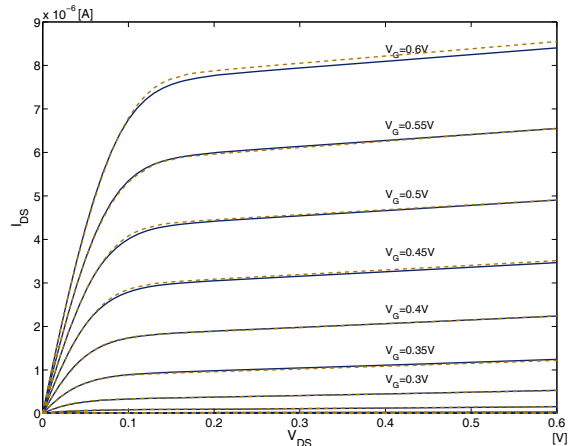


Fig. 7. Drain current characteristics at $T = 300K$ and $E_F = -0.32eV$ for FETToy (solid lines) and piece-wise approximation using Model 2 (dashed lines).

FETToy using different temperature and Fermi level values. Tables II, III and IV show average RMS errors for both models at temperatures $T = 150K, 300K, 450K$ and Fermi levels $E_F = -0.5eV, -0.32eV, -0.5eV$. Figures 8 and 9 illustrate the drain current characteristics of Model 2 at different temperatures and Fermi levels.

VI. COMPARISON WITH EXPERIMENTAL RESULTS

Additionally, to validate the performance of the proposed modelling technique, both Model 1 and Model 2 were compared with recently published experimental results [13] for an n-type carbon nanotube transistor fabricated with K-doping and grounded back gate. The transistor's parameters were: $d=1.6nm$, $t_{ox}=50nm$ and $E_F=-0.05eV$ and measurements were taken at the temperature $T=300K$. Figures 10 and 11 show that the proposed models, as well as the theoretical ballistic model implemented in FETToy, maintain good accuracy compared

TABLE V
AVERAGE RMS ERRORS IN I_{DS} COMPARISON TO THE EXPERIMENTAL RESULTS OF FETTOY MODEL AND THE PROPOSED MODELS FOR $d = 1.6nm$, $t_{ox} = 50nm$, $T=300K$ AND $E_F = -0.05eV$.

V_G [V]	FETTOy	Model 1	Model 2
0.2	8.5%	10.7%	9.9%
0.4	7.8%	9.8%	8.9%
0.6	7.2%	9.3%	8.1%

with the experimental measurements. Table V shows that corresponding average RMS errors do not exceed 10%.

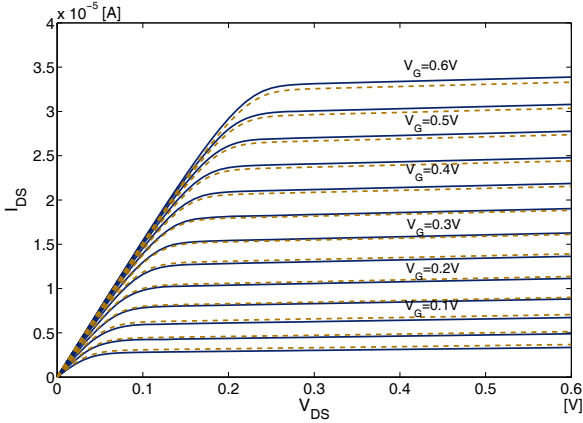


Fig. 8. Drain current characteristics at $T = 150K$ and $E_F = 0eV$ for FETTOy(solid lines) and piece-wise approximation using Model 2 (dashed lines).

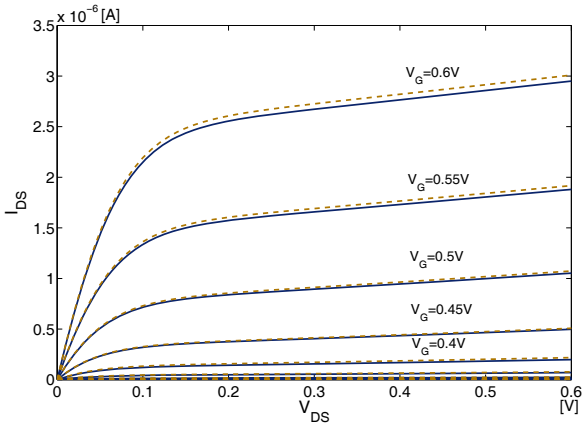


Fig. 9. Drain current characteristics at $T = 450K$ and $E_F = -0.5eV$ for FETTOy(solid lines) and piece-wise approximation using Model 2 (dashed lines).

VII. CONCLUSION

We have proposed a new, fast numerical approach to CNT modelling suitable for a direct implementation in SPICE-like circuit simulators. Results provide further evidence to support recent suggestions [8], [10] that numerical integration and Newton-Raphson iterations can be avoided, leading to a substantial acceleration in the model evaluation. Two specific

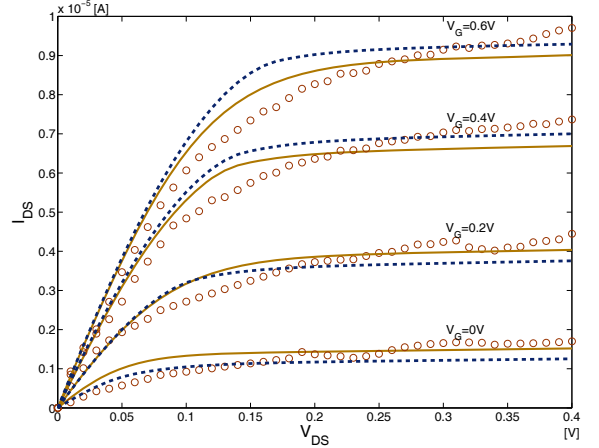


Fig. 10. Comparison with experimental results (circlet lines) of the FETTOy model(solid lines) and the proposed model 1(dashed lines) for $d = 1.6nm$, $t_{ox} = 50nm$, $T=300K$ and $E_F = -0.05eV$.

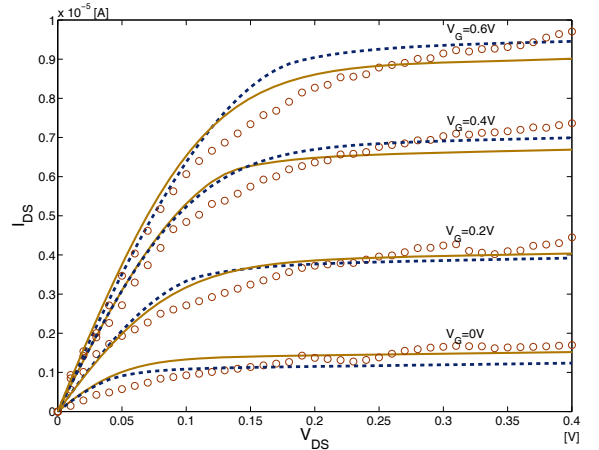


Fig. 11. Comparison with experimental results (circlet lines) of the FETTOy model(solid lines) and the proposed model 2(dashed lines) for $d = 1.6nm$, $t_{ox} = 50nm$, $T=300K$ and $E_F = -0.05eV$.

piece-wise non-linear approximations to the non-equilibrium mobile charge densities have been developed to enable a closed-form solution of the self-consistent voltage equation and thus eliminate the need for costly iterations. The parameters of the piece-wise ranges assure the continuity of the first derivative and were optimised for fitting accuracy. When compared with FETTOy [9], a reference theoretical CNT model, we have demonstrated that the proposed approximation approach leads to a computational cost saving of more than three orders of magnitudes while maintaining the modeling accuracy comparable with that of the state-of-the art ballistic transport theory. This research contributes towards the recent efforts in CNT model development for nano-electronic circuit design. These efforts aim to enable practical and efficient simulations of future analogue and digital circuits built using large numbers of CNT devices. Future work will involve extension

TABLE II
AVERAGE RMS ERRORS IN I_{DS} APPROXIMATION OF MODEL 1 AND MODEL 2 FOR $E_F = -0.32eV$.

V_G [V]	150K		300K		450K	
	Model 1	Model 2	Model 1	Model 2	Model 1	Model 2
0.1	4.0%	1.7%	4.4%	2.0%	4.6%	2.3%
0.2	3.2%	1.3%	3.6%	1.5%	3.7%	1.9%
0.3	2.5%	1.0%	2.7%	1.0%	2.9%	1.4%
0.4	1.8%	0.7%	1.9%	0.6%	2.3%	1.0%
0.5	1.5%	0.4%	1.6%	0.5%	2.6%	1.1%
0.6	1.9%	0.8%	2.2%	0.9%	2.7%	1.3%

TABLE III
AVERAGE RMS ERRORS IN I_{DS} APPROXIMATION OF MODEL 1 AND MODEL 2 FOR $E_F = -0.5eV$.

V_G [V]	150K		300K		450K	
	Model 1	Model 2	Model 1	Model 2	Model 1	Model 2
0.1	4.4%	2.1%	4.7%	2.3%	4.8%	2.8%
0.2	4.0%	1.5%	4.2%	1.8%	4.3%	2.4%
0.3	3.1%	1.2%	3.3%	1.3%	3.5%	1.7%
0.4	2.2%	1.0%	2.9%	0.8%	2.7%	1.4%
0.5	1.8%	0.7%	2.6%	0.8%	3.5%	1.2%
0.6	2.4%	0.9%	3.3%	1.3%	4.1%	1.6%

TABLE IV
AVERAGE RMS ERRORS IN I_{DS} APPROXIMATION OF MODEL 1 AND MODEL 2 FOR $E_F = 0eV$.

V_G [V]	150K		300K		450K	
	Model 1	Model 2	Model 1	Model 2	Model 1	Model 2
0.1	2.9%	1.6%	3.4%	1.9%	4.0%	2.1%
0.2	2.1%	1.3%	2.6%	1.4%	3.2%	1.7%
0.3	1.6%	1.0%	2.0%	1.1%	2.6%	1.4%
0.4	1.3%	0.6%	1.8%	0.7%	2.3%	1.0%
0.5	1.2%	0.4%	1.3%	0.5%	2.1%	0.8%
0.6	1.5%	0.7%	2.0%	0.7%	3.1%	1.0%

of the model to include non-ballistic transport effects as well as further testing for speed and modelling accuracy in practical logic circuit structures based on CNT devices. To facilitate future CNT circuit simulations we have developed a VHDL-AMS implementation of Model 2 and made it available for public use through the Southampton VHDL-AMS Validation Suite website [14].

VIII. ACKNOWLEDGEMENT

The authors would like to acknowledge the support of EPSRC/UK for funding this project in part under grant EP/E035965/1.

REFERENCES

- [1] Anisur Rahman, Jing Guo, Supriyo Datta, and Mark S. Lundstrom. Theory of ballistic nanotransistors. *Electron Devices, IEEE*, 50(9):1853–1864, September 2003.
- [2] Phaedon Avouris, Joerg Appenzeller, Richard Martel, and Shalom J. Wind. Carbon nanotube electronics. *Proceedings of the IEEE*, 91(11):1772–84, November 2003.
- [3] Arash Hazeghi, Tejas Krishnamohan, and H.-S. Philip Wong. Schottky-Barrier Carbon Nanotube Field-Effect Transistor Modeling. In *Sixth IEEE Conference on Electron Devices*, volume 54, Lausanne, Switzerland, March 2007.
- [4] Thao Dang, Lorena Anghel, and Regis Leveugle. CNTFET basics and simulation. In *IEEE Int. conf. on Design and Test of Integrated Systems in Nanoscale Technology (DTIS)*, Tunis, Tunisia, 5-7 September 2006.
- [5] Chris Dwyer, Moky Cheung, and Daniel J. Sorin. Semi-empirical SPICE models for carbon nanotube FET logic. In *4th IEEE Conference on Nanotechnology*, Munich, Germany, 16-19 Aug. 2004.
- [6] Arijit Raychowdhury, Saibal Mukhopadhyay, and Kaushik Roy. A circuit-compatible model of ballistic carbon nanotube field-effect transistors. *Applied Physics Letters*, 23(10):1411–20, October 2004.
- [7] Bipul C. Paul, Shinobu Fujita, Masaki Okajima, and Thomas Lee. Modeling and analysis of circuit performance of ballistic CNFET. In *2006 Design Automation Conference*, San Francisco, CA, USA, 24-28 July 2006.
- [8] Hamidreza Hashempour and Fabrizio Lombardi. An efficient and symbolic model for charge densities in ballistic carbon nanotube FETs. *IEEE-NANO*, 1:17–20, 2006.
- [9] Anisur Rahman, Jing Wang, Jing Guo, Sayed Hasan, Yang Liu, Akira Matsudaira, Shaikh S. Ahmed, Supriyo Datta, and Mark Lundstrom. Fettoy 2.0 - on line tool, 14 February 2006. <https://www.nanohub.org/resources/220/>.
- [10] Tom J. Kazmierski, Dafeng Zhou, and Bashir M. Al-Hashimi. A fast, numerical circuit-level model of carbon nanotube transistor. In *IEEE Int. Workshop on Design and Test of Defect-Tolerant Nanoscale Architectures (Nanoarch)*, Santa Clara, CA, 21-22 October 2007.
- [11] Ming-Hsun Yang, Kenneth B. K. Teo, Laurent Gangloff, William I. Milne, David G. Hasko, Yves Robert, and Pierre Legagneux. Advantages of top-gate, high-k dielectric carbon nanotube field-effect transistors. *Applied Physics Letters*, 88(11):113507–1–3, March 2006.
- [12] Paul L. McEuen, Michael S. Fuhrer, and Hongkun Park. Single-walled carbon nanotube electronics. *Nanotechnology, IEEE Transactions*, 1(1):78–845, March 2002.
- [13] Ali Javey, Ryan Tu, Damon Farmer, Jing Guo, Roy Gordon, and Hongjie Dai. High performance n-type carbon nanotube field-effect transistors with chemically doped contacts. *Nano Letters*, 5:345–348, 2005.
- [14] Southampton VHDL-AMS Validation Suite, 2007. <https://www.syssim.ecs.soton.ac.uk/index.htm>.