

Research Article

The Discrete-Time Sliding Mode Control with Computation Time Delay for Repeatable Run-Out Compensation of Hard Disk Drives

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The presented is a study on the problem of disturbance rejection, specifically the periodic disturbance, by applying discretetime sliding mode control method. For perturbations such as modeling errors and external disturbances, their compensation is formulated using the designed sliding mode control. To eliminate the effect of these perturbations, the convergence rate between the disturbance and their compensation has been shaped by an additional parameter. Decoupling of the resultant perturbation estimation dynamics from the closed loop dynamics is achieved. Computation time delay is also presented to address the perturbation effects. The approach developed ensures the robustness of the sliding mode dynamics to parameter uncertainties and exogenous disturbances, in addition to the complete rejection of the periodic disturbance component. Satisfactory simulation results as well as experimental ones have been achieved based on a fast servo system of a modern hard disk drive to illustrate the validity of the controller for repeatable run-out (RRO) compensation.

1. Introduction

One of the most attractive features of the variable structure system (VSS) with sliding mode is its invariance and robustness to perturbations including modeling errors and external disturbances. Originated in the late 1950s, sliding mode theory has been developed mostly in the continuous time domain especially after the earliest works published by Itkis [1] and Utkin [2]. Thereafter, researches on this topic had increased rapidly, for example, Hui and Żak studied the discrete-time variable structure sliding mode control [3]; Kalsi et al. presented a high gain approach [4]. Employing describing function techniques, sliding mode control of DC servo mechanisms is analyzed in the presence of unmodeled stator and sensor dynamics by Xu et al. [5]. A sliding-modebased learning controller for track-following in hard disk drives was presented by Wu and Liu [6], and the computation time delay is treated as a fault to be detected, using an appropriate controller to minimize its effects by Garcia et al. [7].

Sliding mode requires high-speed discontinuous action to steer the states of a system into a sliding surface and to maintain the subsequent motion on that surface. Nowadays, digital computers have been widely used in all kinds of control systems. However, limited sampling frequency results in control inputs to be constant between two sampling intervals, leading to difficulties for instantaneous actions to be implemented in the sampled-data system. This means that when system dynamics cross the sliding surface between sampling intervals, the control input cannot immediately take measures to enable the system to remain on that sliding surface, leading to VSS controllers implemented in discrete-time system not to possess those desirable properties due to finite sampling time [8]. Therefore, implementation of continuous VSS controllers on digital computers presents difficulties due to limited sampling rate, sample/hold effects, and discretization errors which can possibly lead to unacceptable results [9].

In order to achieve the robustness required, much research on discrete-time variable structure control (DVSC) and discrete-time sliding mode control (DSMC) has been presented recently. Sarpturk et al. [10] and Kotta [11] showed the necessary but not sufficient conditions for achieving a sliding motion of discrete variable structure systems, and proposed a new and more strict condition required to be met. Hung et al. [12] proposed a reaching law for discretetime variable structure control. Gao et al. [13] formulated the concept of discrete sliding mode by making use of the reaching law proposed in [12] to ensure reaching conditions that are satisfied for stability and convergence of s(k). Bartolini et al. [14] incorporated adaptive control strategy into the modeling of system uncertainties and designed a control law in terms of a discrete-time equivalent control. Tesfaye and Tomizuka [15] proposed the concept of time-delay control (TDC) and a robust discrete-time sliding mode control using the delta (δ) operator. The behavior of the discrete-time sliding mode controller for linear, nonlinear, and stochastic systems was investigated by Su et al. [16] who conducted a detailed analysis of sampling and hold effects. Koshkouei and Zinober [17] introduced the notation of the sliding lattice and clarified the concept of discrete-time sliding mode control (DSMC). Corradini and Orlando [18] employed the concept of TDC to eliminate the effects of system perturbation within switching region. With proper sampling period T_s , Su et al. [19] verified that the thickness of the boundary layer can be reduced to $O(T_s^2)$ for continuous smooth disturbance. Eun et al. [20] proposed a decoupled disturbance compensator (DDC) by directly using the variable structure framework and combined it with DVSC. In his DVSC with DDC structure, however, system response may become slow, and controller implementation can become rather complex. To enable tracking error vector to remain close to zero without using any additional disturbance estimation scheme, Kim and Cho [21] proposed a DVSC method with a recursive switching function (R-DVSC). After that, the R-DSVC with an additional parameter to tune the transient response was proposed by Furuta [9]. Furthermore, a sliding surface that allows the transient response to be shaped by introducing information of past states in the sliding surface was proposed by S. M. Lee and B. H. Lee [22].

Beside the limited sampling rate, there is an inherent computation time delay because of the existence of measurement delays of feedback signals as well as the execution time of instructions when a control algorithm is implemented on a digital computer. S. M. Lee and B. H. Lee [22] summarized some of the previous works. Kondo and Furuta [23] examined the computation time delay for an optimal full state-feedback regulator problem. Ha and Ly [24] formulated the computation time delay in *W*-domain. Misawa [25] showed that the presence of computation time delay not only reduces relative stability and robustness but also degrades performance and proposed that a controller that embraces the concept of TDC proposed by Tesfaye and Tomizuka [15] to compensate the

effects of perturbations should be used. For the repetitive control of discrete-time systems, one of the earlier classical works was presented by Tomizuka et al. [26]. Recently, the discrete-time sliding-mode congestion control with time-varying delay for multisource communication networks was presented by Ignaciuk and Bartoszewicz [27]. Sliding mode control for time-varying delayed systems based on a reduced-order observer is considered by Yan et al. [28]. Due to its importance and wide use, a lot of scientists focused on high performance controller for real systems, for example, [29–33]. Here, the high performance servo system for HDDs is investigated via discrete-time sliding mode approach.

Since discrete-time sliding mode control has been developed for guaranteeing the asymptotic stability of uncertain sampled-data systems, as well as to reducing the chattering phenomenon that arises when the controller was implemented on a digital computer, the computation time delay should also be considered in the discrete-time sliding mode structure for improving the system performance. This paper presents a discrete-time sliding mode controller with an additional parameter to adjust the convergence rate for improving the perturbation compensation, and then based on the spirit of [22], the effects of computation time delay are considered for complete disturbances compensation. Experiments have been implemented based on a modern hard disk drive (HDD). It is assumed that the computation time delay is constant and smaller than the sampling time. For the clarity of presentation, a controller with an additional adjustable parameter that does not take computation time delay into account is firstly presented. Then the controller that considers computation time delay is developed. By introducing an additional parameter, the difference between perturbation and its compensation convergences to zero asymptotically. The controller design law makes use of the concept of TDC to compensate the effects of perturbations. In the presence of the influences of perturbations by unknown external disturbances and parametric uncertainties, the developed controller generates a compensation signal to cancel these influences through the mechanism of time delay. The DSMC with decoupled estimator presented in this paper are intrinsic robust to parameter uncertainties and exogenous disturbances and especially effective with the periodical disturbances. For the real servo systems, for example, the hard disk drives have strong repeatable runouts (RROs) due to the eccentric characteristics of the rotating disk and shaft; the high-speed and high-precision multiaxis stages in wire bonder or the ICs (integrated circuits) exposure lithography equipments have the strong periodic disturbances during stepping under the operating modes. The periods of the disturbances are different when the operation modes are changed, and these could be estimated within control algorithm automatically. The positioning accuracy of the control systems could be improved significantly when taking the DSMC with the decoupled estimator when considering computational time delay.

The remains of the paper are organized as follows. Discrete-time models with and without computation time delay are briefly outlined in Section 2. In Section 3, a discrete-time-sliding mode controller with shaped perturbation rejection that does not consider the computation time delay is presented. Afterwards, the controller that accounts for the effects of computation time delay is developed. In Section 4, numerical simulations to verify the proposed control methods are conducted based on the servo-control model of a commercial HDD. In Section 5, experimental digital servo system for track-to-track seeking of HDD is implemented, from which the experimental measured results are provided to show the validity of the proposed method. Finally, conclusions are drawn in Section 6.

2. Preliminaries

Consider the following SISO LTI system with parametric uncertainties and exogenous disturbances described by the continuous-time model:

$$\dot{x}(t) = (F + \Delta F) x(t) + Gu(t) + E\xi(t),$$
 (1)

$$y(t) = Hx(t), \qquad (2)$$

where *x* is the *n*-dimensional state vector, $u \in R$ is the system input, $y \in R$ is the measured output, and $\xi \in R$ is the perturbation, which represents the effects of external disturbances and parametric uncertainties. *F*, ΔF , *G*, *E*, and *H* are properly dimensioned constant matrices. The continuoustime matching condition is assumed to hold; that is, there exists a $1 \times n$ row scalar vector Δf and a scalar Δ_{ξ} such that $\Delta F = G\Delta f$ and $E = G\Delta_{\xi}$.

2.1. Discrete-Time Model without Computation Time Delay. The discrete-time model with sampling period T_s is

$$x(k+1) = A_d x(k) + b_d u(k) + D\xi(k), \qquad (3)$$

denoting with the index k the variable evaluated in $t = kT_s$. In the above system, the pair (A_d, b_d) is assumed to be controllable, and as is shown by Tesfaye and Tomizuka [15], the matching condition can be enforced in discrete system model, if a suitable sampling time is chosen by means of the assumption below.

Assumption 1 (see [15]). Let the sampling interval T_s be small enough so that the first two terms in the Taylor expansion of a function give an acceptably close approximation.

Hence, the following discrete-time model can be obtained [18]:

$$x(k+1) = Ax(k) + bu(k) + bd(k), \qquad (4)$$

where *A*, *b*, *g*, and d(k) are obtained as follows:

$$A = e^{FT_s}, \qquad B = \int_0^{T_s} e^{F\tau} d\tau G,$$

$$\Delta A = \Delta f \left[I + (F + G\Delta f) \frac{T_s}{2} \right],$$

$$d(k) = \frac{\Delta Ax(k)}{g} + \Delta_{\xi} \xi(k),$$

$$g = 1 + \Delta f G \frac{T_s}{2}, \qquad b = Bg.$$
(5)

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In (4), the disturbance is constructed as $d(k) = \Delta Ax(k)/g + \Delta_{\xi}\xi(k)$ including the parametric uncertainty Δf and external disturbance vector $D\xi(k)$.

Assumption 2. Both the matching condition and the (A, b) pair are completely controllable and are assumed to hold through the paper.

So, the switching function is defined as follows:

$$s(k) = c^T x(k) = 0.$$
 (6)

2.2. Discrete-Time Model with Computation Time Delay. Assume there exists a delay δ , which is caused mainly by the execution time of the instructions that generate the control input after the sampling instant when a control algorithm was implemented on a digital computer. Following S. M. Lee and B. H. Lee [22], it is assumed here that the delay is constant and smaller than one sampling interval T_s ($0 < \delta < T_s$), and this condition is assumed to hold through the paper. Therefore, the control input u(t) should be chosen as follows:

$$u(t) = u(k), \quad \text{for } kT_s + \delta \le t < (k+1)T_s + \delta. \tag{7}$$

The discrete-time model of (1) with control input (12) is given as follows [22, 23, 34]

$$x (k + 1) = Ax (k) + b_1 u (k - 1) + b_2 u (k) + D\xi (k)$$

= $Ax (k) + b_1 u (k - 1) + b_2 u (k) + bd (k)$, (8)

where $A = e^{FT_s}$, $b_1 = \int_{T_s-\delta}^{T_s} e^{F\tau} d\tau G = b - b_{T_s-\delta}$, $b_2 = b_{T_s-\delta} = \int_0^{T_s-\delta} e^{F\tau} d\tau G$, and $b = \int_0^{T_s} e^{F\tau} d\tau G$. In spite of the existence of computation time delay, the controllability and observability assumptions are preserved in this model. Although the discrete-time matching condition is not satisfied in system (8), the effects of perturbation can be sufficiently suppressed under the assumption that the perturbation is relatively slower than the sampling frequency $1/T_s$.

3. Discrete-Time Sliding Mode Control Design

The concept of TDC [15] consists of estimating the uncertain dynamics of the system through past observations of the system response, so that a control function can be generated (with some delay) to approximately counterbalance their effects. In order to cancel the effects of perturbation inside the switching region, Corradini and Orlando [18] proposed a discrete-time VSC, in which the perturbation estimator was formulated by using the concept of TDC. Furthermore, a sliding surface was selected to allow the transient time response to be shaped by introducing the past states in the sliding surface. In discrete-time systems, instead of having a hyperplane as in the case of continuous time, a countable set of points is defined a comprising the so-called lattice, and the surface on which these sliding points lie is named the latticewise hyperplane [17, 35]. Here, using the concept of discretetime sliding mode illustrated by the sliding lattice, the DSMC with a decoupled perturbation compensator (DPC) that does not consider computation time delay is firstly presented. Then

regarding the inherent computation time delay, an improved DSMC with DPC is presented.

3.1. Discrete-Time Sliding Mode Control Design without Computation Time Delay. Considering the discrete-time linear time-invariant system given by (10), the discrete-time equivalent control for the nominal plant without perturbation can be obtained by solving the equation s(k + 1) = 0 as

$$u_{\rm eq}(k) = -(c^T b)^{-1} c^T A x(k).$$
(9)

The control objective of the discrete-time sliding mode control is to achieve s(k + 1) = s(k) = 0. When s(k) = 0, that is, when the state vector remains on the sliding surface, the closed-loop dynamics becomes

$$x(k+1) = \left[I - b(c^{T}b)^{-1}c^{T}\right]Ax(k).$$
(10)

The state vector x(k) remains on the sliding surface defined in (11) and converges to the origin in the state space, if $[I - b(c^T b)^{-1} c^T]$ is a contractive matrix.

Based on the spirit of the reaching law proposed by Gao et al. [13], the transient response can be shaped by introducing an additional adjustable parameter to the desired sliding function dynamics such that $s(k + 1) = \beta s(k)$. By solving this equation, the control input that can shape the transient response is obtained as follows:

$$u_{\beta}(k) = -(c^{T}b)^{-1}c^{T}Ax(k) + (c^{T}b)^{-1}\beta s(k), \quad 0 \le \beta < 1.$$
(11)

This control law for the nominal plant can be modified for the system with the unknown perturbations. If v(k) denotes the perturbation compensation input, the following modified control input is obtained:

$$u(k) = u_{\beta}(k) + v(k).$$
 (12)

Then, substituting (11) into the expression of s(k + 1) defined by (6), the following equation can be obtained:

$$s(k+1) = (c^{T}b)[v(k) + d(k)] + \beta s(k).$$
(13)

From (13), we have

$$v(k) = -d(k) + (c^{T}b)^{-1} [s(k+1) - \beta s(k)].$$
(14)

The additive input term v(k) ensures the stability and robustness of the closed-loop system under the presence of perturbations [18], [36]. However, the control cannot be actually implemented because of a lack of knowledge about d(k). If $\xi(t)$ is bounded and considerably slower than the sampling frequency $1/T_s$, the difference between d(k) and d(k - 1) is of $O(T_s)$ [36]. If the disturbance is smooth function, the difference between d(k) and d(k - 1) is of $O(T_s^2)$ [16], so d(k - 1) can be used as an estimate of d(k). From (14), a one-step backward shifted perturbation becomes

$$d(k-1) = -v(k-1) + (c^{T}b)^{-1} [s(k) - \beta s(k-1)].$$
(15)

The compensation input v(k) is defined as an estimate of -d(k): $v(k) = -\hat{d}(k) = -d(k-1)$. So from (15), the perturbation compensation law can be obtained as

$$v(k) = v(k-1) - (c^{T}b)^{-1} [s(k) - \beta s(k-1)].$$
(16)

Obviously, (16) is the separate disturbance estimator, and the convergence rate of |d(k) - v(k)| is only related to $(c^Tb)^{-1}[s(k) - \beta s(k-1)]$. If the perturbation is constant or slowly varying, an adjustable parameter α (0 < $\alpha \le 1$) is added to the item that contains the switching function in (16) to make |d(k) - v(k)| converges asymptotically to zero in a desired sliding function dynamics, leading to the following equations to be obtained:

$$d(k-1) = -v(k-1) + \alpha (c^{T}b)^{-1} [s(k) - \beta s(k-1)],$$
(17)

$$v(k) = v(k-1) - \alpha (c^T b)^{-1} [s(k) - \beta s(k-1)].$$
(18)

Equation (18) is the separate disturbance estimator to be used to compensate system perturbations. The transient response has been shaped by introducing the past state in sliding surface. It can be easily shown that this control law forces the system to be reached at the discrete-time sliding mode and the perturbation can be compensated in a finite time instant, if |d(k) - d(k - 1)| is a decreasing sequence for k > 1 [37, 38].

Theorem 1. For the system described in (4), if one chooses the control law (11) and (12) and the disturbance compensation law (18), with $\varepsilon(k) = d(k) + v(k)$ being the compensation error, the following closed-loop sliding mode dynamics and compensation error dynamics are satisfied:

$$s(k+1) = (c^T b) \varepsilon(k) + \beta s(k), \qquad (19)$$

$$\varepsilon (k+1) = (1-\alpha) \varepsilon (k) + d (k+1) - d (k).$$
 (20)

Proof. From (13), (14) can be easily proven.

By substituting (16) and (19) into the equation $\varepsilon(k) = d(k) + v(k)$, (20) can be derived as follows:

$$\varepsilon (k+1) = d (k+1) + v (k+1)$$

= $d (k+1) + v (k) - \alpha (c^T b)^{-1} [s (k+1) - \beta s (k)]$
= $d (k+1) - d (k) + (1 - \alpha) \varepsilon (k)$.
(21)

Theorem 1 implies that the disturbance estimation dynamics $\varepsilon(k)$ and the sliding mode dynamics s(k) are decoupled. It is similar to the separation principle, allowing both dynamics modes to be tuned separately. The developments for the robustness of the proposed method are presented in the following theorems.

Theorem 2. If $|d(k) - d(k-1)| < \xi$ for (20) holds for with $0 < \alpha \le 1$, and for some constant $\xi > 0$, then for all k > k', there exists some integer k' such that

$$|\varepsilon(k+1)| < \frac{\xi}{\alpha}.$$
 (22)

Proof. It follows from (20) that

$$\begin{aligned} |\varepsilon(k+1)| &= |(1-\alpha)\varepsilon(k) + d(k+1) - d(k)| \\ &\leq (1-\alpha)|\varepsilon(k)| + |d(k+1) - d(k)|. \end{aligned}$$
(23)

Since $|d(k) - d(k-1)| < \xi$, for all *k*, the compensation error satisfies

$$|\varepsilon (k+1)| < (1-\alpha) |\varepsilon (k)| + \xi.$$
(24)

Iterating the above inequality gives rise to

$$|\varepsilon (k+1)|$$

$$< (1-\alpha) |\varepsilon (k)| + \xi$$

$$\leq (1-\alpha)^{2} |\varepsilon (k-1)| + (1-\alpha) \xi + \xi$$

$$\vdots$$
(25)

 $\leq (1-\alpha)^r \left| \varepsilon \left(k-r+1 \right) \right| + \sum_{i=1}^r (1-\alpha)^{r-i} \xi.$

This implies

$$|\varepsilon (k+1)| \le (1-\alpha)^r \max \{|\varepsilon (1)|, |\varepsilon (2)|, \dots, |\varepsilon (r)|\} + \frac{\left[1-(1-\alpha)^{r-i}\right] \xi}{\alpha}$$

and as $k \longrightarrow \infty$, $\lim_{k \to \infty} \sup |\varepsilon (k+1)| \le \frac{\xi}{\alpha}$.
(26)

It is shown in Theorem 2 that the compensation error in (20) converges asymptotically to zero if the disturbance is constant or of slowly varying nature.

Theorem 3. If $|d(k)-d(k-1)| < \xi$ with $0 < \alpha \le 1$ and $0 \le \beta < 1$ for (19) and (20), for all k and $\xi > 0$, then there exists some integer k_0 such that for all $k > k_0$ we have

$$|s(k+1)| \le \frac{\left| \left(c^T b \right) \right| \xi}{\left[\alpha \left(1 - \beta \right) \right]}.$$
(27)

Proof. It follows from (20) that

$$|s(k+1)|$$

$$= |(c^{T}b) \varepsilon(k) + \beta s(k)|$$

$$\leq |(c^{T}b)| \varepsilon(k)| + \beta |s(k)|$$

$$\leq |(c^{T}b)| \frac{\xi}{\alpha} + \beta |s(k)|$$

$$\leq |(c^{T}b)| \frac{\xi}{\alpha} + \beta^{2} |s(k-1)| + \frac{\beta |(c^{T}b)| \xi}{\alpha}$$

$$\vdots$$

$$\leq \beta^{k-k_{0}} |s(k_{0})| + \frac{\sum_{i=1}^{k-k_{0}} \beta^{k-k_{0}-i} |(c^{T}b)| \xi}{\alpha}$$

$$= \beta^{k-k_{0}} |s(k_{0})| + \frac{(1-\beta)^{k-k_{0}} |(c^{T}b)| \xi}{[\alpha(1-\beta)]}$$
and as $k \longrightarrow \infty$,
$$\lim_{k \to \infty} \sup |s(k+1)| \leq \frac{|(c^{T}b)| \xi}{[(1-\beta)\alpha]}$$
(28)

The robustness of the closed-loop sliding mode dynamics to the disturbance is guaranteed from Theorem 3 and the asymptotic bound on the switching function is proportional to the bound on |d(k)-d(k-1)|. By Theorem 3, the switching function is ensured to converge to a bound proportional to the reciprocal of adjustable parameters α and $1 - \beta$.

Similarly, the theorems can be proposed and proved for the periodic perturbation compensation. To the periodic disturbance with known period N, and if N is assumed slower than the sampling frequency $1/T_s$, then the compensation input v(k) is defined as an estimate of -d(k - N). From (18), the compensation for periodic disturbance is obtained as

$$v(k) = v(k - N) - \alpha (c^{T}b)^{-1} [s(k - N + 1) - \beta s(k - N)].$$
(29)

It can be easily shown that this control law forces the system to be reached at the discrete-time sliding mode and the periodic disturbance can be compensated in a finite time instant, if |d(k) - d(k - N)| is a decreasing sequence for k > 1 [37, 39].

3.2. Discrete-Time Sliding Mode Control Design with Computation Time Delay. The discrete-time sliding mode controller considering the effects of computation time delay is considered here. Similar to the control law mentioned in the previous section, the new control law utilizes the concept of TDC to formulate the perturbation estimation process [36]. Considering the discrete-time model with computation time delay described by (13), the following equivalent control can be obtained for the nominal plant without perturbation:

$$u_{\rm eq}(k) = -(c^T b_2)^{-1} c^T A x(k) - (c^T b_2)^{-1} (c^T b_1) u(k-1).$$
(30)

For the desired sliding function dynamics $s(k+1) = \beta s(k)$, the control input that can shape the transient response is obtained as

$$u_{\beta}(k) = u_{\rm eq}(k) + (c^{T}b_{2})^{-1}\beta s(k), \quad (0 \le \beta < 1).$$
(31)

For the compensation of unknown perturbation, the perturbation compensation input v(k) is added to the above control input, leading to a control law as

$$u(k) = u_{\beta}(k) + v(k).$$
 (32)

Note that the control input u(k - 1) is included in the equivalent control input $u_{eq}(k)$. Since u(k - 1) contains the time delay control input v(k - 1), the input $u_{eq}(k)$ that is defined for the nominal plant should be modified to exclude the control term v(k - 1). From (32), we have $u(k - 1) = u_{\beta}(k-1) + v(k-1)$ and redefine the equivalent control input $u_{eq}(k)$ for nominal system as follows:

$$u_{\rm eq}(k) = -(c^T b_2)^{-1} c^T A x(k) - (c^T b_2)^{-1} (c^T b_1) u_\beta(k-1).$$
(33)

So, the control law with compensation for the unknown perturbation can be obtained as

$$u(k) = -(c^{T}b_{2})^{-1}c^{T}Ax(k) - (c^{T}b_{2})^{-1}(c^{T}b_{1})u_{\beta}(k-1) + (c^{T}b_{2})^{-1}\beta s(k) + v(k).$$
(34)

In fact, the control cannot be actually implemented because of a lack of knowledge of current d(k). Under the assumption that the perturbation is bounded and much slower than the sampling frequency $1/T_s$, d(k-1) can be used as an estimate of d(k). Substituting (34) into the expression of s(k + 1) = 0, the following equation is obtained:

$$d(k) = -(c^{T}b)^{-1}[(c^{T}b_{2})v(k) + (c^{T}b_{1})v(k-1)] + (c^{T}b)^{-1}[s(k+1) - \beta s(k)].$$
(35)

Based on (35), a one-step backward shifted perturbation becomes

$$d(k-1) = -(c^{T}b)^{-1} [(c^{T}b_{2})v(k-1) + (c^{T}b_{1})v(k-2)] + (c^{T}b)^{-1} [s(k) - \beta s(k-1)].$$
(36)

The compensation input v(k) is defined as an estimate of -d(k): $v(k) = -\hat{d}(k) = -d(k-1)$. From (36), the following perturbation compensation law can be obtained:

$$v(k) = (c^{T}b)^{-1} [(c^{T}b_{2})v(k-1) + (c^{T}b_{1})v(k-2)] - (c^{T}b)^{-1} [s(k) - \beta s(k-1)].$$
(37)

Obviously, (37) is the decoupled perturbation compensator and the convergence rate of |d(k) - v(k)| is only related to $(c^Tb)^{-1}[s(k) - \beta s(k-1)]$. If the perturbation is constant or slowly varying, another adjustable parameter α ($0 < \alpha \le 1$) was added on the item that contains the switching function in (37) to make |d(k) - v(k)| converges asymptotically to zero in a desired sliding function dynamics, and hence the following equations can be obtained:

$$v(k) = (c^{T}b)^{-1} [(c^{T}b_{2})v(k-1) + (c^{T}b_{1})v(k-2)] - \alpha (c^{T}b)^{-1} [s(k) - \beta s(k-1)].$$
(38)

For a periodic disturbance with the known period *N*, the current disturbance can be actually obtained as a one-period shifted disturbance as

$$d(k - N) = -(c^{T}b)^{-1} [(c^{T}b_{2})v(k - N) + (c^{T}b_{1})v(k - N - 1)] + \alpha(c^{T}b)^{-1} [s(k - N + 1) - \beta s(k - N)].$$
(39)

The compensation input v(k) is defined as an estimate of $-\hat{d}(k) = -d(k - N)$, thus,

$$v(k) = (c^{T}b)^{-1} [(c^{T}b_{2})v(k-N) + (c^{T}b_{1})v(k-N-1)] - \alpha (c^{T}b)^{-1} [s(k-N+1) - \beta s(k-N)].$$
(40)

Obviously (40) is the decoupled compensator for periodic disturbance.

In theory, control signal can steer the errors from any finite value to the sliding surface in one step if the magnitude of the control signal can be arbitrarily large. In real control implementation, there is always a limitation $[u_{\min}, u_{\max}]$ for the actual control signal, whose amplitude must be within certain range. If the control input signal is too large, some components of the systems would be damaged. In order to avoid this, the range must be chosen so as to ensure the steering of the errors to the sliding surface in a finite number of steps instead of a single step. Similar to Bartolini et al. [14], in this study, the control input u(k) is selected as follows:

$$u(k) = \begin{cases} u(k) & \text{if } |u(k)| < u_0 \\ \frac{u_0 u(k)}{|u(k)|} & \text{if } |u(k)| > u_0, \end{cases}$$
(41)

where u_0 is the actual control limit. This control law also forces the errors to reach the discrete-time sliding mode with a decreasing sequence |d(k) - d(k - N)| [39].

Remark 4. If there exists no computation time delay, that is, if $\delta = 0$, then the matrix b_2 becomes b, and the b_1 term disappears. When there exists the time delay, the forms of the control law and disturbance estimator had been changed due to b_1 and b_2 , and so the complete disturbance rejection can also be achieved via the above (40).

4. Application to a Hard Disk Drive

4.1. Modeling the Plant. In hard disk drive (HDD) model, the rigid body model of the voice coil motor (VCM) and the resonance dynamics are the most important for servo control. The block diagram of the HDD servomechanism with DSMC is shown in Figure 1. The nominal model, that is, the rigid body dynamics of VCM, is formulated as a double integrator model as follows:

$$H(s) = \frac{y(s)}{i_c(s)} = \frac{k_v k_y}{s^2},$$
 (42)

where y(tracks) is the position of read/write head, i_c is the VCM input current, k_y is the position measurement gain, and k_v is the acceleration constant.

There exist several resonance modes due to the flexibility of the actuator assembly. The transfer function for the resonance dynamics [34] can be expressed as follows:

$$H_d(s) = \sum_{j=1}^4 \frac{b_{2j}\omega_j s + b_{2j-1}\omega_j^2}{s^2 + 2\xi\omega_j s + \omega_j^2}.$$
 (43)

During the controller design, system resonance dynamics was treated as model uncertainties and only double integrator model for the voice coil motor is considered, and as a result, the corresponding state space model of the system becomes

$$\begin{cases} \dot{y}(t) \\ \dot{h}(t) \end{cases} = \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{cases} y(t) \\ h(t) \end{cases} + \begin{bmatrix} 0 \\ k_h k_y \end{bmatrix} u(t), \qquad (44)$$

where *y* and *h*are the head displacement and velocity, respectively, and *u* is the VCM coil current input. Let $F = \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix}$, $G = \begin{bmatrix} 0 \\ k_h k_y \end{bmatrix}$; if the sampling time is assumed to be T_s , using (8), (*A*, *b*) and their the series expansion expression in [34], the matrix of discrete-time system x(k + 1) = Ax(k) + bu(k) can be obtained as ($F^2 = 0$ in this case)

$$A = e^{FT_s} = I + FT_s + \frac{F^2 T_s^2}{2!} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} + \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} T_s$$

$$= \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} + \begin{bmatrix} 0 & T_s \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & T_s \\ 0 & 1 \end{bmatrix}$$

$$b = \int_0^{T_s} e^{F\tau} d\tau G = \begin{bmatrix} IT_s + F \frac{T_s^2}{2!} \end{bmatrix} G$$

$$= \begin{bmatrix} \frac{k_y k_h T_s^2}{2} \\ k_y k_h T_s \end{bmatrix},$$
(45b)



FIGURE 1: Block diagram of DSMC seeking control of an HDD.

and the discrete-time model of the nominal plant becomes

$$\begin{cases} y(k+1) \\ h(k+1) \end{cases} = \begin{bmatrix} 1 & T_s \\ 0 & 1 \end{bmatrix} \begin{cases} y(k) \\ h(k) \end{cases} + \begin{bmatrix} \frac{k_y k_h T_s^2}{2} \\ k_y k_h T_s \end{bmatrix} u(k).$$
(46)

The measured frequency response function of the system is shown in Figure 2, and the system parameters can be identified as $k_y = 63867.78 \text{ track} \cdot \text{m}^{-1}$, $k_h = 772.13 \text{ m s}^{-2} \text{ A}^{-1}$, $u_{\text{max}} = 0.25 \text{ A}$, $T_s = 1/12e3 \text{ s}$, and the spindle motor speed 4200 rpm. The periodic disturbance (RRO) d(k)in HDD is assumed to be $d(k) = 0.01 \sin 2\pi (70kT_s) +$ $0.005 \sin 2\pi (140kT_s) + 0.003 \sin 2\pi (210kT_s) +$ $0.001 \sin 2\pi (280kT_s) + 0.001 \sin 2\pi (350kT_s)$.

Let the computation time delay be δ , using the expansion equation in [34] and following the above procedure, then the discrete-time model becomes

$$\begin{cases} y(k+1)\\h(k+1) \end{cases} = \begin{bmatrix} 1 & T_s\\0 & 1 \end{bmatrix} \begin{cases} y(k)\\h(k) \end{cases} + \begin{bmatrix} \frac{k_y k_h (2T_s \delta - \delta^2)}{2}\\k_y & k_h \delta \end{bmatrix} u(k-1) + \begin{bmatrix} \frac{k_y k_h (T_s - \delta)^2}{2}\\k_y k_h (T_s - \delta) \end{bmatrix} u(k). \end{cases}$$

4.2. Controller Design. The DSMC and the compensator for periodic disturbance are developed as (14) and (20). Based on the nominal plant model (47), that is, the double integrator model, the switching function is designed by defining c^T = [10000 1]. The other parameters in the closed-loop model are chosen as $\beta = 0.01$ and $\alpha = 0.8$. The saturation for control effort is selected as ±0.25. Simulation results are shown clearly in Figure 3. The periodic disturbance can be estimated accurately after three periods, as shown in Figure 3(a); the proposed method also reduces position error signal (PES) effectively due to the resultant periodic compensation control input, as shown in Figure 3(b). For parametric uncertainties and nonperiodic disturbances, the resonant modes of the head actuator are taken into account; that is, by adding the first fourth resonances to the SIMULINK model for nominal plant, the system with the proposed DSMC and compensator has been found to be just as stable and to exhibit the



FIGURE 2: Measured versus the identified (double integrator model) FRFs.



FIGURE 3: Simulation results of proposed DSMC for periodic disturbance compensation.



FIGURE 4: The experimental setup.

same performance. This demonstrates that the DSMC had effectively suppressed the uncertainties due to the resonance modes of the system with different control effort, as shown in Figure 3(c). To examine the robust stability to parameter

variations, the plant model parameters are assumed to be estimated with errors from their nominal values, and here the resonant dynamics is still taken as part of model uncertainties. Figure 3(d) depicts the simulation results comparison using the same control parameters for the following fours cases: (1) no estimation error; (2) with 10% estimation error; (3) with 20% estimation error; (4) with 30% estimation error. Figure 3(d) clearly exhibits the robust performance of the designed controller.

5. Experimental Results

Figure 4 shows the experimental setup used for this investigation. An HP signal analyzer (HP 35670A) was used for modal testing. The control desk consists of the laser Doppler vibrometer (LDV), an HDD, and a PC installed with the DSpace



FIGURE 5: Control system configurations.



FIGURE 6: DSMC for track-to-track seeking of HDD without external disturbance.



FIGURE 7: DSMC without disturbance compensator for track-to-track seeking of HDD with periodic disturbances (time delay $\delta = 0$).

DSP system (DSPACE 1103) and the MATLAB software packages. The detailed configurations of the control system are shown in Figure 5. A sampling time of $T_s = 83.33 \,\mu s$ was used during the control implementation.

Experimental results for track-to-track seeking in different cases are shown in Figures 6–10. Figure 6 shows the closed-loop response of the system with the proposed DSMC after the parameters were adjusted to become $c^T = [9500 1]$, $\beta = 0.16$, and $\alpha = 0.85$ with ± 0.25 A as the saturation for control effort. The steady-state error of the closed-loop system is zero; that is, PES does not exist in track-to-track following by the DSMC when there does not exist external disturbance in the servo system. The settling time that occurs in the step response of time domain is just about 1ms for one track.



FIGURE 8: DSMC without disturbance compensator for track following of HDD with periodic disturbances (time delay $\delta = 0$).



FIGURE 9: DSMC with disturbance compensator for track-to-track seeking of HDD with periodic disturbances (time delay $\delta = 0$).

The overshoot is also lower than half of the reference signal. When the external disturbances with frequency 420 Hz (period N = 420 Hz) as shown in Figure 7 were added to the servo system, the PES of the closed-loop system with the proposed controller was increased significantly in the case where decoupled disturbance compensator was not incorporated into the control system. The PES of such closed-loop system for track following of HDD with RRO is very significant as shown in Figure 8. On the other hand, PES measurement of the closed-loop system with DSMC and decoupled periodic disturbance compensator for track-to-track seeking is shown in Figure 9. The amplitude of PES was greatly decreased from 25 mv to 6.2 mv after optimizing the parameters of the compensator, decreased more than 75% as compared with that of Figure 7. The PES of the closed-loop system for track following of HDD with RRO was significantly reduced when a disturbance compensator was incorporated,

as shown in Figure 10. Compared with Figure 8, it can be seen clearly that the PES has been reduced by more than 75%. As shown in Figure 10, however, the disturbance could not be compensated completely probably due to the computation time delay as will be shown next.

Experimental PES measurement of DSMC with disturbance compensator that considers the computation time delay is shown in Figures 11 and 12.

After the computation time delay was adjusted to become $\delta = 30.0 \,\mu$ s, responses of DSMC with disturbances compensation for track-to-track seeking were measured and are shown in Figure 11. Obviously, from Figure 11, periodic disturbance has almost been compensated completely, and virtually there is no difference between the reference and the steady response. The effectiveness of DSMC and disturbance compensator with computation time delay can be also seen from the experimental results obtained from track following



FIGURE 10: DSMC with disturbance compensator for track following of HDD with periodic disturbances (time delay $\delta = 0$).



FIGURE 11: DSMC with disturbance compensator for track-to-track seeking of HDD with periodic disturbances (time delay $\delta = 30.0 \,\mu$ s).

control, as shown in Figure 12. From which, it can be seen clearly that PES has been suppressed considerably.

6. Conclusions

A method of achieving robustness for perturbation rejection in applying discrete-time sliding mode control has been presented. The developed DMSC with DPC control method, which considers computation time delay, achieves robust tracking in the presence of unknown disturbances, which include external disturbances as well as parameter uncertainties. The presented method decouples the disturbance estimation dynamics from the closed-loop sliding mode dynamics. An additional parameter has been introduced to allow the asymptotic convergence in a desired sliding dynamics in both tracking and regulation problems. The satisfied simulation results as well as experimental results based on the servomechanism of a modern 2.5["] HDD have been achieved to demonstrate the practicality of the proposed method. The simulation results show the satisfactory feature of the complete rejection of the periodic component. In the experimental implementation of the control based on the DSP1102, the performances of the closed-loop system using DSMC and the perturbation compensator control were improved greatly when computation time delay was considered. This demonstrates the robustness of the sliding mode dynamics to parameter uncertainties and exogenous disturbances containing periodic components and such robustness is always guaranteed for developed control scheme.

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FIGURE 12: DSMC with disturbance compensator for track following of HDD with periodic disturbances (time delay $\delta = 30.0 \,\mu$ s).

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