JPE 11-1-7

# Comparison and Evaluation of Anti-Windup PI Controllers

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#### **Abstract**

This paper proposes a method for comparing and evaluating anti-windup proportional-integral (PI) control strategies. The so-called PI plane is used and its coordinate is composed of the error and the integral state. In addition, an anti-windup PI controller with integral state prediction is proposed. The anti-windup scheme can be easily analyzed and evaluated on the PI plane in detail. Representative anti-windup methods are experimentally applied to the speed control of a vector-controlled induction motor driven by a pulse width modulated (PWM) voltage-source inverter (VSI). The experimental results compare the anti-windup PI controllers. It is empathized that the initial value of the integral state at the beginning of the linear range dominates the control performance in terms of overshoot and settling time.

Key Words: Anti-windup, Integral state prediction, PI controller, Variable-speed motor drives

#### I. INTRODUCTION

PI control has been widely used as a cascaded form of control in variable-speed motor drives. The current control is employed in an inner feedback loop for fast dynamics and peak current protection and the outer speed controller generates a current command for the current controller. Since the current command is typically limited to a prescribed maximum value, the integral state is not consistent with the plant input and becomes very large, which causes the integral windup phenomenon. There exists a large overshoot, a slow settling time, and, sometimes, even instability in the speed response [1], [2].

A number of the anti-windup schemes have been proposed in the literature and may be representatively classified into three categories which include the conditional integration, the tracking back calculation, and the limited integrator schemes. For the conditional integration schemes [3]–[6], the integral action is suspended when the control input is saturated and the P control only is activated. The PI control is effective when the control input lies within the saturation limit. The difference between the saturated and the unsaturated control input signals is used to generate a feedback signal to act on the integrator input for the tracking back calculation methods [1], [7]–[9]. The transient performance such as the overshoot depends heavily upon the feedback gain of the control difference rather than the PI gains. For the limited integrator scheme [10], the

integrator value is limited by feeding the control back with a high-gain dead zone in order to guarantee operation in the linear range. In case of a saturation-type limit, this scheme is same as the back calculation method.

It is very difficult and complex to compare and evaluate anti-windup PI controllers since we should consider the overall operating conditions such as the possible load and command changes as well as the manual/auto transfer operations, etc.

In this paper, a method is proposed for comparing and evaluating anti-windup PI control schemes. An anti-windup PI controller with the integral state prediction is also proposed. The PI plane is utilized and its coordinate is composed of the error and the integral state. The anti-windup scheme can be easily analyzed and evaluated in detail on the PI plane. Representative anti-windup methods are experimentally applied to the speed control of a vector-controlled induction motor driven by a pulse width modulated (PWM) voltage-source inverter (VSI). Through the experimental results, the anti-windup PI controllers are compared and evaluated.

## II. PI PLANE AND THE LINEAR RANGE OF PI CONTROL

It is common that an inner current controller is designed to have much faster dynamics than an outer speed controller in a cascaded form. If a fast current control scheme is employed, the current dynamics is negligible and a variable-speed motor drive can be considered as a first-order system given by:

$$\frac{d\omega_r}{dt} = -\frac{1}{\tau_m}\omega_r + k_l v - T_l \tag{1}$$

where  $\tau_m = J/B$ ,  $k_t = k_T/J$ ,  $T_l = T_L/J$  and  $\nu$  denotes the plant input, namely, the torque-producing current [11]. It is

Manuscript received Aug. 13, 2010; revised Nov. 4, 2010

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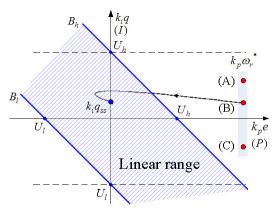


Fig. 1. Linear range of PI control in PI plane.

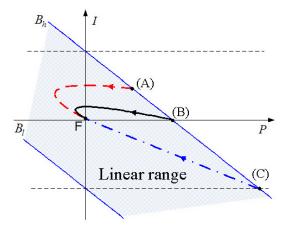


Fig. 2. Error trajectories according to touch conditions.

assumed that the plant input v is limited by a saturation-type nonlinearity as:

$$v = \begin{cases} U_h & \text{if} \quad u > U_h \\ u & \text{if} \quad U_l \le u \le U_h \\ U_l & \text{if} \quad u < U_l \end{cases}$$
 (2)

where u represents the controller output. In the following, this will be called a linear range and a saturation range when u = v and  $u \neq v$ , respectively.

The PI control law is expressed by:

$$u = k_p e + k_i q \tag{3}$$

where  $k_p$  and  $k_i$  denote the proportional and integral gains, respectively. Their error is:

$$e = \omega_r^* - \omega_r \tag{4}$$

where  $\omega_r^*$  denotes the speed reference. The integral state q is dependant upon the anti-windup scheme.

If the plant input constraint in (2) is considered, the upper and lower boundaries of the linear range can be found from (3) as:

$$B_h: \quad I = -P + U_h$$
  

$$B_I: \quad I = -P + U_I$$
(5)

where I and P denote the integral and proportional actions, respectively, and  $I = k_i q$  and  $P = k_p e$ . Fig. 1 shows the linear range in  $k_p e$ - $k_i q$  or the P-I axis, which will be called the PI

plane in the following. If the operating point  $(k_p e, k_i q)$  lies outside the linear range, the control input is saturated and the integral state is not consistent with the plant input. The P action only is effectively activated. From Fig. 1, the steady state value of the integral state  $k_i q_{ss}$  can be written, if the PI control system is stable, as:

$$U_l < k_i q_{ss} < U_h. (6)$$

There are many cases like startup, speed up, speed down, reversal, and manual/auto control mode changes in the controller operation. Some cases occur in the linear range and some cases occur in both the saturation and the linear range. According to the difference between the initial and the final integral states, three initial conditions can be considered as seen in Fig. 1; (A) higher, (B) equal, and (C) lower. The P-axis value of the initial condition is usually dependent upon the speed reference and the *I*-axis value is related to the previous operating conditions. The P-axis value of the steady state condition should be zero due to the error integral action and the I-axis value is related to the operating conditions such as the load torque and the speed reference. If the PI trajectory is like the one in Fig. 1, the PI control enters into the saturation range rapidly, and the trajectory goes to zero error via an integral state corresponding to the anti-windup scheme. This interval affects only the rise time in the control performance specification and the P control dominates. The PI trajectory touches the boundary of the linear range if the attractive condition is satisfied [7].

The error dynamics are dependent upon both the selected PI gain and the touch conditions like the integral state value. Fig. 2 shows the error responses according to the touch conditions under the same PI gain condition. It can be seen that the overshoot is related to the relative position of the integral state against the steady state point. When the integral state on the linear range boundary is higher than the steady state, an overshoot is apt to occur in the error response. On the other hand, the error response may be slowed down as the integral state on the linear range boundary is lower than the steady state value. It is noted that the error response can be deteriorated by an improper touch condition even though a proper PI gain is selected.

There are many schemes for finding the proper touch condition, which is called the anti-windup technique. The touch condition is different depending on the anti-windup scheme. The linear range cases are well expressed and controlled by a well-designed PI controller. However, the saturation range cases are not well understood or controlled.

### III. ANTI-WINDUP PI CONTROL SCHEMES

#### A. Conditional integration

Fig. 3 shows an anti-windup PI controller based on the conditional integration method [3]. Integral action is switched on or off depending on the linear range or the saturation range such as:

$$\dot{q} = \begin{cases} e & \text{if} \quad u = v \\ 0 & \text{if} \quad u \neq v \end{cases} . \tag{7}$$

The method is also called integrator clamping. There are a few switching conditions to be considered. One simple

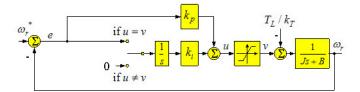


Fig. 3. Conditional integration scheme.

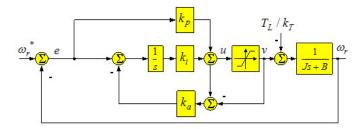


Fig. 4. Tracking back calculation scheme.

approach is to switch off integration during saturation. This method has the disadvantage that the value of the integral part is large when the controller output is different from the plant input. Suppose that the controller is saturated at the upper limit. The integrator is then switched off if the control error is positive, but not if it is negative [1]. Choi et al. [6] have presented a new method based on the conditional integration scheme. The controller changes between the P and PI modes according to specific conditions. However, the main difference is how to set the initial value of the integrator in the PI control mode. The initial value of the integrator is loaded in the P mode, and the controller works in the PI mode utilizing the loaded initial value. The integrator value is written to compensate for the viscous damping and the load torque at a well-controlled steady state condition without speed error [6]. However, with this method it is difficult to choose a gain to satisfy the anti-windup performance, and the selection of gain is related to the zero of the transfer function. However, the proposed method can result in no overshoot speed response performance.

#### B. Tracking back calculation

Fig. 4 shows the tracking back calculation method [6]. In the linear range, the error is integrated and the difference between the saturated and the unsaturated control signals is used to generate a feedback signal to properly control the integral state in the saturation range such as:

$$\dot{q} = \begin{cases} e & \text{if } u = v \\ e - k_a(u - v) & \text{if } u \neq v \end{cases}$$
 (8)

It may seem advantageous to choose a very large value for the anti-windup gain  $k_a$  because the integrator can be limited quickly. If an anti-windup gain is chosen that is too big, a spurious error can cause input saturation, which accidentally resets the integrator. Usually, the gain  $k_a$  is selected as  $1/k_p$ . There is a new scheme for the tracking back calculation method [7]. If the PI controller output is saturated, the integral state is reduced with the rate of the integral time constant by negatively feeding back the controller output. Otherwise, the

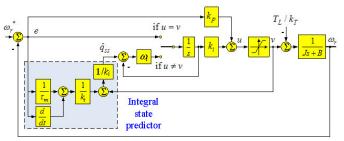


Fig. 5. Integral state prediction scheme.

integral state accumulates a speed error and the PI action is activated.

### C. Integral state prediction

Fig. 5 shows the integral state prediction (ISP) method [12]. For a step change of the speed reference, the error dynamics can be obtained from (1) and (4) by:

$$\dot{e} = -\frac{1}{\tau_m} e - k_t u + T_l + \frac{1}{\tau_m} \omega_r^*. \tag{9}$$

If a PI controlled system is stable, the integral action in the steady state can be expressed as:

$$k_i q_{ss} = \left(T_l + \frac{1}{\tau_m} \omega_r^*\right) / k_t \tag{10}$$

where the subscript ss denotes the steady state value. It is noted that the steady state integral action depends on the external load torque and the speed reference. Substituting (10) in to (9) yields the error dynamics as:

$$\dot{e} = -\frac{1}{\tau_m} e - k_t u + k_t k_i q_{ss}. \tag{11}$$

When PI control operates in the linear range, the output error is connected to the integrator input. When the control operates in the saturation range, the integral state is reset to a predicted steady state value through a low-pass filter to prevent abrupt integral state changing such as:

$$\dot{q} = \begin{cases} e & \text{if } u = v \\ \omega_i(\hat{q}_{ss} - q) & \text{if } u \neq v \end{cases}$$
 (12)

where  $\hat{q}_{ss}$  is the integral state value in the steady state and it is predicted by using (11) as:

$$\hat{q}_{ss} = \frac{1}{k_i} \left( \frac{1}{k_t} \left( \dot{e} + \frac{1}{\tau_m} e \right) + v \right). \tag{13}$$

The integral state loading time can be determined by adjusting the parameter  $\omega_i$  properly. Since the integral state prediction in (13) includes the error derivative, the bandwidth of the low-pass filter is constrained by the derivations. In order to achieve the desired performance dynamics specifications while maintaining closed-loop stability, it is therefore important to select an integral state loading time that is faster than the PI controlled system dynamics.

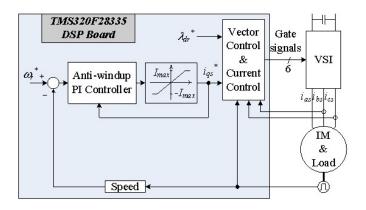


Fig. 6. Block diagram of vector-controlled induction motor drive using anti-windup PI speed controller.

TABLE I PARAMETERS FOR SIMULATION & EXPERIMENT

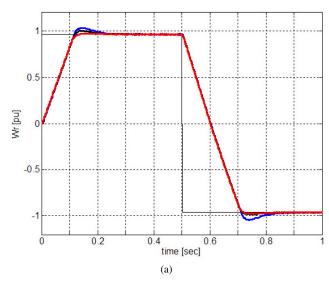
Motor speed	1730 [rpm]
Rated frequency	60 [Hz]
Saturation limited range	[-7, 7] [A]
Torque constant $(K_T)$	1.3 [Nm/A]
Moment of inertia ( <i>J</i> )	5.7 10 <sup>-3</sup> [Kgm2]
Viscous damping coefficient(B)	$1.65 \times 10^{-3} [\text{Kgm2/s}]$

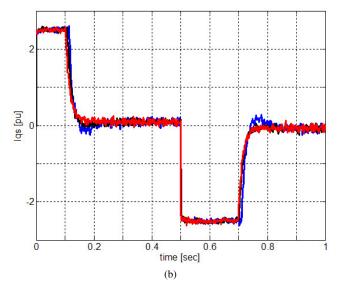
# IV. EXPERIMENTAL COMPARISON OF ANTI-WINDUP PI CONTROL SCHEMES

In order to compare and evaluate anti-windup PI control schemes, the speed control of an induction motor has been implemented and experimented on. The parameters of a 1-hp induction motor are listed in Table I. Fig. 6 shows a block diagram of the experimental system. In the vector control method, the induction motor is controlled like a separately excited dc motor. The control algorithm is fully implemented in software with a TMS320F28335 DSP, which includes A/D converters and a three-phase PWM generator. The three-phase currents are controlled to settle within 2 ms by using a synchronous PI regulator and the rotor flux is controlled to settle within 40 ms. The space vector PWM method is used and the PWM frequency is 10 KHz. The sampling times of the current and the speed control loops are 0.1 ms and 2 ms, respectively. The shaft encoder has 1,000 pulses per revolution. Among the various anti-windup PI controllers, the conditional integration, the tracking back calculation, and the proposed ISP schemes are compared.

The PI gains are chosen as a rise time of 0.1 sec and a damping ratio of 1. The gain  $k_a$  in the tracking back calculation is taken as  $1/k_p$ . The parameter  $\omega_i$  is selected as a 1/(0.5\*rise time).

Fig. 7 shows the experimental results of the anti-windup schemes at no load. The base values of the speed and current are 1800 rpm and 2.77 A, respectively. It can be seen from Fig. 7(c) that the magnitude order of the integral state value, compared with its steady state value, is the conditional integration, the ISP, and the tracking back calculation when the PI controller enters the linear range. Therefore, the tracking back calculation method has the largest overshoot in speed response and the proposed ISP scheme has a similar response to the conditional integration scheme as shown in Fig 7(a).





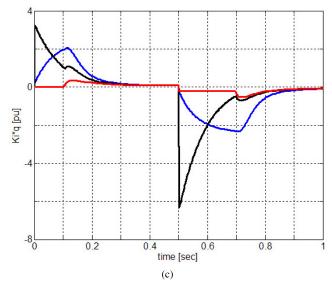


Fig. 7. Experimental comparison at no load. (a) speed response. (b) torque producing currents. (c) integral states. (dotted: conditional integration, dashed: tracking back calculation, solid: proposed ISP).

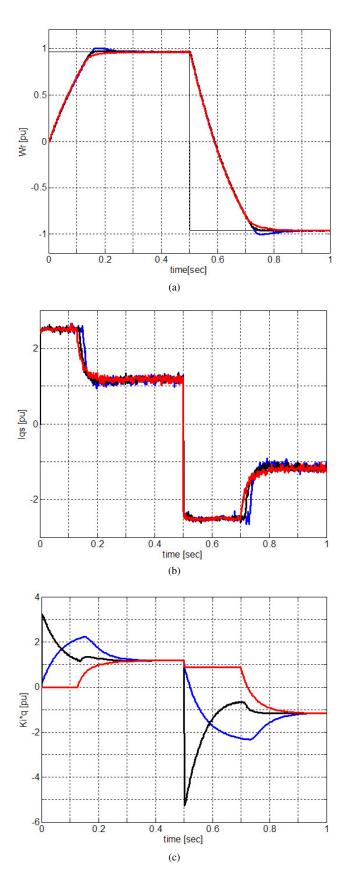


Fig. 8. Experimental comparison at full load. (a) speed response. (b) torque producing currents. (c) integral states. (dotted: conditional integration, dashed: tracking back calculation, solid: proposed ISP).

Compared with the PI controlled system, the integral state loading of the ISP has slow dynamics because the speed sensing from the encoder has a high noise at the low speed range and the predicted integral state also has noise due to the differential action. Therefore, it can be seen from Fig. 7(c) that the integral state of the ISP cannot reach its steady state value at the startup condition but that it reaches near the steady state value at the speed reversal condition.

Fig. 8 compares experimentally the speed responses of the anti-windup controllers at full load. When the PI controller enters into the linear range, the integral state of the ISP scheme goes into its steady state value as seen in Fig. 8(c). Hence, the speed output has nearly the same responses in both the startup and the reversal conditions. In the case of the conditional integration, the integral state value is much lower than its steady state value at the beginning of the linear range, so that the speed response is overdamped as shown in Fig. 8(a). For the tracking back calculation method, the integral state has a larger magnitude than the steady state and an overshoot occurs in the speed response.

From the PI plane analysis and the experimental results, the transient response of the anti-windup PI controllers can be considered as a saturation range response and a linear range response. The saturation range response is related to the rise time of the PI system and it depends mainly on the control output limit value and the P gain. The linear range response is related to the control performances such as the overshoot and the settling time. The linear performance depends on both the PI gain and the initial integral state at the beginning of the linear range. The PI gain determines the system stability and the initial integral state value is very important for obtaining a well-designed transient response.

## V. CONCLUSIONS

An anti-windup PI control scheme for variable-speed motor drives has been proposed in order to overcome the windup phenomenon and some representative anti-windup PI controllers have been compared and evaluated. The control schemes have been applied to the speed control of a vector-controlled induction motor driven by a PWM-VSI and its usefulness has been experimentally verified.

The PI plane is very useful for evaluating the anti-windup PI controller. The rise time is mainly related to the control input limit during the saturation range regardless of the anti-windup scheme. Also, the control performances such as the overshoot and the setting time depend on the touching condition in the PI plane rather than the PI gains.

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