Long-haul Secure Data Transfer using Hardware-assisted GridFTP

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***Abstract* — Extreme-scale scientific collaborations require high-performance wide-area end-to-end data transports to enable fast and secure transfer of high data volumes among collaborating institutions. GridFTP is the de facto protocol for large-scale data transfer in science environments. Existing predominant network transport protocols such as TCP have serious limitations that consume significant CPU power and prevent GridFTP from achieving high throughput on long-haul networks with high latency and potential packet loss, reordering and jitter. On the other hand, protocols such as UDT that address some of the TCP shortcomings demand high computing resources on data transfer nodes. These limitations have caused underutilization of existing high-bandwidth links in scientific and collaborative grids. To address this situation, we have enhanced Globus GridFTP, the most widely used GridFTP implementation, by developing transport offload engines such as UDT and iWARP on SmartNIC, a programmable 10GbE network interface card (NIC). Our results show significant reduction in server utilization and full line-rate sustained bandwidth in high-latency networks, as measured for up to 100 ms of network latency. In our work, we also offload OpenSSL on SmartNIC to reduce host utilization for secure file transfers. The offload engine can provide line-rate data channel encryption/decryption on top of UDT offload without consuming additional host CPU resources. Lower CPU utilization leads to increased server capacity, which allows data transfer nodes to support higher network and data-processing rates. Alternatively, smaller or fewer DTNs can be used for a particular data rate requirement.**

Keywords— GridFTP; UDT; Network Offload; RDMA; SSL; Scientific Grid

1 INTRODUCTION

Extreme-scale scientific computations, experiments, and collaborations require unprecedented wide-area end-to-end data transfer capabilities with high-throughput data transports to enable the exchange of high data volumes between organizations or facilities. Such requirements arise in a number of science areas including climate, high energy physics, astrophysics, combustion, Nano-science, and genomics. The modeling of complex systems, such as climate or supernovae, at higher and higher fidelity generates proportionately larger volumes of data that must be visualized, examined, and analyzed by widely dispersed scientist teams looking for insight and discovery. Unfortunately, the amount of data being created by many computational codes faces significant transfer limitations, despite the available 10 Gbps and 100 Gbps capacities of science network backbone connections. Supernovae simulation, genomics, and combustion modeling are some of the areas affected by such shortcomings.

The current data transfer limitations are no longer an artifact of the limited capacity in the network backbone as originally surmised. Indeed, the 10Gbps and 100Gbps backbones of either the Energy Science Network (ESNet) [1] or Internet2 [2] can currently offer the capacity to connect pairs of sites for extended periods. These networks enable scientific applications to transfer extremely large data files, primarily by using high-performance data transfer protocols such as GridFTP [3]. However, the fact that science users rarely see or use this bandwidth is symptomatic of much deeper challenges, which will only get worse with the next generation of multi-petascale and future exascale projects. Effectively utilizing

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the available bandwidth requires efficient underlying transport protocols and end-to-end implementations that can sustain a high bandwidth in high-latency transfers.

Historically, wide-area data transport has been handled mostly by the Transmission Control Protocol (TCP), which has been the basis for the File Transfer Protocol (FTP), GridFTP [3], bbcp [4], and HyperText Transfer Protocol (HTTP). The unprecedented demands that extreme-scale applications place on data transport, and the geo-diversity of today's scientific collaborations have pushed TCP beyond its useful envelope [5]. The fundamental problem with TCP ultimately reduces to its treatment of bandwidth as a shared resource. A number of efforts have been made to develop high-performance versions of TCP [6][7][8], but with only limited success.

Existing research shows that the UDT transport protocol [9], a reliable user-level protocol on top of UDP, can achieve a much better throughput than a TCP connection can over a long-haul network [10]. Nevertheless, the user-level processing incurred by UDT consumes many more resources than does a TCP connection (for the same data rate), effectively preventing the widespread realization of UDT benefits. In addition, host resource requirements are expected to limit the maximum achievable bandwidth of GridFTP, since on many systems the processing time required to process the UDT messages will be even larger than the data transfer time. Therefore, technologies are needed to enable near "wire" transfer speeds, while using fewer host resources, allowing for more host capacity.

Another challenge is data transfer security. Many applications (e.g., medical, pharmaceutical, aerospace, and security applications) require secure GridFTP communications. GridFTP offers options to secure data transfers. However, the resources required for performing operations such as encryption and decryption greatly reduce the achievable bandwidth. Therefore, users either must see greatly reduced throughput or risk sending unencrypted data [11].

Besides the actual protocol processing, data transfer nodes (DTNs) [12] at the edge of the networks are responsible for several other data-processing tasks. Such tasks include file integrity check by computing the checksum after the file is written to disk (as done by the Globus transfer service [13]), data compression, and data reduction. Therefore, it is crucial to free some of the DTN's processing power for these additional data-processing tasks without affecting the observed throughput.

To address these challenges, we present a set of asynchronous network interface card (NIC) offload engines that improve the GridFTP long-range throughput and host resource utilization. Using a SmartNIC 10GbE card [14], we examine the benefits of offloading UDT to allow for line-rate bandwidth on long-haul networks. We also use the UDT engine as the underlying transport protocol for offloading the Remote Direct Memory Access (RDMA) and Secure Sockets Layer (SSL) protocols to further reduce resource consumption.

The main contributions of this paper are demonstrating the UDT, RDMA and OpenSSL offload implementation, its integration with GridFTP and its benefits for long-haul data transfers. We conduct a series of experiments to demonstrate our UDT offload implementation and show how it can reduce host CPU utilization and increase network throughput, compared to using a host-based UDT alternative. Our experiments show that by using NIC offload, near-line-rate throughput can be achieved through a single data stream on high-performance long-haul networks such as ESNet, while offering a multifold reduction in host CPU utilization. The offload engines developed in this work are portable (with minor tweaks) to any card featuring an OCTEON-based network processor.

The rest of this paper is organized as follows. Section 2 provides a background on SmartNIC, as well as the protocols and tools used in this work. Section 3 expands on the motivations of our work. Section 4 discusses the related state of the art. In Section 5, we discuss our detailed design and implementation of NIC offload engines. In Section 6, we present experimental results, followed by analysis and conclusion in Section 0.

2 BACKGROUND

In this section, we provide a brief background on GridFTP, UDT, and RDMA, along with the SmartNIC user programmable card.

2.1 GridFTP

GridFTP is a high-performance, secure and reliable data transfer protocol optimized for high-bandwidth wide-area networks [3]. It is based on the Internet FTP protocol, with extensions for high-performance operation and security. GridFTP is the preeminent standard for science projects requiring secure, robust, high-speed bulk data transport.

The GridFTP protocol is a backward-compatible extension of the legacy RFC 959 FTP protocol [15]. It maintains the same command/response semantics introduced by RFC 959. It also maintains the two-channel protocol semantics. The separation of the control and data channels in GridFTP enables third-party transfers, that is, the transfer of data between two end hosts, mediated by a third host. This functionality made it possible to develop hosted clients such as Globus Transfer [13] for GridFTP servers.

The de facto implementation of GridFTP is the Globus distribution [1][16]. This implementation is used by thousands of users with millions of data transfers per day. The Globus implementation of GridFTP provides a software suite optimized for a wide range of data access issues, from bulk file transfer to the details of getting data out of complex storage systems within sites.

Key features of GridFTP and its predominant implementation (Globus) include the following:

1) Parallel TCP streams: GridFTP uses parallel streams to overcome the inherent limitation in AIMD-based (Additive Increase, Multiplicative Decrease) TCP congestion control algorithm [17]. Typically, it provides orders of magnitude higher performance compared with that of standard FTP.
2) Cluster-to-cluster data movement: GridFTP can do coordinated data transfer by using multiple nodes and streams at the source and destination. This approach can increase performance by another order of magnitude.
3) Reliability: GridFTP provides support for reliable and restartable data transfers.
4) Multiple security options: The Globus GridFTP framework supports various security options, including Grid Security Infrastructure (GSI) [18], anonymous access, username- and password-based security similar to that of regular FTP servers, SSH-based security, and Kerberos. SSL is one of the essential standards utilized by GridFTP for security processing. GSI is built on SSL/TLS for encryption and mutual authentication. GridFTP-Lite [19] also uses SSL for its security purposes. OpenSSL is an open-source commercial-grade implementation of SSL/TLS protocols, utilized by GridFTP.
5) Modularity: The XIO-based [20] Globus GridFTP framework makes it easy to plug in other transport protocols. The Data Storage Interface (DSI) [21] allows for easier integration with various storage systems.
6) Third-party control: GridFTP also allows secure third-party clients to initiate transfers between remote sites.
7) Partial file transfer: Scientists often want to download only portions of a large file, instead of the entire file. GridFTP supports this capability by specifying the byte position in the file to begin the transfer.
8) Negotiation of TCP buffer/window sizes: GridFTP employs FTP command and data channel extensions to support both automatic and manual negotiation of TCP to get optimal performance.

2.2 Alternative Transports in GridFTP – UDT and RDMA

UDT is a reliable messaging protocol developed by the University of Illinois [9]. It is built on the top of UDP with reliability control and congestion control. With its congestion control algorithm, UDT can efficiently utilize the high-speed wide-area networks with a high bandwidth-delay product.

RDMA is one of the most significant breakthroughs in efficient network data transfer. RDMA allows for direct transfer of host buffers residing on distinct memory spaces without intermediate copies, host CPU involvement, or operating system overheads. The Internet Wide-Area RDMA Protocol (iWARP) specification (proposed by the RDMA Consortium [22] to the IETF in 2002) defines a stack of RDMA layers on top of standard TCP/IP over Ethernet. The stack decouples the processing of Upper Layer Protocol (ULP) data from the operating system and reduces the host CPU utilization by avoiding copies

during data transfer. In order to achieve these goals, iWARP must be fully offloaded to an RDMA-capable NIC (RNIC) on top of an offloaded transport protocol. While standard iWARP specifies only TCP and SCTP as the underlying protocols, it can be ported to other transports, such as UDP [23][24] (with modifications) or UDT.

2.3 SmartNIC – The Network Offload Platform

The 10 Gbps Ethernet SmartNIC [14] that we have used as our offload platform in this project consists of a Cavium OCTEON Plus CN5750 network processor [25], with 12 cnMIPS64 cores running at 750 MHz. An on-board 2 GB DRAM allows ample packet buffering space to enable deep message inspection and large windows. The SmartNIC has a PCIe x8 host connection and an SFP+ connector for the 10GbE networking connection.

The OCTEON network processors contain hardware processing units to accelerate common networking related functions, deep packet inspection, compression, data deduplication, RAID, security, DMA, and packet scheduling. The C/C++ based programmability of the OCTEON enables the SmartNIC to be easily programmed with software both during development and by the end user.

3 MOTIVATIONS

Several shortcomings prevent applications from achieving high throughput on long-haul network connections. The TCP protocol is not designed for high-throughput, high-latency links. Therefore, a TCP-based protocol stack cannot achieve good utilization in such circumstances. Because of its RTT[1] bias problem [26][27], which can lead to a drop or fluctuation in the observed throughput, it cannot efficiently handle data transfers over long-latency links.

Besides TCP throughput scaling issues, network processing overhead can limit the overall throughput a DTN node can provide, leading to deployment of more high-end servers to support the required throughput. Processing the entire TCP-based networking stack can potentially consume a large percentage of host resources. For example, a TCP message on a 10Gbps Ethernet link can easily consume most of the CPU cycles using a 2.2 GHz Opteron CPU [6]. This can be even worse for a UDT-based transport. While data transfer may be the main job provisioned for a DTN, other data-processing tasks (such as integrity check or compression) are also expected to be performed on the data, which require extensive CPU resources on the DTN. When the amount of host-based network protocol processing is high, little CPU resources will be left to perform other data operations. These shortcomings serve as the main motivations for our work in assisting large-scale wide-area data transfers with flexible hardware offloading. Here, a programmable platform can allow for flexibly utilizing a multitude of offload engines, as required by application and host requirements.

InfiniBand (IB) [28] was introduced as an alternative to Ethernet and TCP/IP, revolving around RDMA technology, to reduce host overheads and increase maximum achievable throughput. However, IB has received limited adoption among networks interconnecting scientific grids. It was originally designed for short-range data transfers typically between high-performance storage and computing systems. Devices such as Longbow [29] extend the reach of IB over wide-area connections, but with limited market. Efforts such as porting IB transport over an Ethernet link layer (e.g., RoCE [30]) have also received limited use, partially because of the lack of support for wide-area networks[2]. Therefore, traditional TCP/IP protocols are still the predominant choice for scientific data transfers.

Using UDT as the transport protocol, GridFTP has seen a much higher percentage of the available bandwidth. Despite the fact that UDT has proven to be a better alternative for sustaining higher throughput over high-latency networks [10], its CPU usage can be prohibitive, causing limited practical use in high-speed data transfer applications. With such limitations, a hardware offloading of UDT to the NIC is desired, in order to achieve both high network throughput and higher server capacity.

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[1] Round-trip time

[2] RoCEv2, under development, can support WANs using IPv6 routing.

Data security is another bottleneck. GridFTP is a command/response protocol. GridFTP, like any FTP, uses two channels: a control channel and a data channel. The control channel is used for sending commands and responses and is encrypted for security reasons. The data channel is used for transferring the actual data of interest. By default, the data channel is authenticated at connection time, but no integrity checking or encryption is performed because of performance overheads. Based on our experience in this work, enabling encryption or integrity protection can greatly reduce the throughput, sometimes by over 80%. Despite having accelerated encryption instructions in modern Intel processors (i.e., AES-NI and vector instructions), the processing requirements for data channel security are still high (as we discuss later). These issues have led many application users not to enable encryption or integrity checking on GridFTP data channel. Here is an area where NIC offload can help, significantly improving the observed throughput.

4 Related Work

Balman et. al [5] describe their experience with long-haul 100Gbps networks, particularly talking about application design issues, protocol shortcomings and tuning recommendations to enable high-rate long-haul data transfers. The authors in [31][32] particularly discuss the effect of CPU affinity on network processing performance, and provide recommendations on how to assign interrupts and user processes to cores in order to achieve higher performance.

Several attempts have been made to improve the performance of GridFTP file transfers. Bresnahan et al. compared the performance and system resources utilized for GridFTP-TCP and GridFTP-UDT transfers [10]. The experiments, which were conducted on the TeraGrid [31] network between Argonne and Oak Ridge national laboratories, showed that while UDT can achieve higher throughput than TCP can on a long-haul network, the CPU utilization for TCP transfers was lower, in the range of 30–50%, compared with that of UDT transfers, which was around 80%.

Kissel and Swany [34] presented an RDMA XIO driver for GridFTP, which uses revolving sender- and receiver-side queues for buffering RDMA messages over an emulated long-haul network based on RoCE. They showed how RDMA can sustain line-rate throughput over their emulated network. In an earlier attempt, Subramoni et al. [35] extended GridFTP to support RDMA over InfiniBand. The experiments were done using Longbow WAN routers [29], and close to line-rate throughput was demonstrated for short- to medium-range network latencies.

Gunter et al. [36] investigated the benefits of using parallel networks, one IP-based and one circuit-based network between two endpoints, to increase the overall throughput of GridFTP.

Vardoyan et al. [11] investigated file transfer bottlenecks in GridFTP especially when security processing is involved. They demonstrated a threaded mechanism with which multiple transfer threads can utilize system resources effectively for protocol and security processing.

While these projects have significantly contributed to the file transfer performance of GridFTP using various transport protocol techniques, our work in this paper utilizes programmable NICs for offloading CPU-intensive protocol processing to achieve higher performance and lower utilization. Besides improving the observed file transfer throughput over long-haul networks, this method increases the available processing power for the GridFTP server host, allowing for higher data handling and transfer capacity.

5 NIC Offload Engines

In this section, we discuss the design and implementation of the NIC offload engines that are used as the building blocks of our hardware offload solution.

5.1 UDT (Reliable UDP)

We have ported the UDT v4 implementation [37] to the SmartNIC, with substantial redesign and modifications to make it work efficiently on OCTEON processors. The NIC-resident firmware leverages the OCTEON's data flow processing model, its unique Schedule/Synchronization/Order (SSO) unit,

hardware timers (TIM), locking mechanisms, atomic operations, and network accelerator units for parallelization and workload partitioning [38]. Here we highlight some features of the implementation.

Network Processing of Data Flow: Figure 1 depicts the arrangement of the OCTEON cnMIPS64 cores for network stack processing, including UDT, SSL, and RDMA. Blue (dark) boxes represent OCTEON integer cores, which are logically divided into a send and a receive side. Note that 10 cores participate in different data-processing tasks, including send and receive, in a data-flow model. They process work requests submitted by the OCTEON Packet Order and Work (POW) unit [38] upon network or host arrival. The remaining 2 cores are dedicated for asynchronous sending of UDT messages and UDT timing control. The asynchronous arrangement of protocol processing steps increases the utilization of the OCTEON cores, resulting in higher capacity for packet processing, as rarely is a core left idling or polling for any synchronization.

At the send side, data are pushed by the host through a PCIe interface and the OCTEON's Packet Input Processing (PIP) unit [38]. Messages are then transferred using DMA to the card and placed on the UDT send queue of corresponding socket. A dedicated asynchronous send core transfers the UDT data (through the lower network layers) to the link. Pushing large data chunks using OCTEON's direct DMA mechanism ensures the lowest kernel overhead and host utilization at the sender side.

At the receive side, the UDT receive queue is integrated with the existing control loop that terminates incoming network data. Upon arrival and after initial checking and time management, any UDT packet is placed on the receive queue for delivery to the upper layer protocols. Each UDT socket has a callback function, which is invoked by using asynchronous timers after initial processing of the incoming data in the asynchronous receive. The callback function is assigned based on the upper layer protocol that receives the UDT data. The UDT protocol data (belonging to host-based UDT sockets) are pushed up to the host receive rings by this callback (using DMA, through OCTEON's Packet Output – PKO – engine [38]). Configurable interrupts generated by the NIC inform the host of the arrival of the UDT packets. The data are then placed into kernel socket buffers (SKBs), and a work queue entry is posted for the udt module to transfer the data to the user. RDMA data (discussed later) are handed to the Direct Data Placement (DDP) layer on the card, which are then directly transferred to user buffers using DMA.

Improved Parallelism: For protocols that require packet ordering (such as UDT in our case), data packets of a single stream at the card are normally ordered through OCTEON's atomic tags that impose strict ordering on scheduling packets to OCTEON cores. Atomic tags do not allow two packets of the same flow (tag) to be scheduled simultaneously to parallel cores. To improve parallelism in cases that part of the protocol processing on different packets can be performed in parallel, we employ other mechanisms to ensure ordering. For instance, we use internal sequence numbers along with parallel tags (instead of atomic tags) to impose ordering on parts that require sequential processing (e.g. pushing data to the host or to the network).

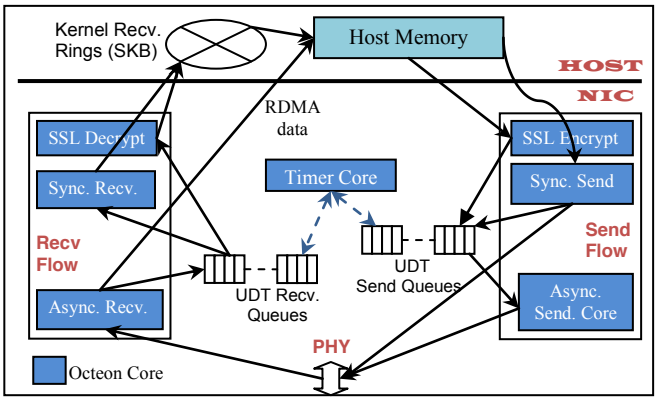


Figure 1- Transport offload packet processing dataflow on SmartNIC

Zero-Copy Processing: All protocol processing on the SmartNIC is performed without data copies (zero copy). There is no extra copy of packet data to the NIC buffers in any of the processing stages. The same packet buffers that are allocated by the hardware at ingress are re-chained and forwarded to the egress path. This approach saves a significant amount of processing power and latency, while allowing for line-rate throughput. At the host side, however, a travel through the kernel and a single copy is inevitable for UDT sockets, to transfer the data out of the kernel's socket buffers into the user space and vice versa (for non-RDMA cases).

Backpressure Flow Control: We use several backpressure mechanisms at the NIC to control the flow of data between the NIC UDT engine and the host side driver. To prevent flooding the NIC engine by the sender, the number of active NIC-side packet buffers is limited to a configurable value. This is usually set to a fraction of the total NIC-side packet buffers and will affect traffic from all flows. This parameter, along with the total number of packet buffers need to be increased to better support higher network latencies, due to higher number of outstanding packets in those situations.

At the receiver side, to prevent flooding the host kernel, we limit the total size of data outstanding packets for host side UDT driver handling. Card side data needs to wait for the backpressure to clear, reflecting the congestion to the UDT stack on the NIC, and further to the sender.

5.2 iWARP and RDMA

A version of iWARP that runs over UDT has been designed for the SmartNIC. The RDMA Protocol (RDMAP) and DDP layers are offloaded to the card, while the verbs layer, including queue pair processing, is partially managed at the host. iWARP over offloaded UDT is expected to use less system resources due to relying on RDMA semantics.

DDP Layer: DDP enables a ULP to send data to a data sink without requiring an intermediate buffering. A TCP-based DDP is typically implemented on top of the Marker PDU Aligned (MPA) framing protocol. One can, however, implement DDP on top of any protocol that follows the reliability requirements set forth in RFC 5041 [39]. Using datagram-based UDT releases the requirement of using the MPA layer, thus removing both latency and bandwidth overheads associated with using markers. DDP uses special callbacks for asynchronous transfer of data to the host using OCTEON's DMA engines, to help increase the overall capacity of the NIC.

RDMAP Layer: The RDMAP firmware is layered immediately on top of offloaded DDP. RDMAP relies on the DDP layer for transmission and placement of data into tagged buffers and the passing of untagged buffers. This implementation of RDMAP is based on the specification described by RFC 4296 [40]. RDMAP is generally a thin layer that uses DDP header fields for different RDMA operations. Our current implementation of RDMAP supports RDMA send, receive, and write operations; and we are planning to add RDMA read operation in the future.

iWARP Driver Implementation: The host driver implementation is compatible with the Open Fabrics Alliance's OFED [41]. The SoftiWARP software stack [42] is used as a base for our host-side iWARP support, in which the software processing of RDMAP and DDP are bypassed to the card. We have modified the base OFED SoftiWARP software to support the SmartNIC offload engines.

RDMA Connection Manager (CM) is an essential piece to transparently support IB and iWARP networks using the same OFED API. SoftiWARP implements a version of RDMA CM that utilizes TCP/IP. We have modified this implementation to perform over the offloaded UDT protocol. When establishing an RDMA connection, a pre-connected UDT socket that has been offloaded to the card is registered to the RDMAP and DDP layers. At this stage the socket is considered to be "in RDMA mode" and all its data will be processed by the NIC-resident iWARP layers.

5.3 Security Offload using OpenSSL

Cavium has provided the core functionality for card-side SSL, in an API that offers base functions for context management, encryption/decryption, digital signature, and so forth, utilizing OCTEON's per-core security coprocessors. The base functions are inefficient, since they essentially use only one OCTEON core and perform several data copies, causing the overall throughput to be very low. The low performance

is partially due to the library's internal mechanism for handling SSL sequence numbers that is non-scalable and serializes the process, not allowing multiple blocks to be processed in parallel.

We have created an API that utilizes this functionality for OpenSSL security processing in an asynchronous data-flow fashion on the NIC. We have modified the library to handle SSL sequence numbers outside of the core functions, using OCTEON's powerful atomic operations. With this redesign, since we can concurrently utilize up to 10 cores on the 12-core OCTEON processor for a single SSL stream, we are able to fully parallelize the cryptography process on independent blocks, increasing the observed throughput by eight to nine times.

We have further improved the SSL offload performance by removing data copies and by enabling concurrency in SSL API processing on the card. We have eliminated memory copies that happen on the card, both in our library and in Cavium's base crypto library. Note that the inherent data touch in the security processing is inevitable, where the data is read from the source packet buffer, and the encrypted/decrypted data is written into the destination packet buffer. We also use cache prefetching and aligned buffers to improve the memory access time for the one-time data read/write. Prefetching and buffer alignment further improves the SSL throughput by ~20%.

To reduce firmware complexity, we keep the buffer size equal to a packet buffer size (currently near 8-9 KB). This helps avoid extra copies for moving the entire data in an intermediate contiguous buffer for cryptography, as the card side packet buffers belonging to a ULP data unit are not necessarily contiguous in memory.

5.4 GridFTP over NIC Offload Engines

To enable the Globus GridFTP to use the offloaded transport protocols, we use XIO (eXtendible Input/Output System) drivers. XIO in GridFTP [20] provides a modular framework in which different communication protocols can be utilized by using a common standard interface supporting operations such as open/close/read/write. The XIO interface connects to the communication protocol using a driver structure, in which multiple drivers for supported protocols can be used by the GridFTP server/client.

We have used two XIO drivers for UDT and RDMA. The UDT XIO driver in GridFTP works on top of NIC-resident UDT offload engine. For RDMA, we have used the XIO-RDMA driver developed as part of Phoebus project [34], as the base for our iWARP (RDMA) XIO driver on top of the SmartNIC iWARP stack. This driver uses multiple threads at the server and client sides for performing RDMA operations. RDMA Send/Recv operations are used for control purposes. We have modified this driver to support both Send/Recv and RDMA Write for data transfer.

A socket-based channel is used in the original RDMA XIO driver for handshaking and control. Since RDMA Write operations are one-sided, the server and client need a separate mechanism to inform the data sink (receiver) of the completion of an RDMA Write operation. The control messages need to complete only *after* the data transfer is completed on the RDMA write channel. The original driver uses a separate non-RDMA socket for control-channel completion notifications. We have moved the control messages over to the RDMA channel to allow for guaranteed completion sequence. In fact, two mechanisms are used for completion checking:

- Destination buffer checking/polling (using a small header and footer around the data in XIO driver's internal SLAB buffers). These checks are intermittent, in order to avoid high CPU utilization.
- Two-sided (Send/Recv) RDMA operations to inform the data sink of a completed RDMA Write (recommended by the standard).

At completion, the data sink sends a TCP or RDMA Send/Recv based receipt to the data source, indicating the full reception of data, so that source OS SLAB buffer resources can be freed. Our performance results are best when using the first completion method; however, this method is non-standard and may not be used on systems where memory placement of network data can be out-of-order.

For increased performance, we also modified the XIO driver to send larger data chunks of up to a certain size to the card. The best RDMA performance on SmartNIC is achieved when the data is pushed from the host (using the scatter/gather-based DMA capabilities of OCTEON's IPD/PIP unit) instead of

asking the OCTEON cores to initiate DMA operations. However, such data-pushing operations are limited to no more than 14 user buffers; and since we are dealing with 4 KB user pages as the largest buffer size, our DMA size is limited to less than 56 KB. Note that this does not negatively affect performance or functionality and is completely opaque to the GridFTP user.

6 Experimental Evaluation

To evaluate the offload engines and their benefits for throughput and host CPU utilization, we begin with some microbenchmark results for several offload engines, including UDT, RDMA, and SSL offload. We then present GridFTP results over an emulated long-delay network. To further verify our findings, we also present results for GridFTP over UDT on a long-haul 10GbE link reservation on ESNet.

6.1 Experimental Platforms

We have evaluated our implementation on two distinct platforms:

RNET_P is a cluster, each node with two quad-core Intel Xeon E5620 processors running at 2.4 GHz and 12 GB of RAM. The nodes run CentOS 5.3, Linux kernel 2.6.18-128. We use synthetic delay insertion on a middle node to emulate a long-haul network with various latency numbers.

UC_P is a two-node configuration at the Argonne/University of Chicago Computation Institute. Each node has a quad-core Intel Xeon E5504 processor running at 2 GHz, with 4 MB of cache. The nodes have 4 GB of RAM and run RedHat Enterprise Linux 6 with kernel version 2.6.32-358. We use an OSCARS [43] 10GbE virtual circuit reservation over ESNet, when using this platform (our tests are performed on various link configurations with different latencies ranging from 84ms to 104ms).

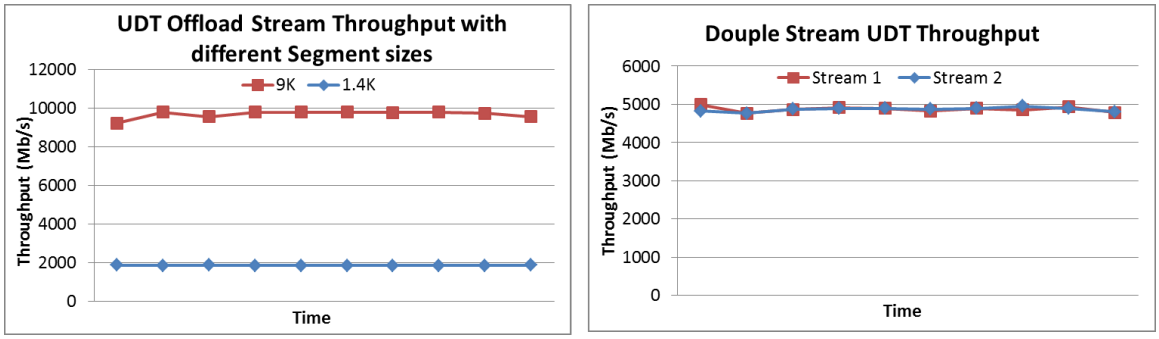
On both platforms, each node has an RNET 10GbE card (SmartNIC) installed in an x8 PCIe slot. We turn off CPU frequency scaling and processor c-state switching to allow for maximum host performance. Interrupt coalescing is also enabled for the cards.

6.2 Microbenchmark Results over the LAN

In this section, we present results from benchmarking the UDT, RDMA and SSL offload engines using simple throughput microbenchmarks.

6.2.1 *UDT Offload Engine*

In the UDT microbenchmark, a one-way stream of messages is transferred from one node to the other node, and a final small acknowledgment is sent back. The card is set to coalesce interrupts in order to reduce the burden on the host, an approach that has a positive effect on throughput. Jumbo frames are used, and the user-level API chunks the data into 16 KB pieces. The UDT offload single-stream test can reach over 9.9 Gbps. Figure 2 shows the results for UDT throughput benchmark with no inserted delay. Figure 2 (a) compares small segment sizes (1.4K) with jumbo segment sizes (9K) and Figure 2 (b) shows how two concurrent UDT streams share the bandwidth.



(a) (b)

Figure 2- UDT Stream Throughput Benchmark Results

We use the *mpstat* tool in Linux to report CPU utilization. Based on the measurements, offloaded UDT requires nearly 50% of a core cycles at the sender side and nearly 80% of a CPU core cycles at the receiver side to reach line-rate throughput. The CPU utilization for UDT shows that at the sender side, CPU is consumed mostly in system calls, which are due to the UDT driver handling send requests. At the receiver side, the majority of the consumed CPU cycles are in soft IRQ (interrupt), processing the arrived data. We have moved a significant portion of the host-side processing to the interrupt bottom half (soft IRQ), where the socket buffers containing arrived data are posted into work queues for user-space processing. Eliminating multiple sender-side and receiver-side copies at the host has helped reduce the overall CPU utilization. Most of the CPU utilizations observed are due to kernel-space socket buffer processing and polling activities to check for the availability of data; no UDT protocol processing occurs at the host side.

6.2.2 *RDMA Engine*

To measure RDMA throughput, we use RDMA Write (memory semantics) in a one-way stream of data, terminated by a final acknowledgment message from the receiver. The observed one-way streaming throughput is over 9.8 Gbps; and the CPU utilization is low, due to using offloaded processing and direct memory transfer into user-space buffers. The sender-side CPU utilization is ~11% of a CPU core, spent mostly in system calls to post data to the card. The receiver-side CPU utilization is negligible (less than 2% of a CPU core), since no CPU involvement is required in order to place the data in user space. The small amount of CPU cycles consumed is to periodically check the arrival of data.

6.2.3 SSL Offload Engine

For benchmarking the SSL offload, we have used an emulated HTTPS server/client, where a one-way stream of HTTPS data is sent and finalized by a final response from the receiver to end the session. Using this benchmark with no inserted delay, we observe 6.2 Gbps throughput in a single flow of OpenSSL-based data transfer (Figure 3).

The main reason for not reaching line rate in full OpenSSL processing on the card is OCTEON's relatively low performance of MD5 hash processing. The MD5 engine performance is about 1.3 Gbps per core. If we skip the card-side MD5 calculations (and leave the final integrity check of the file data to the host), the card's capacity will increase, allowing the total single-stream observed throughput to reach 9.9 Gbps using offloaded encryption. With the next generation 4-port SmartNIC, which is based on a 32-core OCTEON II processor at 1.2 GHz, up to 27 Gbps of full OpenSSL throughput is expected.

Undoubtedly, the main benefit for SSL offload is reduced host CPU utilization. The offloaded SSL implementation consumes no more CPU cycles than does a UDT offload (essentially getting free security processing on top of UDT). This is due to the fact that SSL data essentially pass through the same data path as UDT data, with little extra processing. We discuss OpenSSL CPU utilization in the next section, where we also present long-delay network results.

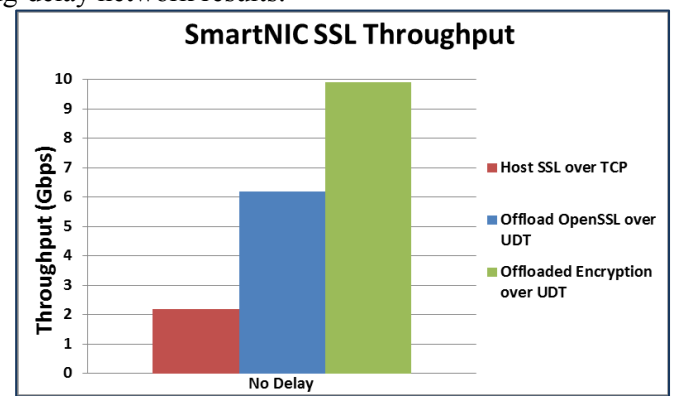


Figure 3- SSL offload vs. host throughput

6.3 GridFTP with Offload Support

Here we present the results for benchmarking Globus GridFTP over SmartNIC offload engines. We run our benchmark with and without an injected network delay. We focus on GridFTP throughput and CPU utilization when using UDT and RDMA. OpenSSL integration with GridFTP is underway and here we present microbenchmark results for OpenSSL implementation.

The TCP stack is tuned based on the TCP performance tuning recommendations by ESNet [44]. We also use 4MB GridFTP block sizes, jumbo frames, and HTCP congestion control (HTCP shows a better throughput over long delays). For host-based UDT, 8 KB maximum segment sizes are used, similar to those for the offloaded UDT (this is different from upper layer block sizes of 4 MB used for the GridFTP software).

No-Delay Network Results: We first measured GridFTP memory-to-memory file transfer throughput over the local-area network with a ~250 µs RTT. The leftmost side of Figure 4 shows the observed throughput for the local-area network with no injected delay. Except for host-based UDT, other transports are able to saturate the link. While host-based UDT takes close to a minute to reach its maximum throughput, offload UDT takes only about 5 seconds.

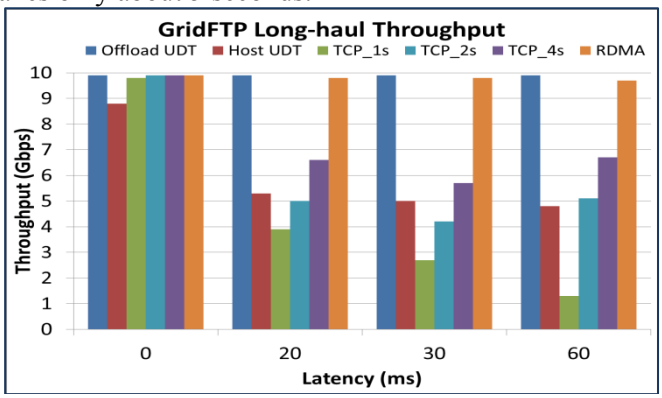


Figure 4- GridFTP throughput over emulated long-delay network

Figure 5 shows CPU utilization at sender (data source) and receiver (data sink) of a GridFTP file transfer session, when using any of the above transports. We measure the total host CPU utilization and calculate the average amount of one CPU core power that is utilized for 1 Gbps of throughput, when there is no inserted latency. As expected, RDMA transport shows the lowest CPU utilization for GridFTP, because of little CPU involvement in the data transfer (especially at the receiver side). After RDMA, the UDT offload shows the next lowest CPU utilization at both sides. Clearly, host-side UDT has the highest CPU utilization. Regardless of how we bind the processes to CPU cores, one core is completely saturated in the host UDT case; hence lower than line-rate bandwidth is observed for host-side UDT.

Table 1 shows CPU usage breakdown for various transports (shown as percentage of the total CPU utilization). The numbers are presented for both sender-side and receiver-side. The majority of offloaded UDT time is spent in system calls; negligible time is spent in user space. In contrast, the host UDT spends a large portion of its sender time in user space. At the receiver side, a significant part of offload UDT processing is spent in IRQ processing, which includes checking for data arrival and handling packet buffers. We plan to improve this portion by using kernel-assisted wait queues and reduced kernel-side polling. For TCP, most of the processing at both sides occurs in system calls and soft IRQ processing.

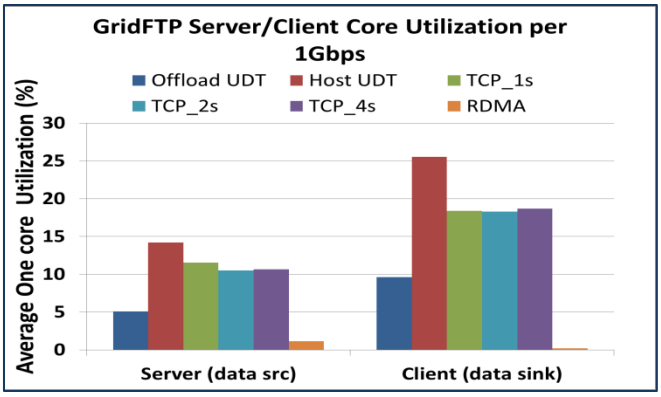


Figure 5- GridFTP CPU utilization with different transports over RNET_P

Table 1- CPU usage breakdown for TCP and UDT

GridFTP Transport		IRQ %	Syscall %	User space%	Total %
TCP (4 stream)	Sender	45	51	4	100
	Receiver	60	35	5	100
Host UDT	Sender	21	31	48	100
	Receiver	43	16	41	100
Offload UDT	Sender	16	81	3	100
	Receiver	71	26	3	100

Long-Delay Network Setup: One of the major expected benefits of using UDT as the underlying transport is its ability to sustain high throughput over long-haul networks, that is, wide-area networks with high latencies. To examine the GridFTP performance with various network latencies, we used an emulated long-delay network, where the latency is induced by a middle node, as depicted in Figure 6. The middle node has a two-port OCTEON Plus 10GbE card and uses OCTEON's timers to schedule each arrived message at its ingress port for forwarding to the egress port at a requested future time, inducing an artificial delay. This method of injecting artificial delay also introduces a small amount of jitter, where less than 10% of the packets experience a delay variance higher than 20%. In addition, the setup also introduces a mild (less than 10%) packet reordering. These characteristics are reasonable for emulating a long-haul scientific network, where mild jitter and reordering are expected. Little to no packet drops are present in this experiment. We examine the effects of various packet drop scenarios using artificial packet drops in Section 6.4.

In order to sustain line rate, the middle node needs to have buffering space for the bandwidth-delay product of the network. For example, for 10 ms latency, we need to have $10ms * 10Gbps = 100Mb$ of buffering. This includes buffering at the UDT protocol level, mainly the space for the arrived packet buffers, before being forwarded. Due to the elimination of card side copies for UDT processing, the only data-dependent buffering required is the amount of packet buffers held in buffers during the delay. For RDMA to be able to sustain this capability, besides UDT's internal buffering, we also need to have enough buffering at the XIO driver's SLAB interface for a message's acknowledgment to arrive from receiver to sender, before we discard the message at the sender side. For this purpose, we use a SLAB buffer with 2,048 partitions of up to 64 KB each.

Figure 4 presents the GridFTP file transfer throughput over several transports using various injected delays. The delays are shown as round-trip latencies in milliseconds and the performance is the maximum

reached (over a long window to ensure stability). Offloaded UDT and RDMA offer a sustained near-line-rate throughput for GridFTP. UDT offload takes less than five seconds to saturate the link. On the other hand, host-based UDT suffers significantly from long delays.

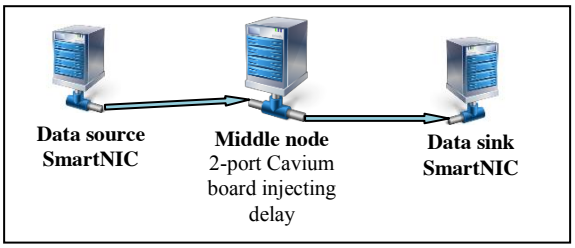
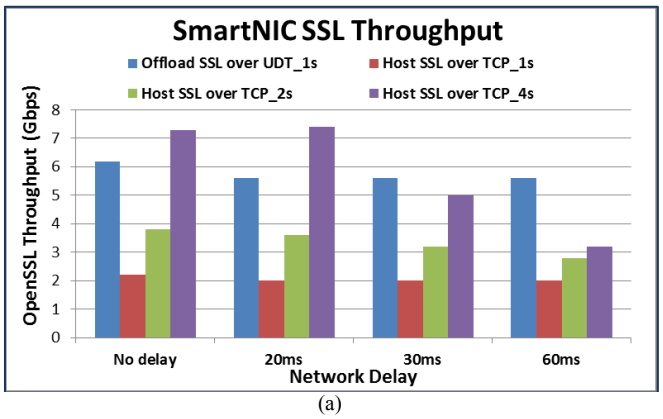


Figure 6- Injecting long delays to emulate a long-haul network

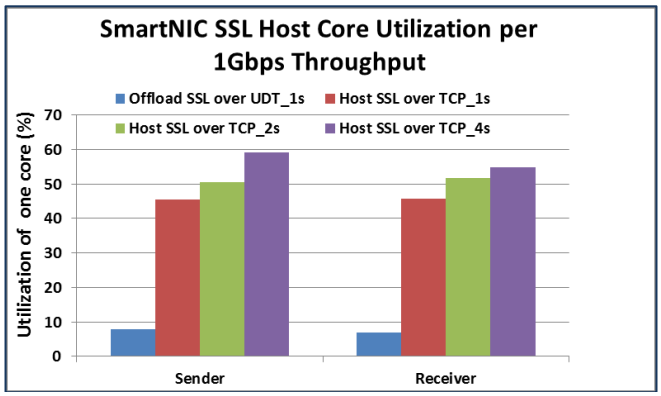
OpenSSL Results over Long-Delay Network: We run the OpenSSL microbenchmark over the emulated long-delay network, with the aim of comparing throughput and CPU utilization for the single-stream offloaded OpenSSL with host-based OpenSSL over TCP when using multiple TCP streams. For the multi-stream TCP test, we use multiple independent process pairs and utilize MPI calls for synchronization. We run the test for multiple consecutive windows (transferring near 1 GB of data in each window), until the throughput converges. For TCP we use 1 MB buffer sizes.

In Figure 7(a) we present the throughput under various network delays. As one can see, multi-stream TCP-based SSL can do better than single-stream offloaded SSL over UDT in a no-delay case. The reason is that the processing power of the current-generation SmartNIC limits the throughput to about 6.2 Gbps of OpenSSL + UDT, as also shown in Figure 3. As the network delay increases, however, while there is about 10% drop in SSL offload throughput (sustained regardless of the amount of network latency), the multi-stream TCP throughput drops more dramatically, falling by up to 50% to about 3 Gbps.

Figure 7 (b), which depicts normalized CPU utilization, clearly shows that using offloaded SSL can significantly benefit host CPU usage, decreasing it by 80% or higher (compared to host-based SSL). Using two SmartNIC boards, we can expect near twice the observed offload performance (over 11 Gbps) over a long-delay network while utilizing the cycles of less than two host cores (less than 20% of the total host CPU cycles). Conversely, for a host-based solution to achieve 10 Gbps, the power of 5 to 6 host cores (~70% of the total host power in our RNET_P platform) is required, when Intel AES-NI acceleration instructions are also utilized.



(a)



(b)

Figure 7- SSL streaming performance over long-delay network: (a) throughput, (b) host core utilization per 1 Gbps throughput

6.4 Effect of Packet Loss

Long-haul data transfers, especially those going over inter-continental connections are prone to packet drops, due to the effect of link congestion and bandwidth over-subscription [45]. We have examined the effect of packet loss on the throughput of GridFTP data transfers over long-latency networks. To be able to inject various packet drop rates with various network latencies, we have conducted our tests on the RNET_P platform, where a dual-port OCTEON-based board in the middle-node (that is responsible for injecting delays and emulating a long-latency link) performs the synthetic packet drops. We have varied packet drop rates from 0.01% to 5%. Some of these values are representative of real-world network packet-loss rates over the Internet. While some of the extreme conditions may not typically be present on high-performance science networks such as ESNet or Internet2, the results can still help to understand the capabilities of the transport protocols in typical and extreme packet drops. A loss rate of 0.1% or less is similar to that of intra-North-America or North-America to Europe traffic, while a 0.5%-1% loss rate is in line with expectations of loss between North-America to much of Asia. Larger packet loss rates of 1–5% approximate traffic from U.S. to Africa [46].

Figure 8 shows the results for various loss rates and different network latencies, from 20ms to 60ms. As it can be observed, while both protocols are suffering considerable drop in throughput, UDT offload sustains a significantly higher throughput compared to TCP, especially in high error rates. UDT behavior also shows less sensitivity to the inserted latency in the presence of packet loss. This is something that is not observed for TCP. While the high-loss-rate TCP results depicted in Figure 8 may not highlight this fact, due to their small magnitude, looking at the actual TCP numbers shows significantly lower throughput for higher round-trip latencies, given the same packet drop rate. This could be attributed to the sensitivity of TCP's BW recovery to the network latency. UDT, on the other hand, is able to recover faster and with less sensitivity to the long-haul latency, due to its complex bandwidth estimation and congestion management algorithm.

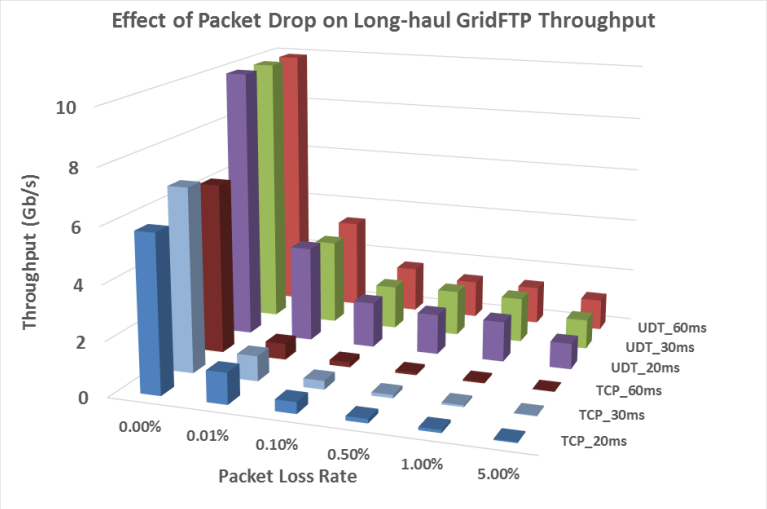


Figure 8- Effect of various packet loss rates on GridFTP throughput over TCP and UDT over various long delays on RNET_P

6.5 GridFTP Results over ESNet Long-Haul Connection

To verify the benefits of using offloaded UDT-based file-transfer over real scientific networks, we examined our implementation on a long-haul scientific grid connection over ESNet. ESNet is a high-performance grid network run by the U.S. Department of Energy, connecting hundreds of research institutions to facilitate scientific collaborations [18]. As shown in Figure 9, in this setup the UC_P platform nodes at the University of Chicago are connected through a loop-back long-range network that travels through ESNet to a switch residing at NERSC in Berkeley, California. The total round-trip latency is ~96 ms.

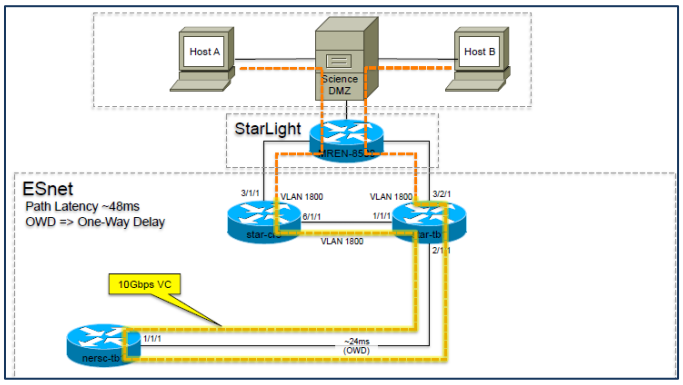
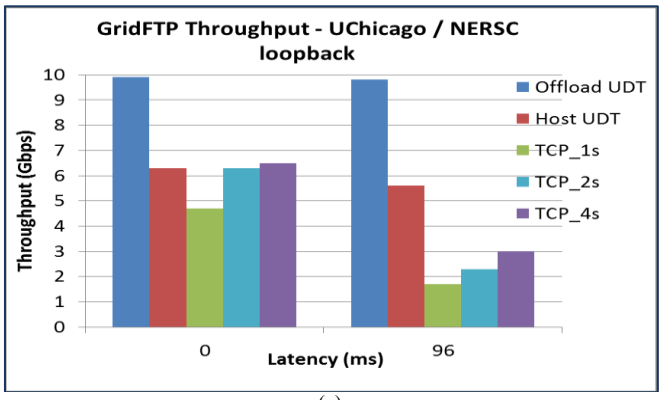
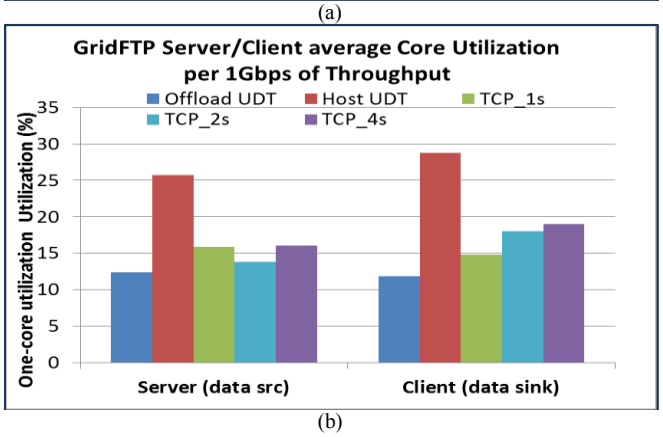


Figure 9- Long-haul loopback network set-up at UChicago through ESNet



(a)



(b)

Figure 10- GridFTP test results on UC_P: (a) maximum throughput on back-to-back connection and ESNet long-haul network, (b) CPU utilization

Figure 10 (a) shows the maximum observed throughput of GridFTP file transfer using offloaded UDT, compared with that of host-based TCP and UDT. The results are presented for both a back-to-back connection and the long-haul network. TCP performance tuning similar to those on the RNET_P platform were performed. We can see that offloaded UDT can nearly saturate the link under high latency. Our initial investigations show that the link introduces jitter and some mild reordering of packets, which partially contribute to TCP's poor performance. The amount of packet drops over this link is negligible. As we will see later, more deep NIC-level tuning may also help TCP performance.

Increasing UDT's internal bandwidth estimation window as well as increasing the next packet expected time improved its sustained throughput and helped offloaded UDT avoid fluctuations in the observed throughput. We note that such parameters should be adjusted based on network conditions (such as RTT and jitter), which could be a potential burden on the user.

Despite the tuning efforts, host-based UDT is unable to saturate the link in either case (no-delay and long delay). However, it still shows significantly less throughput drop compared to that of TCP, when going from no-delay (back-to-back connection) to the long-haul network. The reason both host-based UDT and TCP are not able to saturate the link even when the nodes are connected back-to-back is that the UC_P nodes are less capable than the RNET_P nodes and the interrupt processing fully saturates a CPU core. This situation results despite using interrupt coalescing, larger packet push-up from the card (4 KB instead of 1536 B), presence of the IRQ-balance tool (to spread IRQ processing), and jumbo frames.

Figure 10 (b) shows the CPU utilization of UDT and TCP on UC_P, in terms of the utilization of one CPU core per 1 Gbps of data transfer throughput. The measurements show twofold to threefold improvement in the CPU utilization when using the offloaded vs. host-based UDT.

To have a better insight into the transient behavior of these transport protocols on the long-haul connection, we also examined the observed instantaneous file transfer throughput (as reported by

GridFTP) over a 100-second window (Figure 11). In this window of time, offloaded UDT is able to transfer over 113 GB of data, whereas the amount for host-based UDT and 4-stream TCP is only 59 GB and 24 GB, respectively. We also observe that GridFTP over offloaded UDT is able to sustain the maximum throughput over the entire run. On the other hand, significant throughput variation is observed for host-based transfers.

The initial warm-up time for offloaded UDT, as observed in the beginning of the graph, is longer than expected, particularly longer than what we observe on the RNET_P platform, as reported in Section 6.6.3. The cause is most probably related to the UDT parameter settings, which we are investigating.

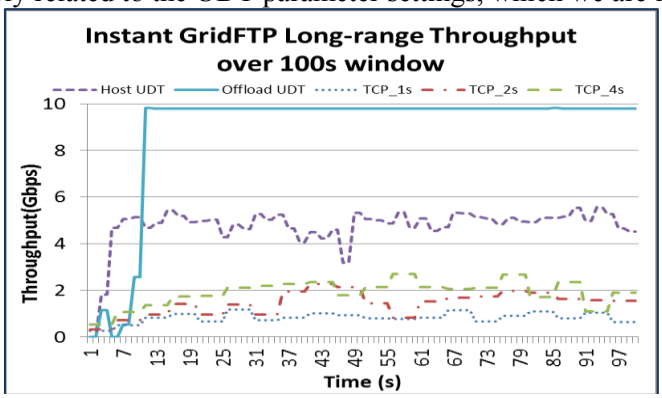
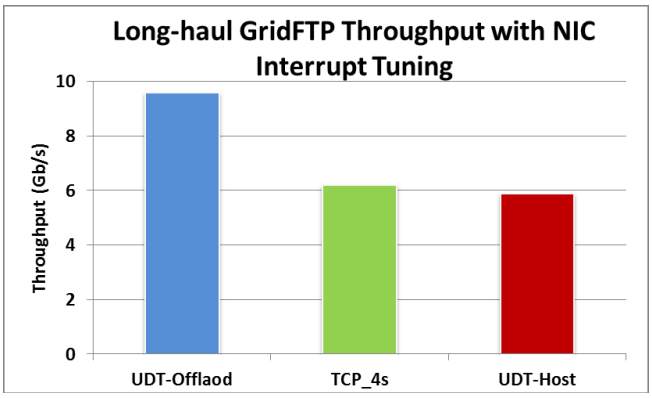


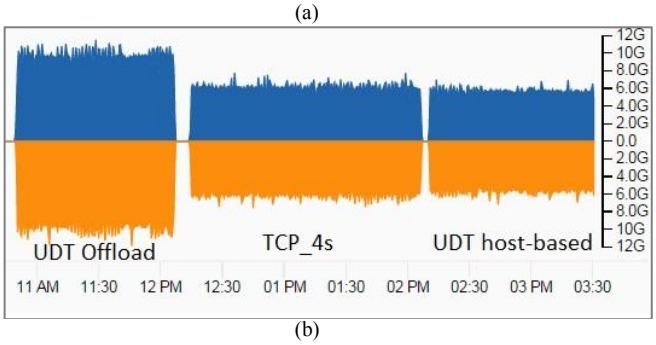
Figure 11- Time-series throughput for GridFTP long-range file transfer over a 100- second window of data transfer

As pointed out above, a rather unexpected observation in the UC_P results is TCP's throughput being much lower than the observed throughput on RNET_P platform. During demonstrations at two venues, including the supercomputing conference (SC14) we performed further investigation and tuning of the end points. Particularly, one parameter that showed the most significant effect is the number of packets being processed by every interrupt by the driver. The NIC driver defaults to 64 packets per interrupt, equal to the NIC side's interrupt coalescing parameter. In cases of bursty traffic (e.g., in multi-hop long-haul networks), more data packets than expected may arrive during the time that the interrupt is delivered to the CPU. Thus, setting a higher value at the host side will increase the driver's ability to handle bursts.

This NIC-specific tuning increases host-side packet processing per interrupt, allowing for the host to handle more data with the same interrupt. Increasing the value to 256 packets per interrupt dramatically improves the observed throughput for host-based TCP, which is entirely processed in the OS kernel and is sensitive to traffic bursts. The above mentioned demonstrations were performed on two long-haul connections, with 104ms and 84ms RTT. The results shown in Figure 12 are for the connection with 84ms used during SC14. Comparing with the results in Figure 10, the host-based four-stream TCP throughput is almost doubled, while host-based UDT is also slightly improved, both getting closer to the no-delay case.



(a)



(b)

Figure 12- Long-haul GridFTP throughput on SmartNIC with further tuning host-based protocols; Connection over ESNet (84ms RTT link over ESNet main network during SC14); (a) observed GridFTP throughput, (b) screenshot of the actual demo time-series (by ESNet ESMOND)

7 Conclusions and Future Work

In this paper we demonstrated how long-haul secure and regular file transfers can benefit from using an alternative transport protocol such as UDT that is offloaded to the network interface card. The benchmarks show how the UDT protocol offload can reduce the processing burden on DTNs, allowing for more communication capacity or data-processing capability. GridFTP over offloaded UDT can achieve over 60% reduction in host CPU usage, while near line-rate throughput can be sustained over high-latency high-performance networks. Moreover, CPU usage can reach near-zero when utilizing RDMA over UDT. The results also show that high-throughput secure data transfer over long delays can be achieved by using SSL and UDT protocol offloading, while freeing the host processors for other purposes.

In addition to the above, many applications can now opt for secure data transfers, something significantly CPU-intensive when using a host-based approach on a DTN. The freed host resources can be utilized for various use-cases, including higher network capacity (using more network ports), data compression, and end-to-end file integrity check, which are resource-intensive tasks. Moreover, the observed reduction in DTN processing requirements (up to 6 times for the same throughput in the case of secure data transfers) can lead to significantly smaller or fewer DTNs.

In our future work, we plan to complete the integration of offload engines, particularly SSL, with a production GridFTP release. Moreover, we plan to examine the potential usage of OpenSSL over RDMA that could further reduce the observed host utilization. To further examine the benefits of network offloading for increasing the capacity of a data transfer node, we plan to utilize multiple SmartNIC (and next-generation SmartNIC II) cards, and simultaneously perform other operations on the DTN, such as file integrity check or data compression.

Acknowledgment

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