

Fast 3D-HEVC Depth Intra Coding Based on Boundary Continuity

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ABSTRACT The encoding format of the 3D extension of high efficiency video coding (3D-HEVC) consists of a multiview color texture and an associated depth map. Because of the unique characteristics of the depth map, advanced coding techniques are designed for depth map coding at the expense of computational complexity. In this paper, fast algorithms are conceived to accelerate the intra coding time of the depth map based on boundary continuity. First, the proposed fast prediction unit (PU) mode decision reduces the number of conventional intra prediction modes based on calculating the total sum of squares (TSS) of the PU boundaries. Second, the proposed fast depth modeling mode (DMM) decision makes use of the variances of the boundary pixels to determine the execution of the DMM. Third, the proposed coding unit (CU) early termination algorithm decides whether to further split the current CU by utilizing the thresholds of the TSS and the rate-distortion cost (RD-cost). The experimental results show that the proposed algorithm provides better performance in terms of coding speed and bitrate than the algorithm in previous work. The coding time of the depth map is reduced by 56.08%, while the Bjøntegaard delta bitrate (BD-BR) is only increased by 0.32% for the synthesis view.

INDEX TERMS 3D-HEVC, Depth Map, Intra Coding, Fast Algorithm, Boundary Continuity.

I. INTRODUCTION

With the requirement of video applications, video technology has become increasingly advanced in recent years. 3D videos have the ability to provide users with fantastic experiences in a stereoscopic world. The 3D extension of high efficiency video coding (3D-HEVC) [1],[2] is the newest coding standard for encoding 3D videos effectively. The video format of the 3D-HEVC encoder is multiview plus depth (MVD) [3]-[5]. 3D-HEVC is based on the quad-tree coding architecture of high efficiency video coding (HEVC), which comprises coding tree units (CTUs) with coding units (CUs), prediction units (PUs), and transformation units (TUs). HEVC is designed basically for texture coding. However, the depth map in the 3D-HEVC system has some features, such as large amounts of smooth regions and sharp edges, which are totally different from each other in terms of color texture. Therefore, the conventional HEVC encoders may not always maintain good coding efficiency when encoding depth maps that consist

of smooth regions or edges. For this reason, new coding tools have been created for depth map coding, such as the depth modeling mode (DMM) [6] and depth intra skip (DIS) [7]. The DMM in 3D-HEVC is intended to enhance the conventional intra coding by keeping the edge information and smoothing the unimportant regions. Fig.1 illustrates the system structure of 3D-HEVC [8].

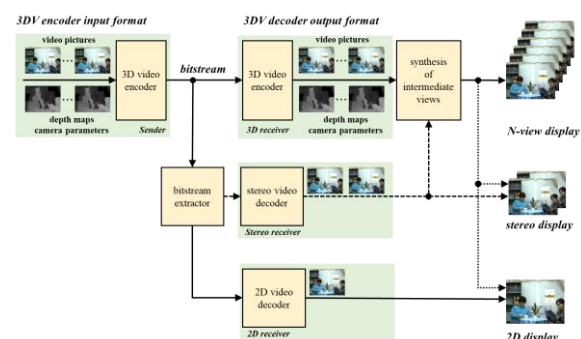


Fig. 1 System structure for 3D-HEVC [8].

Along with the evolution of the reference software, two DMM modes (DMM1 and DMM4) also evolved with the latest version of the 3D-HEVC encoder [8]. DMM1 is a Wedgelet partition while DMM4 is a contour partition. DMM1 (Wedgelet partition) uses a straight line to separate two regions, as shown in Fig. 2, where each region is represented by a constant value. Rather than checking the total Wedgelet partitions, the best Wedgelet partition mode is evaluated by estimating part of the possible cases with the refinement candidates. DMM4 (contour partition) divides the regions into two areas and represents each region with the same value by the threshold from the pixel values in the collocated texture block. The contour partition pattern is illustrated in Fig. 3. In addition, there are also several new coding tools for depth map coding. Segment-wise depth coding (SDC) represents the residual signal with a constant pixel value and replaces the traditional quantized discrete cosine transformation. Depth maps contain information for view synthesis rather than direct displays, so the original rate-distortion optimization (RDO) [9] method may not always be efficient for depth maps. View synthesis optimization (VSO) can take the quality of synthesized views into consideration during the calculation of RDO to improve depth map coding. In addition, several studies investigated how to further improve the compression efficiency or 3D video quality. Zhang *et al.* [10] proposed a full reference synthesized video quality metric and minimized the perceptual quality loss of the synthesized view by improving the RDO approach. Zhu *et al.* [11] conceived a convolutional neural network (CNN)-based VSO to elevate the coding performance. Shahriyar *et al.* [12] developed an independent depth map encoder with restricted quantization and proposed binary tree-based decomposition.

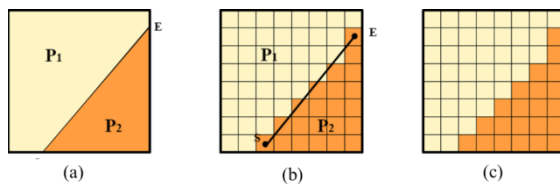


Fig. 2 The Wedgelet partition of DMM1 [8].

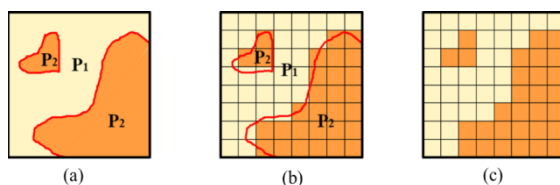


Fig. 3 The Contour partition of DMM4 [8].

The flowchart of the intra prediction of the depth map is demonstrated in Fig. 4. The conventional intra prediction performs a rough mode decision (RMD) from the planar mode, the DC mode and 33 kinds of angular modes to obtain the intra mode candidates. Then, the most probable modes (MPM) are added. Finally, DMMs are carried out and inserted into the candidate list for rate-distortion optimization (RDO). The

unique encoding techniques for depth maps contribute to the enhancement of the compression efficiency. However, the complicated encoding procedures in 3D-HEVC increase the coding complexity and coding time. Researchers have dedicated themselves to simplifying the encoder or reducing the depth coding time. Chung *et al.* [13] proposed a bitrate-savings scheme with a no-synthesis-error model and terminated the quad-tree coding structure early. Lei *et al.* [14] employed grayscale similarity of the depth map as well as interview dependency to perform a fast mode decision. Shen *et al.* [15] classified the CU complexity by weighting the intercomponent, interview and spatiotemporal coding information to select the corresponding intra prediction modes. Hamout and Elyousfi [16] simplified depth intra coding by extracting tensor features and data clustering. Sanchez *et al.* [17] found the positions of the eight highest gradients among the PU boundaries. Only DMMs with a start point or an end point passing through these positions were executed. Sanchez *et al.* [18] proposed the strong gradient-based mode one filter (S-GMOF) to detect the two boundaries with the highest gradients. If the start point and end point of the DMM1 partition were located at the two boundaries, the DMMs were executed. Otherwise, the modes in DMM 1 were ignored. Saldanha *et al.* [19] proposed single degree of freedom GMOF (SDF-GMOF) and double degree of freedom GMOF (DDF-GMOF) to quickly select the best positions to perform the prediction of DMM1. Fu *et al.* [20] separated the PU into two parts and defined the directions into four classes, including vertical, horizontal, and two diagonal (45° and 135°) directions. Then, the number of DMMs was reduced according to the variance and the direction. By using the correlation between the current PU and parent PUs, Zhang *et al.* [21] reduced the number of intra modes during RMD and determined whether to implement SDC. Hong *et al.* [22] analyzed the rate-distortion cost (RD-cost) distributions of both rough mode decisions and DMMs with the one-sided Chebyshev's inequality to decide the adjustable threshold for early termination. Gu *et al.* [23] analyzed the RD-cost of the RMD to estimate a threshold for the early termination of the following intra modes. Park [24] selectively skipped the unrelated partition of DMM1 by classifying the edge orientation of a PU. Zhang *et al.* [25] evaluated the smoothness of the current CU to bypass the DMM decision by referencing the collocated CU in the depth map and the corresponding CU in the color texture. Zhang *et al.* [26] replaced the view synthesis distortion (VSD)-based VSO with the proposed squared Euclidean distance of variances (SEVD) to diminish the complexity of the DMM1 prediction. In addition, the probability-based early depth intra mode decision (PBED) in [26] skipped the DMM prediction and chose SDC as the only candidate for RDO if the minimum RD-cost of the RMD candidates was smaller than the threshold. Saldanha *et al.* [27] used specialized decision trees for I-frames, P-frames and B-frames to define 64×64 , 32×32 , and 16×16 partitions. The characteristics of the human visual system were utilized in [28]

to propose a fast algorithm based on visual perceptions for fast depth intra coding of 3D-HEVC. Shen *et al.* [29] designed an edge-aware prediction scheme to reduce the prediction error energy in blocks with arbitrary edge shapes and thus decrease the bitrate for the depth map. Shen *et al.* [30] presented a new set of edge-adaptive transforms as an alternative to the standard discrete cosine transform to provide significant bitrate reductions for depth map coding. Important contributions were made by [31]-[33] to intra predictions when edges are found. Our previous works [34]-[36] also contributed to the reduction of the depth coding time. In [34], we defined a tunable threshold based on boundary variance and proposed a fast DMM decision scheme. In [35], we terminated the CU partition early based on the total sum of squares to accelerate depth intra coding. Fast intra mode selection [36] was conceived to rapidly extract the intra prediction modes according to the total sum of squares. A complete framework is unified in this paper, which jointly considers coding properties at the CU and PU levels. In addition, more analysis and validation are provided to justify the proposed algorithm.

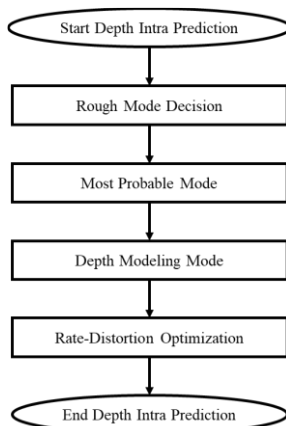


Fig. 4 The flowchart of the intra prediction on depth map.

Table 1. BD-BR and time-saving performance of the synthesized view with DMM disabled.

Sequence	BD-BR(%)	TS(%)
Kendo	2.25	25.6
Balloons	7.56	27.4
Newspaper	9.65	29.8
Poznan_Street	1.95	25.0
Poznan_Hall2	8.53	18.5
Shark	6.58	25.5
Undo_Dancer	7.80	21.8
GT_Fly	2.66	25.1
Average	5.87	24.8

Table 1 tabulates the coding performance of Bjøntegaard delta bitrate (BD-BR) and time saving (TS) with disabled DMMs. We know that the DMM evidently occupies the coding time and affects coding efficiency. Most of the previous works proposed fast DMM decisions to reduce the

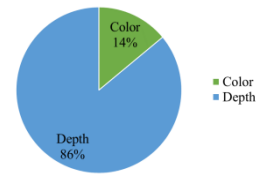


Fig. 5 Coding time distribution of color texture and depth map under all-intra configuration.

coding time and minimize the BD-BR loss. However, under the all-intra configuration, the computational time consumption of DMMs only accounts for 24.8% of the overall coding time, as shown in Table 1, whereas the depth coding time accounts for 86% for the all-intra configuration, as shown in Fig. 5. Consequently, depth coding time savings should be improved. In this paper, we propose three fast algorithms based on boundary continuity checks to accelerate the encoding procedures of depth intra coding. First, at the prediction unit (PU) level, we abridge the number of intra-angular modes and shrink the candidates for RMD. Second, the proposed fast DMM decision determines whether to execute DMM prediction by investigating the variance of each boundary and detecting whether there is an edge passing through it. Third, at the CU level, the CU early termination scheme is designed by inspecting the overall total sum of squares (TSS) of a CU and combining the limitation of the RD-cost.

II. PROPOSED METHOD

A. Observations from the Depth Map

Fig. 6 illustrates the depth map of the Shark sequence. Apparently, there exists a large quantity of sharp edges (blue squares in Fig. 6) and smooth areas (red squares in Fig. 6) within the depth map. Sharp edges are very likely to traverse the boundaries of the coding blocks. Studies also indicate that 3D-HEVC is prone to encode smooth areas with larger CU sizes. In this paper, we detect whether there is an edge passing through the current coding block based on the boundary continuity. Further CU depths without any edge are terminated early. Additionally, the intra prediction modes and DMMs are conditionally skipped to accelerate the depth map coding.

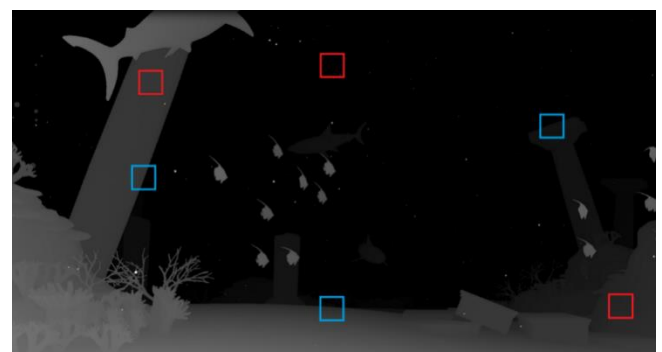


Fig. 6 The depth map of the Shark sequence.

B. Fast Intra mode Decision

For blocks with smooth contents, planar mode, DC mode, vertical mode and horizontal mode are most likely to be encoded as the best intra mode. Accordingly, pruning the number of intra prediction modes and the number of rough mode decision (RMD) candidates may contribute to coding time reductions.

1) Estimation of the boundary complexity

First, we aim to identify the smoothness. A general case of a sharp edge in the depth map is illustrated in Fig. 7. Sharp edges are formed by an obvious gap in pixel variation. In addition, the edge extends, so an obvious pixel variation gap also appears at the block boundary. Along with these properties, in this paper, we calculate the total sum of the squares (TSS) of each boundary by (1)-(2) to analyze the boundary complexity. TSS_k symbolizes the total sum of the squares on the k -th boundary. $pix_{k,i}$, $pix_{k,avg}$ and w denote the i -th pixel value, the average pixel value on the k -th boundary and the width of the current PU, respectively. Supposing that the values of TSS_k for each boundary are all less than or equal to a threshold (Th_{TSS}), as described in (3), there is probably no edge passing through, and the current coding PU is less complicated. Instead of searching all 35 intra modes, we may only perform planar, DC, vertical, and horizontal modes, which are frequently the best intra modes. The number of RMD candidates is reduced from 8 to 3 when the PU sizes are 8×8 and 4×4 . The flowchart of the fast intra mode decision is presented in Fig. 8.

$$TSS_k = \sum_{i=1}^w (pix_{k,i} - pix_{k,avg})^2, k \in \{1,2,3,4\} \quad (1)$$

$$pix_{k,avg} = \frac{1}{w} \sum_{i=1}^w pix_{k,i}, k \in \{1,2,3,4\} \quad (2)$$

$$TSS_k \leq Th_{TSS}, k \in \{1,2,3,4\} \quad (3)$$

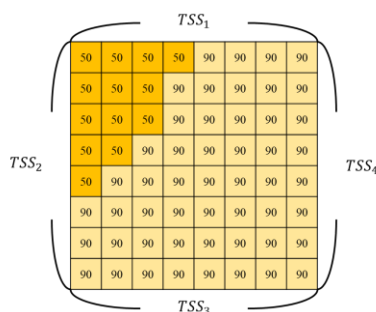


Fig. 7 Illustration of the TSS calculation.

2) Analysis and Selection of Th_{TSS}

To select a suitable threshold, we encode 100 frames for the Poznan_Street sequence with five Th_{TSS} values (0, 250, 500, 750, and 1000) and analyze the interaction between the depth coding time and the BD-BR performance of the synthesized view. Fig. 9 shows the statistical data of the Poznan_Street sequence. From the observation, the encoding time decreases as the threshold increases. However, the

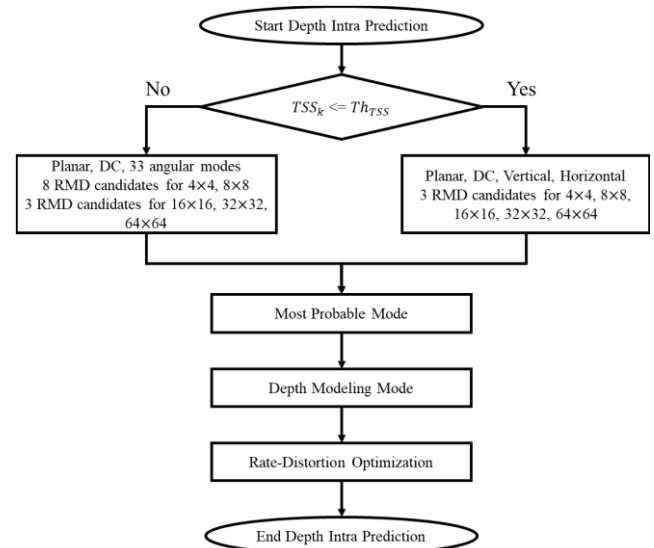


Fig. 8 The flowchart of the proposed fast intra mode decision.

larger the threshold is, the more significant the BD-BR degradation. We find that when the thresholds are larger than 250, the encoding time becomes stable, whereas the BD-BR continues to rise dramatically. The saturation point of the encoding time is approximately 250. To balance the coding time reduction and coding efficiency, we set Th_{TSS} to 250. When Th_{TSS} is set to 250 and the quantization parameter (QP) is set to 30, the hit-rate of the PU mode, which is correctly selected as a DC, planar, horizontal or vertical mode, achieves 95.51% on average and at least 91.94%, as shown in Table 2, which validates the threshold selection.

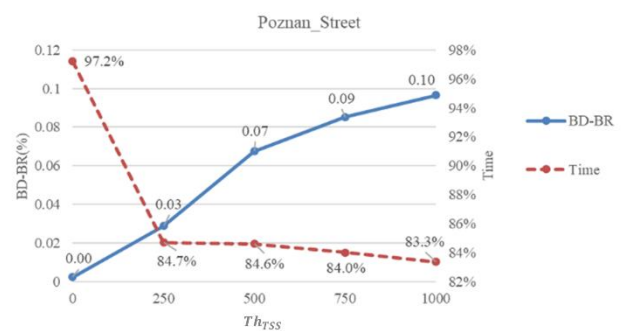


Fig. 9 Coding performance under various values of Th_{TSS} for the Poznan_Street sequence.

Table 2. Hit-rate of PU mode selected as a DC, planar, horizontal or vertical mode when Th_{TSS} is set to 250 and QP is set to 30.

Sequence	Hit-rate (%)
Kendo	96.69
Balloons	94.85
Newspaper	92.43
Poznan_Street	96.05
Poznan_Hall2	98.99
Shark	91.94
Undo_Dancer	98.04
GT_Fly	95.12
Average	95.51

C. Fast DMM Decision

With the evolution of the HTM encoder, DMM1 partition types are simplified from approximately 1000 kinds to 500 kinds. The simplified DMM and built-in fast criterion decrease the DMM execution time. However, according to the analysis in [26], the DMM still occupies approximately 20.25% of the depth coding time, as shown in Fig. 10. As a result, we consider that a fast DMM decision is desired to accelerate coding.

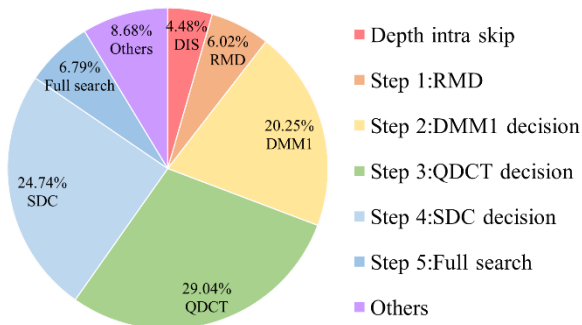


Fig. 10 Coding time distribution of the depth map [26].

1) Fast DMM Decision Based on Boundary Variance

The principle of DMM1 is separating a PU into two regions by a straight line and predicting each region with a single value. In other words, DMM1 is designed particularly for a PU with any edge dividing two smooth areas. As a result, if the current coding PU selects the DMM as the best mode, there is probably an edge passing through it. In the proposed fast DMM decision, we detect the edge by calculating the boundary variance.

We calculate the variance of each boundary within the current PU and sort them from the largest to the smallest as formulated in (5). Var is the set that includes the ordered variances of the k -th boundary (Var_k). Equations (6) and (7) formulate the calculation of Var_k . w is the boundary width, $Pix_{k,i}$ denotes the i -th pixel value on the k -th boundary, and $Pix_{k,avg}$ is the average pixel value of the k -th boundary.

$$Var = \{Var_k \mid Var_1 \geq Var_2 \geq Var_3 \geq Var_4, k = 1 \text{ to } 4\} \quad (5)$$

$$Var_k = \frac{1}{w} \sum_{i=1}^w (Pix_{k,i} - Pix_{k,avg})^2 \quad (6)$$

$$Pix_{k,avg} = \frac{1}{w} \sum_{i=1}^w Pix_{k,i} \quad (7)$$

$$(Var_1 + Var_2) - (Var_3 + Var_4) \leq Th_{var} \quad (8)$$

Among the four variances, Var_1 and Var_2 are denoted as the first two larger variances, while Var_3 and Var_4 are the other two smaller variances. Fig.11 illustrates an example of the four variances of the four boundaries. The difference between $(Var_1 + Var_2)$ and $(Var_3 + Var_4)$ is obvious, and

the DMM is highly possible in this case. To confirm whether the boundaries are smooth enough or too complex to skip the DMM, we calculate the difference between $(Var_1 + Var_2)$ and $(Var_3 + Var_4)$ by (8) and check whether the difference is less than or equal to a threshold (Th_{var}). If the statement of (8) is true, the DMM will not be executed because it means that there may be no edge or the edges are too complicated and the DMM may not be suitable for this situation.

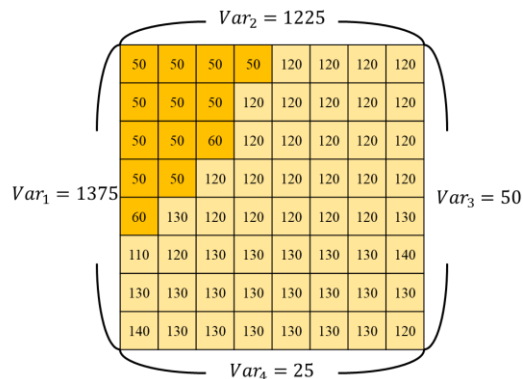


Fig. 11 Illustration of boundary variance calculation.

2) Analysis and Selection of Th_{var}

To analyze the performance of the proposed fast DMM decision under different values of Th_{var} , we encode 100 frames of each benchmark sequence (Kendo, Balloons, Newspaper, Poznan_Street, Poznan_Hall2, Shark, Undo_Dancer, and GT_Fly) with various Th_{var} values (1, 2, 3, 5, 10, 20, 30, and 50). The interaction among the BD-BR of the synthesized view, depth coding time, and Th_{var} are plotted in Fig. 12. According to Fig. 12, the BD-BR degradation is still climbing, and the curve of the depth coding time becomes smooth when Th_{var} is approximately 20~30. It seems that 20 or 30 is a suitable threshold value. However, we find that the BD-BR degrades too much when larger thresholds are used. To maintain the coding efficiency and avoid conflicts with other algorithms proposed by this paper, we decide to select a smaller threshold value. As a result, Th_{var} is set to 3 in this paper, and the flowchart of the proposed fast DMM decision is illustrated in Fig. 13.

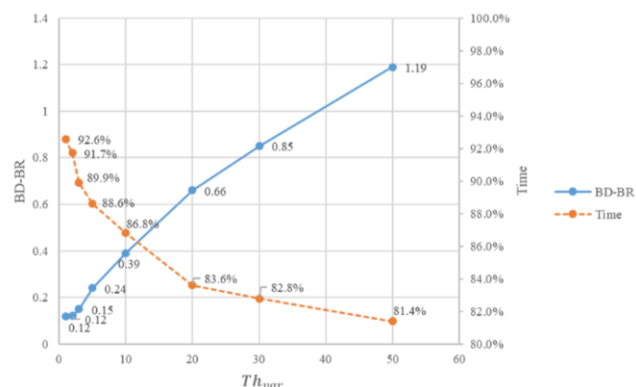


Fig. 12 The interaction among BD-BR of the synthesized view, depth coding time, and Th_{var} .

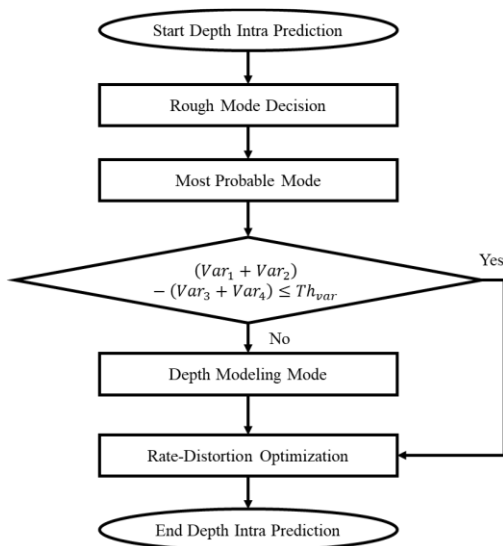


Fig. 13 The flowchart of the proposed fast DMM decision.

D. Fast CU Early Termination Algorithm with RD-cost Limitation

According to the analysis in [37], the encoding process of a CU depth of 0 only occupies 11.4% of the depth map coding time, as labeled in Fig. 14. In addition, CU depths of 1~3 accounted for 88.6% of the overall proportion. We further encode 100 frames for each benchmark sequence (Kendo, Balloons, Newspaper, Poznan_Street, Poznan_Hall2, Shark, Undo_Dancer, and GT_Fly) to estimate the probability distribution of each CU depth that is selected as the best CU depth, as shown in Fig. 15. Approximately 67.4% of the

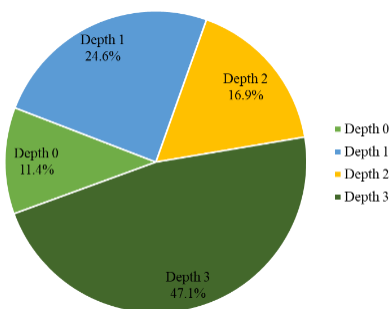


Fig. 14 The encoding time distribution of the depth map at each CU depth under QP45 (Quantization Parameter of 45) [37].

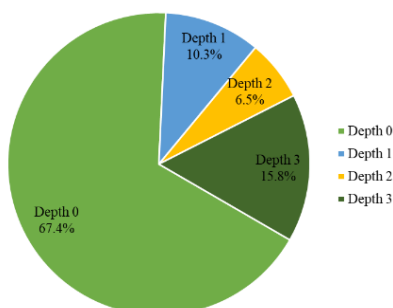


Fig. 15 The probability distribution of each CU depth which is selected as the best CU depth.

CTUs are partitioned at a depth of 0, whereas only 32.6% of the CTUs are split to CU depths of 1~3. In other words, the original encoder consumes much coding time at CU depths of 1~3, but most CTUs are partitioned at a CU depth of 0. Consequently, if we can determine the CU depth in advance and terminate the following CU encoding early, much time will be saved.

1) CU Early Termination Based on Total Sum of Squares

The 3D-HEVC encoder is prone to select smooth areas with larger CU sizes. In other words, unnecessary partitioning at further CU splitting is likely to be bypassed to speed up coding. First, we need to distinguish whether this area is smooth or if there is a complex edge. A sharp edge is formed by an obvious gap in pixel variation. In addition, the edge would extend, so an obvious pixel variation gap also appears at the block boundary. According to this observation, we calculate the TSS of each boundary (TSS_1 , TSS_2 , TSS_3 , and TSS_4) of the CU to estimate the pixel variation as in (1) and find the TSS_{total} with (9). If TSS_{total} is less than or equal to the threshold (Th_{total}), there may be no edge passing through it. The block will be denoted as a smooth region, and further partitioning for smaller CU sizes will be terminated early.

$$TSS_{total} = \sum_{k=1}^4 TSS_k \quad (9)$$

2) Analysis and Selection of Th_{total}

To find an appropriate threshold, we encode 100 frames of each benchmark sequence (Kendo, Balloons, Poznan_Hall2, and GT_Fly) with various Th_{total} values (0, 500, 1000, 1500, 2000, and 2500). We analyze the tendency of coding time and the BD-BR performance under different settings of Th_{total} , as shown in Fig. 16. The larger Th_{total} is, the more CU partitions will be terminated early, and more coding time savings can be achieved. However, it is also accompanied by an increased BD-BR. The encoding time continues to decrease as Th_{total} and the BD-BR increase. When Th_{total} is approximately 1000, the curve of the coding time reduction seems to be mitigated, and the BD-BR continues to degrade. Consequently, we set the Th_{total} to 1000.

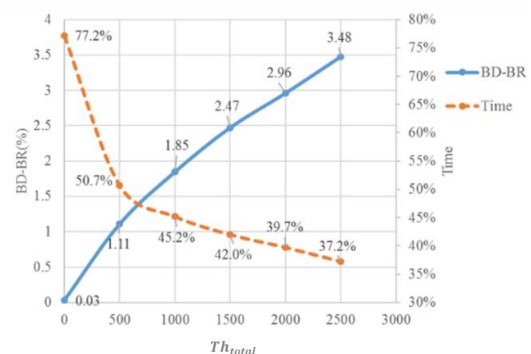


Fig. 16 The interaction among BD-BR of the synthesized view, depth coding time and Th_{total} .

3) Threshold Limitation Based on the RD-cost

Table 3 shows the detailed data of the BD-BR and TS performance when Th_{total} is set to 1000. We observe that the performance depends on the sequence, even though the average time saving is approximately 54.8% and the maximum time saving reaches 71.3%. For Poznan_Hall2, the BD-BR performance dramatically degrades to 5.21%, which means that this criterion is too rough to bring about the prediction error. Some kinds of CUs are smooth, but their RD-costs are extremely large, and it is not appropriate to terminate CU splitting early. To overcome this disadvantage, we incorporate the proposed CU early termination algorithm with an RD-cost limitation. If TSS_{total} is less than or equal to Th_{total} and the RD-cost of the best intra mode in the current CU depth (RD_{cur}) is less than or equal to the threshold of the RD-cost limitation (TH_{RD}), further CU splitting will be terminated early.

Table 3. Coding performance of the CU early termination algorithm when Th_{total} is set as 1000.

Sequences	BD-BR (%)	TS (%)
Kendo	0.83	49.6
Balloons	0.74	41.7
Poznan_Hall2	5.21	71.3
GT_Fly	0.60	56.4
Average	1.85	54.8

The range of the RD-cost varies for different QPs, so the threshold TH_{RD} should also be adaptively adjusted according to the QP variation. We collect the best RD-cost of each testing sequence under particular QP pairs and sort them from smallest to largest; the testing sequences and QP pairs are tabulated in Table 4. Furthermore, the RD costs on the order of 90% of every testing sequence under each specific QP pair, as illustrated in Fig. 17, are averaged and exploited to fit the regression curve portrayed in Fig. 18. The regression curve is formulated in (10), where QP denotes the texture QP in the QP pairs. Therefore, regardless of the QP, TH_{RD} will be adaptively fine-tuned, and we can obtain an appropriate value of TH_{RD} with (10). The flowchart of the complete fast CU early termination algorithm with RD-cost limitation is demonstrated in Fig. 19; $Depth_{CU}$ indicates the current CU depth.

Table 4 Testing sequences and QP pairs for the curve fitting of Th_{RD} .

Sequences	QP(color, depth)	Frames
Kendo, Balloons, Poznan_Hall2, GT_Fly	(25,34), (28,37), (30,39),	100
	(33,41), (35,42), (38,44),	
	(40,45), (43,47), (45,48),	
	(48,50), (51,51)	

$$Th_{RD} = 1.3729e^{0.199QP} \quad (10)$$



Fig. 17 Illustration of selecting the RD-cost threshold.

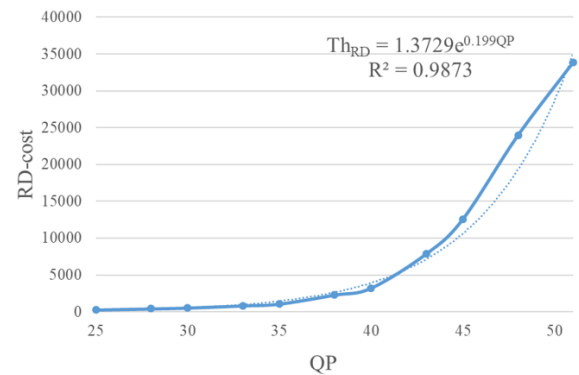


Fig. 18 The regression curve of RD-cost and QP.

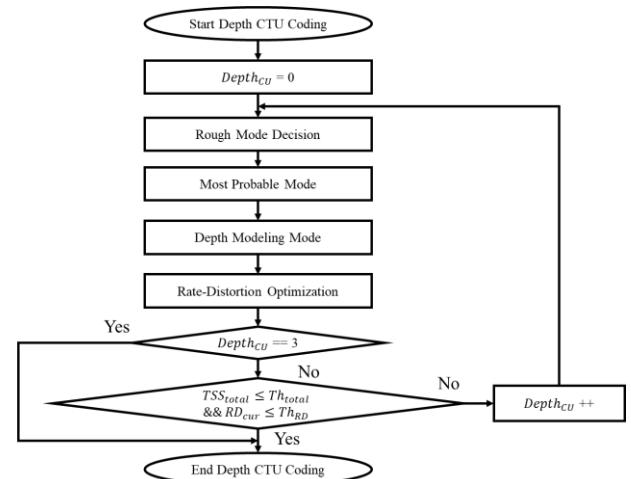


Fig. 19 The flowchart of the fast CU early termination algorithm with RD-cost limitation.

E. Overall Algorithm

The complete flowchart of the overall proposed algorithm is shown in Fig. 20 and includes the fast intra mode decision, fast DMM decision and fast CU early termination with the RD-cost limitation described in Section II.B to Section II.D. The detailed procedures are explained in the following.

- Step 1: Start depth CTU encoding.
- Step 2: Set current CU depth ($Depth_{CU}$) to 0.
- Step 3: Calculate TSS_k by (1)-(2), Var_k by (5)-(7), and TSS_{total} by (9).
- Step 4: As described in (3), if TSS_k is less than or equal to Th_{TSS} , continue to Step 5. Otherwise, go to Step 6.
- Step 5: Only perform planar, DC, vertical, and horizontal intra prediction modes. In addition, reduce the number of RMD candidates to 3. Then, go to Step 7.

- Step 6: Perform the unmodified conventional intra prediction modes.
 Step 7: Add the MPM modes.
 Step 8: As described in (8), if $(Var_1 + Var_2) - (Var_3 + Var_4)$ is less than or equal to Th_{var} , go to Step 10. Otherwise, continue to Step 9.
 Step 9: Perform DMM prediction.
 Step 10: Execute RDO. Then, if $Depth_{CU}$ is 3, go to Step 12. Otherwise, continue to Step 11.
 Step 11: If TSS_{total} is less than or equal to Th_{total} and if RD_{cur} is less than or equal to Th_{RD} simultaneously, terminate CU splitting early and continue to Step 12. Otherwise, go back to Step 3 with $Depth_{CU}++$.
 Step 12: Finish depth CTU encoding.

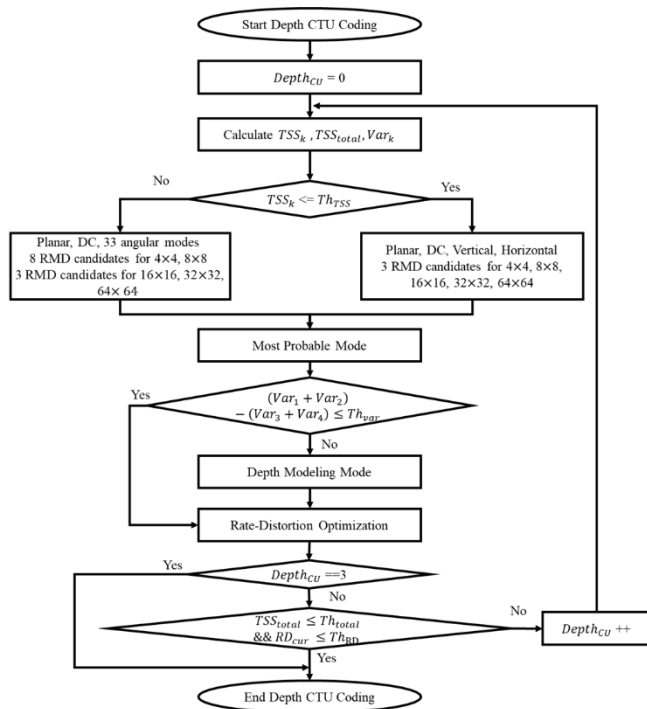


Fig. 20 The complete flowchart of the overall proposed algorithm.

III. EXPERIMENTAL RESULTS

The proposed method is implemented on the 3D-HEVC reference software version 16.0 (HTM-16.0) [38] under all-intra configuration and three-view case. Four QP pairs are encoded, and all the setups of the experimental environment follow the common test condition (CTC) [39]. The setups of the testing environment are listed in Table 5. Eight testing sequences containing two resolutions (1024×768 and 1920×1088) are encoded. Detailed information on the testing sequences is itemized in Table 6. To investigate the effectiveness of the proposed fast coding algorithm, the time saving of depth map coding (Depth TS) related to HTM-16.0 is calculated by (11). The Bjøntegaard delta bitrate (BD-BR) [40], [41] is utilized to evaluate the coding efficiency. However, the depth map is for view synthesis or 3D displays instead of direct viewing. As a result, we compute the BD-BR of the synthesized view by the overall bitrate (color

texture + depth map) and the quality of the synthesized view rather than only considering the depth map itself to evaluate the coding efficiency.

Table 5. Setups of the testing environment.

Testing Conditions	Settings
HTM version	HTM-16.0
Testing Case	Three-View Case with Depth Maps
Configurations	All-intra
QP(Color Texture, Depth Map)	(25,34),(30,39),(35,42),(40,45)
Intra Period	1

Table 6. Testing sequences.

Sequence	Resolution	Frame Rate	Frames	View
Kendo	1024×768	30	300	3-1-5
Balloons	1024×768	30	300	3-1-5
Newspaper	1024×768	30	300	4-2-6
Poznan_Street	1920×1088	25	250	4-5-3
Poznan_Hall2	1920×1088	25	200	6-7-5
Shark	1920×1088	30	300	5-1-9
Undo_Dancer	1920×1088	25	250	5-1-9
GT_Fly	1920×1088	25	250	5-9-1

$$\text{Depth TS (\%)} = \frac{\text{Depth Time}_{\text{HTM-16.0}} - \text{Depth Time}_{\text{proposed}}}{\text{Depth Time}_{\text{HTM-16.0}}} \times 100\% \quad (11)$$

We compare the performance of the proposed algorithm to that of the algorithm proposed by Zhang *et al.* [26]. The BD-BR and depth time saving (TS) results are tabulated in Table 7. From Table 7, the proposed approach provides more significant depth time savings and outperforms previous work on each testing sequence. For the Poznan_Hall2 sequence, the time saving of the proposed algorithm even reach 72.25%, which is the greatest among all testing sequences. The averaged BD-BR performance of the proposed method is 0.32%, which is superior to that of [26] 0.51%. Compared to [26], approximately 16.77% of the depth coding time is further reduced by our scheme. In other words, we can diminish the depth coding time more significantly with negligible BD-BR degradation.

Table 7. Performance comparison of BD-BR and depth time-saving.

Sequence	BD-BR(%)		Depth TS(%)	
	Zhang's [26]	Proposed	Zhang's [26]	Proposed
Kendo	0.43	0.17	38.23	48.04
Balloons	0.67	0.19	35.49	45.29
Newspaper	0.89	0.53	33.85	42.48
1024×768	0.66	0.30	35.86	45.27
Poznan_Street	0.29	0.22	39.67	59.03
Poznan_Hall2	0.75	0.71	48.33	72.25
Shark	0.33	0.22	34.16	58.92
UndoDancer	0.49	0.28	41.57	66.47
GT_Fly	0.21	0.25	43.14	56.16
1920×1088	0.41	0.34	41.38	62.57
Average	0.51	0.32	39.31	56.08

Most of the previous works only design a fast algorithm for the PU level. In this paper, we detect the boundary continuity and determine the boundary complexity in advance to reduce the computational complexity of conventional intra prediction and DMM prediction on the PU level. Moreover, we further combine the CU early termination algorithm with the RD-cost limitation on the CU level, which is why we can provide significant time-savings compared to previous work.

Table 8 shows the contribution of each individual part of the proposed algorithm. Based on the boundary complexity of the PU, we conditionally simplify the intra modes to 4 kinds and reduce the number of rough mode decision candidates to 3. Approximately 18.90% of the coding time is saved, with only an 0.08% increase in the BD-BR on average. The fast DMM decision works more effectively for sequences with larger resolutions because they contain smoother areas. The fast CU early termination with RD-cost limitation, which has the largest contribution to time savings, determines whether to perform further CU splitting by an adaptively adjusted threshold with 50.58% time-saving. The proposed overall algorithm can considerably lessen the depth encoding time.

Fig. 21 and Fig. 22 demonstrate the subjective comparison of synthesized views by HTM-16.0 and the proposed criteria for the Newspaper and Undo_Dancer sequences, respectively. From the zoomed-in regions, it can be observed that the synthesized views generated by the proposed fast

intra mode decision, fast DMM decision, fast CU early termination algorithm with RD-cost limitation and overall algorithm can maintain similar quality as those produced by HTM-16.0. In summary, the time-saving result of the overall proposed algorithm significantly outperforms that the previous work, and the proposed overall algorithm maintains great coding efficiency.

Table 8. Contributions of each individual part of the proposed algorithm.

Sequence	Fast Intra Mode Decision		Fast DMM Decision		Fast CU Early Termination Algorithm with RD-cost Limitation		Overall Algorithm	
	BD-BR (%)	Depth TS (%)	BD-BR (%)	Depth TS (%)	BD-BR (%)	Depth TS (%)	BD-BR (%)	Depth TS (%)
	Kendo	0.07	17.25	0.05	8.19	0.03	42.23	0.17
Balloons	0.08	17.40	0.06	7.17	0.06	39.13	0.19	45.29
Newspaper	0.17	16.97	0.21	6.85	0.04	35.47	0.53	42.48
1024×768	0.11	17.21	0.11	7.40	0.04	38.94	0.30	45.27
Poznan_Street	0.07	19.70	0.10	10.73	0.02	53.56	0.22	59.03
Poznan_Hall2	0.11	21.73	0.35	12.18	0.06	68.52	0.71	72.25
Shark	0.06	19.44	0.07	11.77	0.00	52.64	0.22	58.92
UndoDancer	0.02	19.76	0.14	16.92	0.03	60.95	0.28	66.47
GT_Fly	0.04	18.98	0.08	10.82	0.00	52.19	0.25	56.16
1920×1088	0.06	19.92	0.15	12.48	0.02	57.57	0.34	62.57
Average	0.08	18.90	0.13	10.58	0.03	50.58	0.32	56.08

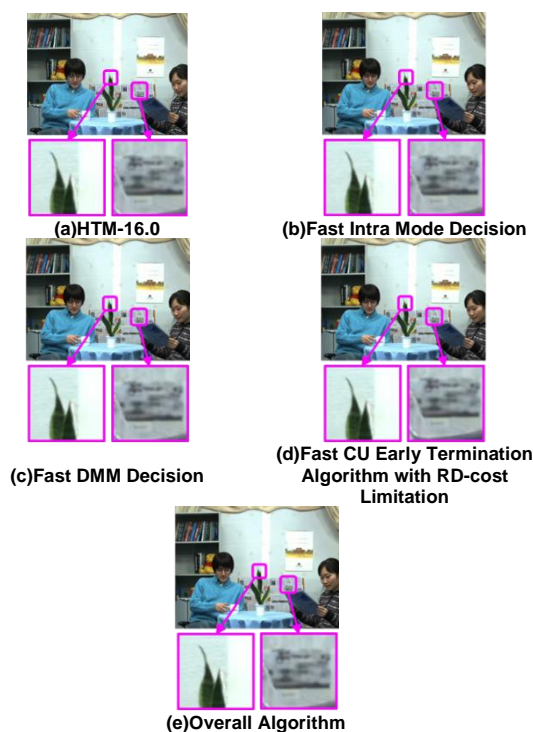


Fig. 21 Subjective comparison of synthesized views and their zoomed regions generated by HTM-16.0 and the proposed criteria for the Newspaper sequence at the 2nd frame of the synthesized view 3.5.

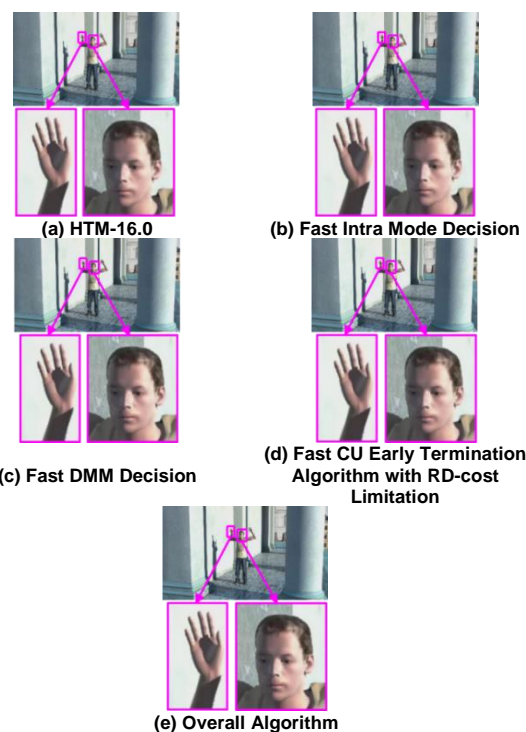


Fig. 22 Subjective comparison of synthesized views and their zoomed regions generated by HTM-16.0 and the proposed criteria for the Undo_Dancer sequence at the 30th frame of the synthesized view 2.0.

IV. CONCLUSION

The 3D-HEVC encoder consumes considerable coding time during the PU mode decision and CU splitting. In addition, advanced techniques for depth map coding also bring about additional computational complexity. In this paper, we accelerate the depth map intra coding of 3D-HEVC based on boundary continuity. Three fast coding methods are proposed. First, the fast intra mode decision reduces the number of intra modes and RMD candidates based on boundary complexity. Second, the fast DMM decision effectively chooses whether to perform DMM prediction according to the difference of the boundary variances. Third, the fast CU early termination algorithm combines the RD-cost limitation to avoid unnecessary CU splitting in smooth areas. The experimental results show that we decrease the depth coding time by 56.08% with only a 0.32% increase in the BD-BR. The proposed algorithm outperforms the algorithm in the previous work with apparent time-saving improvements and maintains great coding efficiency.

REFERENCES

- [1] Q. Zhang, Y. Wang, T. Wei, L. Huang, and R. Su, "A Fast and Efficient 3D-HEVC Method for Complexity Reduction Based on the Correlations of Inter-View, Spatio-Temporal, and Texture-Depth," *IEEE Access*, vol. 8, pp. 129075-129086, July 2020.
- [2] O. Stankiewicz, K. Wegner, and M. Domański, "Study of 3D Video Compression Using Nonlinear Depth Representation," *IEEE Access*, vol. 7, pp. 31110-31122, March 2019.
- [3] O. Stankiewicz, M. Domański, A. Dziembowski, A. Grzelka, D. Mieloch, and J. Samelak, "A Free-Viewpoint Television System for Horizontal Virtual Navigation," *IEEE Transactions on Multimedia*, vol. 20, no. 8, pp. 2182-2195, August 2018.
- [4] P. C. Huang, J. R. Lin, G. L. Li, K. H. Tai, and M. J. Chen, "Improved Depth-Assisted Error Concealment Algorithm for 3D Video Transmission," *IEEE Transactions on Multimedia*, vol. 19, no. 11, pp. 2625-2632, November 2017.
- [5] H. Roodaki, Z. Irvani, M. R. Hashemi, and S. Shirmohammadi, "A View-Level Rate Distortion Model for Multi-View/3D Video," *IEEE Transactions on Multimedia*, vol. 18, no. 1, pp. 14-24, January 2016.
- [6] P. Merkle, K. Müller, and T. Wiegand, "Coding of Depth Signals for 3D Video Using Wedgelet Block Segmentation with Residual Adaptation," in *Proceedings of IEEE International Conference on Multimedia and Expo (ICME)*, San Jose, CA, USA, July 2013.
- [7] K. J. Oh, J. Lee, and D. S. Park, "Depth Intra Skip Prediction for 3D Video Coding," in *Proceedings of Annual Summit and Conference on Signal & Information Processing Association (APSIPA ASC)*, Hollywood, CA, USA, December 2012.
- [8] Y. Chen, G. Tech, K. Wegner, and S. Yea, "Test Model 11 of 3D-HEVC and MV-HEVC," JCT3V-K1003, February 2015.
- [9] A. Ortega, and K. Ramchandran, "Rate-Distortion Methods for Image and Video Compression," *IEEE Signal Processing Magazine*, vol. 15, no. 6, pp. 23-50, November 1998.
- [10] Y. Zhang, X. Yang, X. Liu, Y. Zhang, G. Jiang, and S. Kwong, "High-Efficiency 3D Depth Coding Based on Perceptual Quality of Synthesized Video," *IEEE Transactions on Image Processing*, vol. 25, no. 12, pp. 5877-5891, December 2016.
- [11] L. Zhu, Y. Zhang, S. Wang, H. Yuan, S. Kwong, and H. H. -S. Ip, "Convolutional Neural Network-Based Synthesized View Quality Enhancement for 3D Video Coding," *IEEE Transactions on Image Processing*, vol. 27, no. 11, pp. 5365-5377, November 2018.
- [12] S. Shahriyar, M. Murshed, M. Ali, and M. Paul, "Depth Sequence Coding with Hierarchical Partitioning and Spatial-Domain Quantization," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 30, no. 3, pp. 835-849, March 2020.
- [13] K. L. Chung, Y. H. Huang, C. H. Lin, and J. P. Fang, "Novel Bitrate Saving and Fast Coding for Depth Videos in 3D-HEVC," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 26, no. 10, pp. 1859-1869, October 2016.
- [14] J. Lei, J. Duan, F. Wu, N. Ling, and C. Hou, "Fast Mode Decision Based on Grayscale Similarity and Inter-View Correlation for Depth Map Coding in 3D-HEVC," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 28, no. 3, pp. 706-718, March 2018.
- [15] L. Shen, K. Li, G. Feng, P. An, and Z. Liu, "Efficient Intra Mode Selection for Depth-Map Coding Utilizing Spatiotemporal, Inter-Component and Inter-View Correlations in 3D-HEVC," *IEEE Transactions on Image Processing*, vol. 27, no. 9, pp. 4195-4206, September 2018.
- [16] H. Hamout and A. Elyousfi, "Fast Depth Map Intra Coding for 3D Video Compression Based Tensor Feature Extraction and Data Analysis," *IEEE Transactions on Circuits and Systems for Video Technology*, Early Access, May 2019.
- [17] G. Sanchez, M. Saldanha, G. Balota, B. Zatt, M. Porto, and L. Agostini, "A Complexity Reduction Algorithm for Depth Maps Intra Prediction on the 3D-HEVC," in *Proceedings of 2014 IEEE Visual Communications and Image Processing Conference*, Valletta, Malta, December 2014.
- [18] G. Sanchez, M. Saldanha, B. Zatt, M. Porto, and L. Agostini, "S-GMOF: A Gradient-based Complexity Reduction Algorithm for Depth-Maps Intra Prediction on 3D-HEVC," in *Proceedings of 2015 IEEE 6th Latin American Symposium on Circuits and Systems (LASCAS)*, Montevideo, Uruguay, February 2015.
- [19] M. Saldanha, B. Zatt, M. Porto, L. Agostini, and G. Sanchez, "Solutions for DMM-1 Complexity Reduction in 3D-HEVC Based on Gradient Calculation," in *Proceedings of 2016 IEEE 7th Latin American Symposium on Circuits and Systems (LASCAS)*, Florianopolis, Brazil, March 2016.
- [20] C. H. Fu, H. B. Zhang, and W. M. Su, "Fast Wedgelet Pattern Decision for DMM in 3D-HEVC," in *Proceedings of 2015 IEEE International Conference on Digital Signal Processing (DSP)*, Singapore, July 2015.
- [21] H. B. Zhang, S. H. Tsang, Y. L. Chan, C. H. Fu, and W. M. Su, "Early Determination of Intra Mode and Segment-wise DC Coding for Depth Map Based on Hierarchical Coding Structure in 3D-HEVC," in *Proceedings of 2015 Asia-Pacific Signal and Information Processing Association (APSIPA)*, Hong Kong, China, December 2015.
- [22] R. H. Hong, M. J. Chen, and J. R. Lin, "Efficient DMM Decision of Depth Intra Coding in 3D-HEVC," in *Proceedings of the 30th IPPR Conference on Computer Vision, Graphics, and Image Processing (CVGIP 2017)*, Taiwan, August 2017.
- [23] Z. Y. Gu, J. H. Zheng, L. Nam, and P. Zhang, "Fast Bi-partition Mode Selection for 3D HEVC Depth Intra Coding," in *Proceedings of 2014 IEEE International Conference on Multimedia and Expo (ICME)*, Chengdu, China, July 2014.
- [24] C. S. Park, "Edge-Based Intra Mode Selection for Depth-Map Coding in 3D-HEVC," *IEEE Transactions on Image Processing*, vol. 24, no. 1, pp.155-162, January 2015.
- [25] Q. W. Zhang, N. N. Li, L. X. Xun, and Y. Gan, "Effective Early Terminate Algorithm for Depth Map Intra Coding in 3D-HEVC," *Electronics Letters*, vol. 50, no. 14, pp. 994-996, July 2014.

- [26] H. B. Zhang, C. H. Fu, Y. L. Chan, S. H. Tsang, and W. C. Siu, "Probability-Based Depth Intra Mode Skipping Strategy and Novel VSO Metric for DMM Decision in 3D-HEVC," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 28, no. 2, pp. 513-527, February 2018.
- [27] M. Saldanha, G. Sanchez, C. Marcon, and L. Agostini, "Fast 3D-HEVC Depth Map Encoding Using Machine Learning," *IEEE Transactions on Circuits and Systems for Video Technology*, vol. 30, pp. 850-861, February 2020.
- [28] Jie-Ru Lin, Mei-Juan Chen, Chia-Hung Yeh, Yong-Ci Chen, Lih-Jen Kau, Chuan-Yu Chang, and Min-Hui Lin, "Visual Perception Based Algorithm for Fast Depth Intra Coding of 3D-HEVC," Early Access, *IEEE Transactions on Multimedia*, 2021.
- [29] G. Shen, W. S. Kim, A. Ortega, J. Lee, and H. Wey, "Edge-Aware Intra Prediction for Depth-Map Coding," in *Proc. 2010 IEEE International Conference on Image Processing*, pp.3393-3396, Hong Kong, September 2010.
- [30] G. Shen, W. S. Kim, S. K. Narang, A. Ortega, J. Lee, and H. Wey, "Edge-Adaptive Transforms for Efficient Depth Map Coding," in *Proc. 28th Picture Coding Symposium*, pp.566-569, Nagoya, Japan, December 2010.
- [31] V. Sanchez, "Sample-Based Edge Prediction Based on Gradients for Lossless Screen Content Coding in HEVC," in *Proc. 2015 Picture Coding Symposium*, pp. 134-138, Cairns, Australia, June 2015.
- [32] S. Hu, R. Cohen, A. Vetro, and C.C. J. Kuo, "Screen Content Coding for HEVC using Edge Modes," in *Proc. 2013 IEEE International Conference on Acoustics, Speech and Signal Processing*, pp. 1714-1718, Vancouver, Canada, May 2013.
- [33] H. Chen, A. Saxena, and F. Fernandes, "Nearest-Neighbor Intra Prediction for Screen Content Video Coding," in *Proc. 2014 IEEE International Conference on Image Processing*, pp. 3151-3155, Paris, France, October 2014.
- [34] Yu-Chih Hsu, Jie-Ru Lin, and Mei-Juan Chen, "Fast DMM Decision Based on Variance Calculation for Depth Intra Coding in 3D-HEVC," in *Proceedings of 2017 Taiwan Academic Network Conference (TANET 2017)*, Taiwan, October 2017.
- [35] Yu-Chih Hsu, Jie-Ru Lin, Mei-Juan Chen, Chia-Hung Yeh, Min-Hui Lin, and Wei-Chieh Lu, "Acceleration of Depth Intra Coding for 3D-HEVC by Efficient Early Termination Algorithm," in *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS 2018)*, Chengdu, China, October 2018.
- [36] Yu-Chih Hsu, Jie-Ru Lin, Mei-Juan Chen, Chia-Hung Yeh, and Ro-Min Weng, "Fast Depth Intra Coding in 3D-HEVC Based on Boundary Continuity," in *Proceedings of the 11th International Conference on Digital Image Processing (ICDIP 2019)*, Guangzhou, China, May 2019.
- [37] G. Sanchez, R. Cataldo, R. Fernandes, L. Agostini, and C. Marcon, "3D-HEVC Depth Maps Intra Prediction Complexity Analysis," in *Proceedings of 2016 IEEE International Conference on Electronics, Circuits and Systems (ICECS)*, Monte Carlo, Monaco, December 2016.
- [38] (2015) 3D-HEVC reference software version 16.0 (HTM-16.0), available online at https://hevc.hhi.fraunhofer.de/svn/svn_3DVCSoftware/tags/HTM-16.0/.
- [39] K. Müller and A. Vetro, "Common test conditions of 3DV core experiments," document Rec. JCT3V-G1100, Joint Collaborative Team on 3D Video Coding Extension Development of ITU-T SG 16 WP 3 and ISO/IEC JTC 1/SC 29/WG 11, San Jose, USA, Jan. 2014.
- [40] G. Bjontegaard, "Calculation of Average PSNR Differences between RD Curves," ITU-T SG16/Q6 Document, VCEG-M33, Austin, April 2001.
- [41] G. Bjontegaard, "Improvements of the BD-PSNR Model," ITU-T SG16/Q6, Document, VCEG-A111, Berlin, July 2008.



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