

## ABOUT THE INSTITUTE

Delhi Technological University (formerly known as Delhi College of Engineering) located in Delhi is a premier institution of Engineering and Technology in India. The university plays a key role in the national and global knowledge network, empowering India with the wings of knowledge and innovation. The university aims to imbibe a culture of methodical research and to develop a scientific temper for the integration of science, engineering, and management. DTU has been ranked amongst the top institutions in multiple reputed national and international surveys.

## ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering has experienced considerable growth since its inception in 1976. The department's vision is to focus on the incubation of innovations in electronic design/

fabrication, and communication technologies, which are needed to address the growing challenges of tomorrow. The overall aim is to harbor a sustainable, and continuously evolving scientific, technological, and educational environment which is both internationally-adapted and industry relevant.

## OBJECTIVE OF THE FDP

This FDP aims to provide a deep insight into the latest concepts, advancements, and challenges of modern VLSI Design. Also, it explores the design, issues, and applications of novel nanoscale devices and cutting-edge VLSI technologies. In addition, FDP integrates these advancements into teaching, fostering research capabilities, and promoting collaboration between academia and industry. This FDP facilitates networking among participants to create a vibrant community of VLSI educators and researchers.

## ONE WEEK FACULTY DEVELOPMENT PROGRAM

on

**“Advancement and Challenges in VLSI Design and Nanoscale Devices”**

May 27-31, 2024



Organized by

**Department of Electronics and  
Communication Engineering  
Delhi Technological University  
Delhi-110042**

## RESOURCE PERSONS

- ❖ Prof. Samresh Das, IIT Delhi
- ❖ Prof. Ankur Gupta, IIT Delhi
- ❖ Prof. Manoj Kumar Taleja, NIT Delhi
- ❖ Dr. Sneha Saurabh, IIIT Delhi
- ❖ Dr. Rakesh K. Palani, IIT Delhi
- ❖ Dr. Anuj Grover, IIIT Delhi
- ❖ Dr. Somesh Kumar, IIITM Gwalior
- ❖ Dr. Mahendra Sakre, IIT Ropar
- ❖ Dr. Sachin Agarwal, NIT Delhi
- ❖ Dr. Dharmendra Singh Yadav, NIT Kurukshetra

## TOPICS TO BE COVERED

- ✓ Low Power VLSI Design and Bandgap circuit design
- ✓ Simulations for Prototyping and Test of an IC
- ✓ Device and Circuit Co-Design, In-Memory Computing
- ✓ Recent Trends in VLSI Interconnects and RF Energy Harvestings
- ✓ THz Electronic Devices for Communication and Imaging
- ✓ Formal Verification of Digital Systems: Techniques and Applications
- ✓ Advancement and Challenges in Nano-Scale Devices

## ORGANIZING COMMITTEE

### Chief Patron

**Prof. Prateek Sharma**  
Vice Chancellor, DTU

### Patron

**Prof. Madhusudan Singh**  
Registrar, DTU

### Convenor

**Prof. O. P. Verma**  
HOD, ECE Dept., DTU

### Coordinator

**Dr. Sumit Kale**

### Co-Coordinators

**Dr. Sonam Rewari**  
**Mr. Sachin Dhariwal**

### Organizing Members

**Dr. Deva Nand**  
**Dr. Sonal Singh**  
**Dr. Chaudhary Indra Kumar**  
**Mr. Akshay Mann**

## IMPORTANT DATES

Last Date of Registration: May 23, 2024  
Confirmation to the Participants: May 24, 2024  
Date of FDP: May 27-31, 2024

For any query please contact:

**Dr. Sumit Kale**  
Email: [sumit.kale@dtu.ac.in](mailto:sumit.kale@dtu.ac.in)  
Contact no. 09926337025

## PARTICIPANTS

The program is open to the faculty members of AICTE/ UGC approved Engineering Institutes/Universities, and PhD scholars.

## REGISTRATION DETAILS

- ✓ The number of participants is limited to 40 only (first come first serve basis).
- ✓ The registration fee per participant is Rs. 1000/-.
- ✓ The candidates can deposit the registration fee online in the account details given below-

**Account Holders Name-** Registrar, DTU  
**Bank Name and Branch-** State Bank of India, Delhi College of Engineering  
**Account No-** 30875679275  
**IFSC Code-** SBIN0010446

- ✓ **Registration Link/QR Code:**  
<https://forms.gle/5xYA4ivHotFtUxCT8>



**Note: Certificates will be issued to the participants on successful participation in the FDP.**