

# No More Gotos: Decompilation Using Pattern-Independent Control-Flow Structuring and Semantics-Preserving Transformations

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# Agenda

01 Motivation

02 Control Flow Structuring

03 The DREAM Decompiler

04 Results

05 Conclusion

# Motivation

## Decompilation in Security

```
010101010101010100
010101010101010100
010101010101010100
010101010101010100
010101010101010100
```

**Binary code**

# Motivation

## Decompilation in Security

### Source code

```
int f(int a){  
    int i = 0;  
    for(; i < a ; i++)  
        ...  
}
```

Compilation

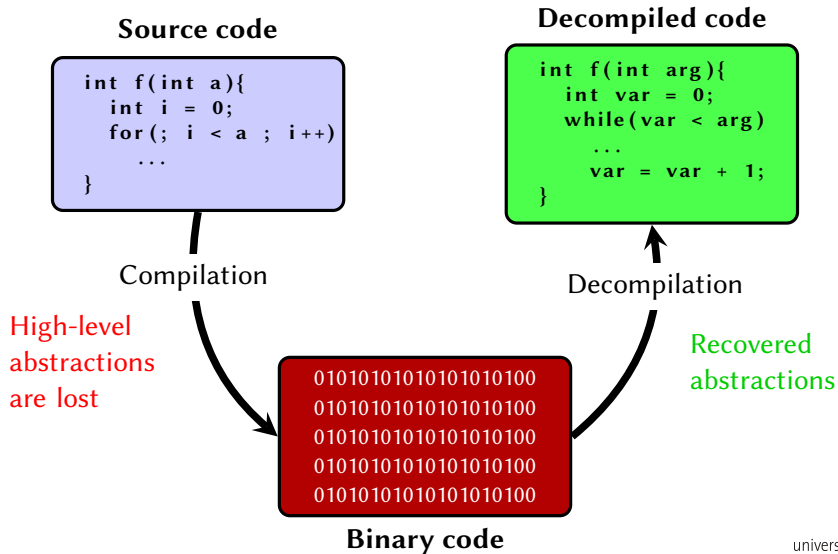
High-level  
abstractions  
are lost

```
010101010101010100  
010101010101010100  
010101010101010100  
010101010101010100  
010101010101010100
```

### Binary code

# Motivation

## Decompilation in Security



- Manual reverse engineering

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- Apply source-based techniques to binary code

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- Manual reverse engineering
- Apply source-based techniques to binary code
  - Find vulnerabilities, bugs



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## Decompilation in Security

- Manual reverse engineering
- Apply source-based techniques to binary code
  - Find vulnerabilities, bugs
  - Taint tracking

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### Goal: Enhanced Structuredness

Effective control flow structure recovery to improve readability and enhance program analysis

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01 Motivation

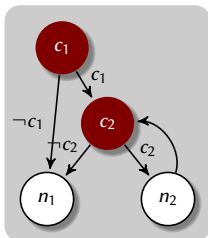
02 Control Flow Structuring

03 The DREAM Decompiler

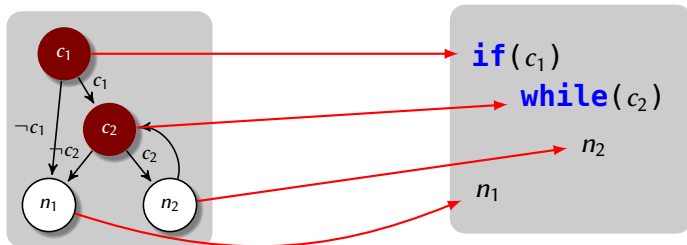
04 Results

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# Control Flow Structuring



# Control Flow Structuring



# Structured vs. unstructured code

```
int f(int a, int b){
    int sum = 0;
    if(a < b){
        for(int i = a; i < b; i++)
            sum += i;
    }
    return sum;
}
```

Structured code

```
int f(int a, int b){
    int sum = 0;
    if(a >= b)
        goto Label_2;
    int i = a;
Label_1:
    if(i >= b)
        goto Label_2;
    sum += i;
    i++;
    goto Label_1;
Label_2:
    return sum;
}
```

Unstructured code

# Control Flow Structuring

## Structural Analysis

State of the art: **Structural Analysis** [Sharir80]

- Pattern-matching using a predefined set of region schemas (patterns)

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- Pattern-matching using a predefined set of region schemas (patterns)
- Use goto statements if no match is found



State of the art: **Structural Analysis** [Sharir80]

- Pattern-matching using a predefined set of region schemas (patterns)
- Use goto statements if no match is found
- Example: Decompiling a P2P Zeus sample with Hex-Rays
  - 1,571 goto for 49,514 LoC
  - **1 goto for each 32 LoC**

Improving vanilla structural analysis to recover more structure

- SESS Analysis [Engel et al., SCOPES 2011]
- Phoenix Decompiler [Schwartz et al., USENIX Security 2013]

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New control-flow structuring algorithm

**Pattern-Independent Structuring**

**Semantics-Preserving Transformations**

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01 Motivation

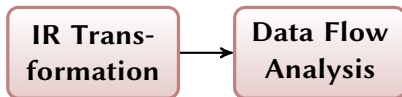
02 Control Flow Structuring

**03 The DREAM Decompiler**

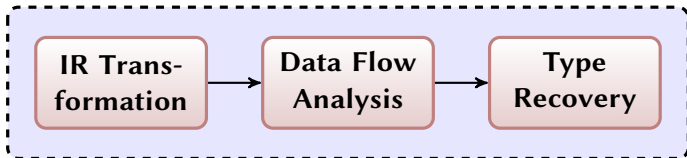
04 Results

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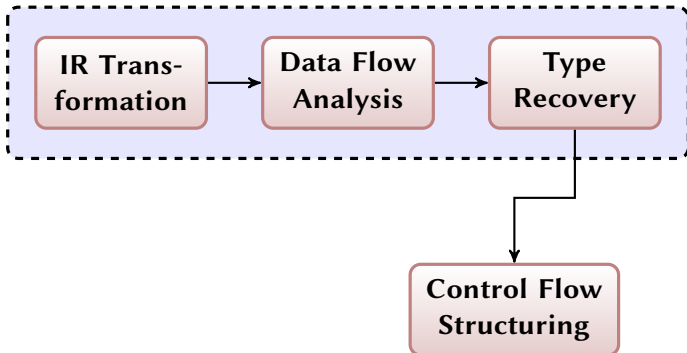
## IR Trans- formation

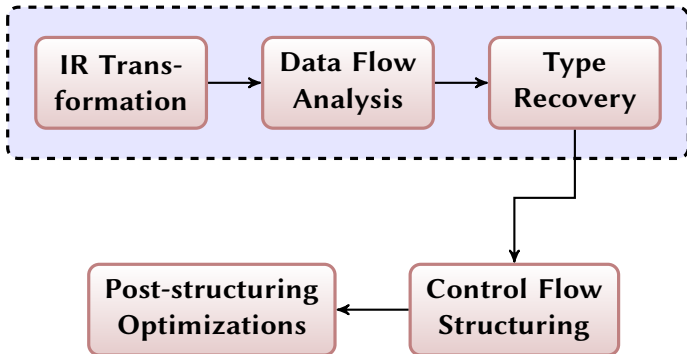


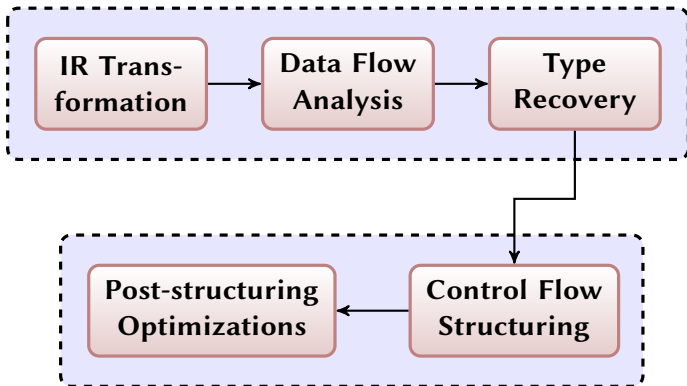


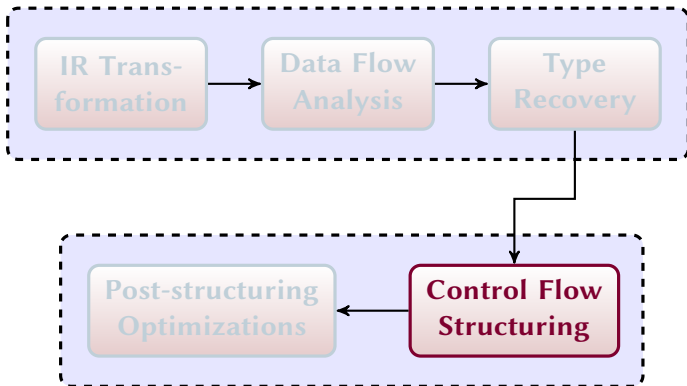




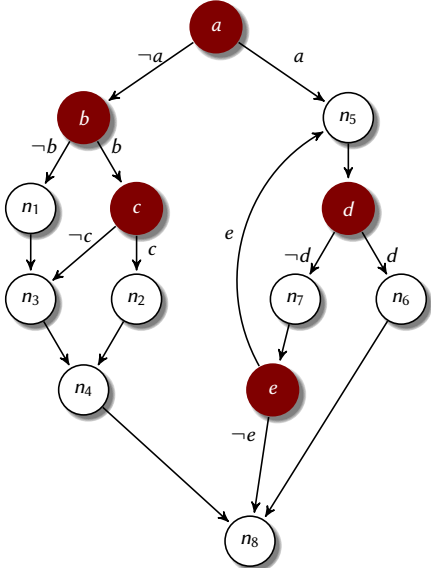




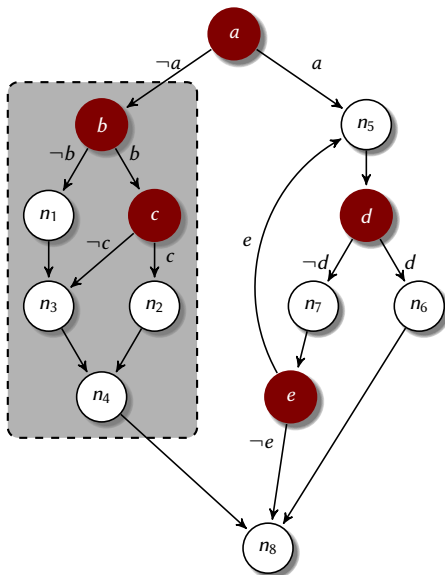




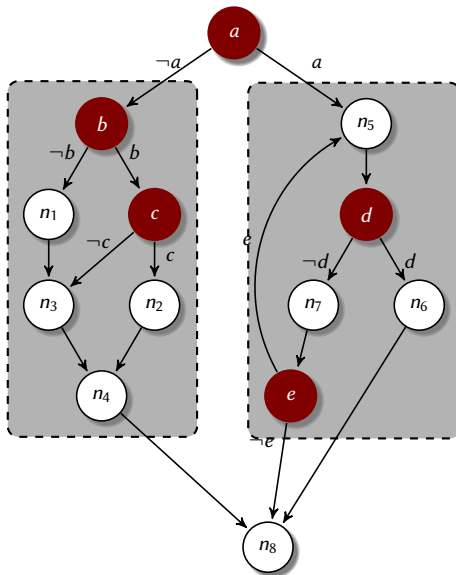
# Running Example



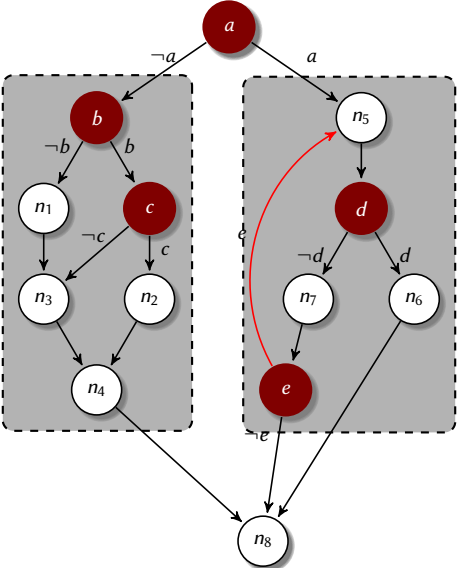
# Running Example



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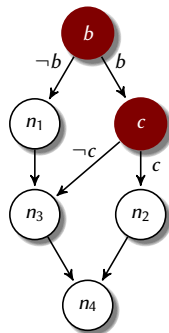


# Running Example



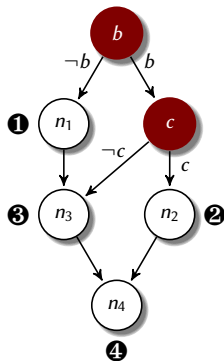


# Acyclic Regions



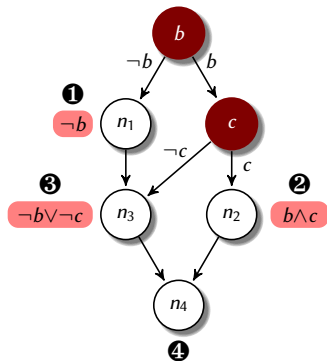
# Acyclic Regions

- Lexical order



# Acyclic Regions

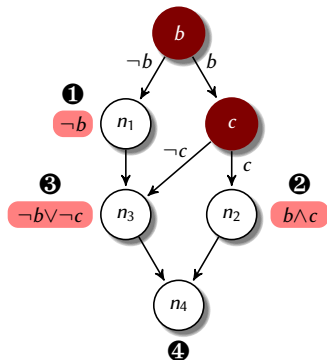
- Lexical order
- Reaching conditions



# Acyclic Regions

- Lexical order
- Reaching conditions
- Initial AST as a sequence of **if** constructs

```
if( $\neg b$ )  
   $n_1$   
if( $b \wedge c$ )  
   $n_2$   
if( $\neg b \vee \neg c$ )  
   $n_3$   
 $n_4$ 
```

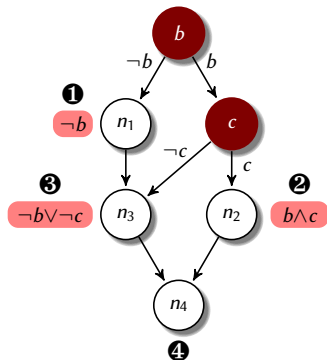


# Acyclic Regions

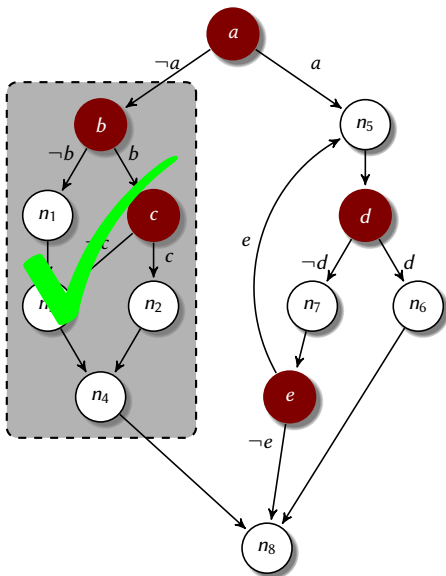
- Lexical order
- Reaching conditions
- Initial AST as a sequence of **if** constructs
- Refine initial AST to find **switch**, **if-else** constructs

```
if( $\neg b$ )
   $n_1$ 
if( $b \wedge c$ )
   $n_2$ 
if( $\neg b \vee \neg c$ )
   $n_3$ 
 $n_4$ 
```

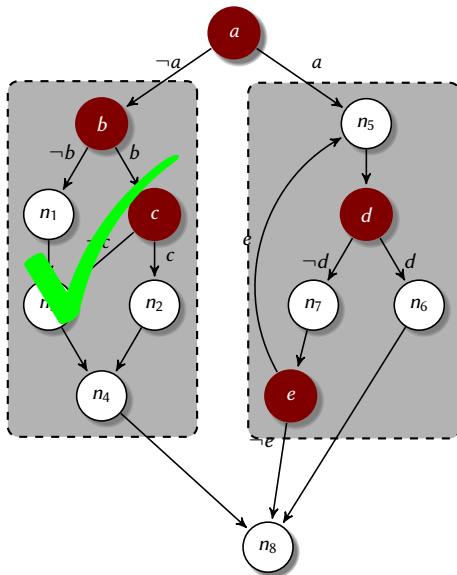
```
if( $\neg b$ )
   $n_1$ 
if( $b \wedge c$ )
   $n_2$ 
else
   $n_3$ 
 $n_4$ 
```



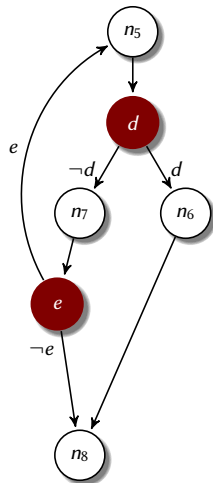
# Running Example



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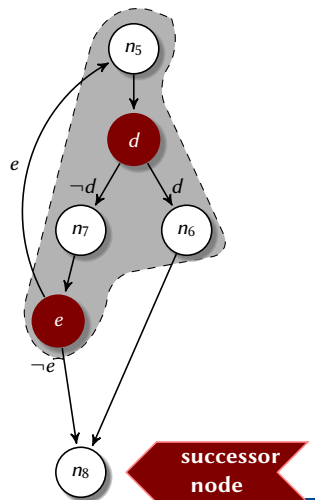
# Cyclic Regions





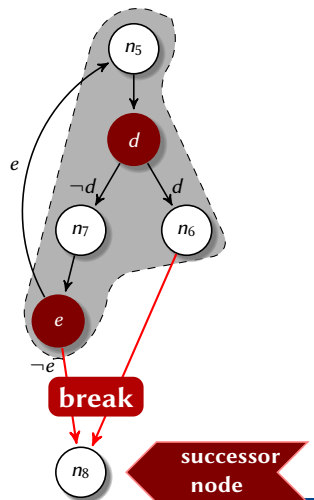
# Cyclic Regions

- Identify loop nodes and successor node



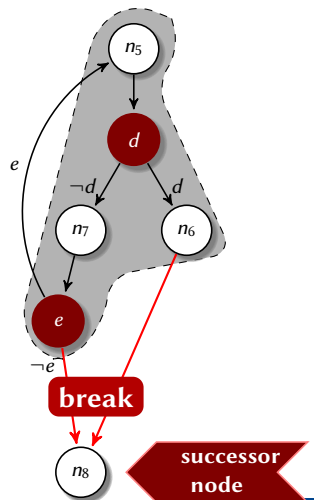
# Cyclic Regions

- Identify loop nodes and successor node
- Replace edges to the successor node by **break** statements



# Cyclic Regions

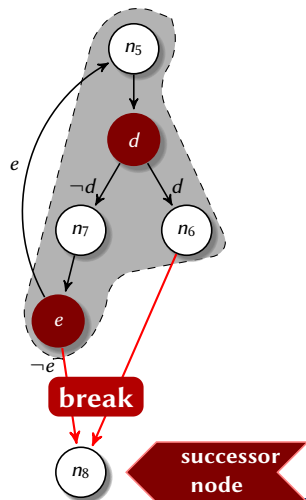
- Identify loop nodes and successor node
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- Structure loop body  $B_{AST}$



# Cyclic Regions

- Identify loop nodes and successor node
- Replace edges to the successor node by **break** statements
- Structure loop body  $B_{AST}$
- Initial AST: **while** (1) {  $B_{AST}$  }

```
while (1)
  ...
  if ( $\neg e$ )
    break
```

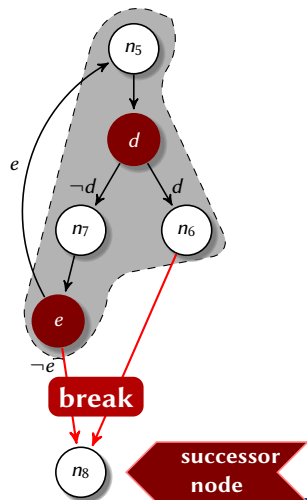


# Cyclic Regions

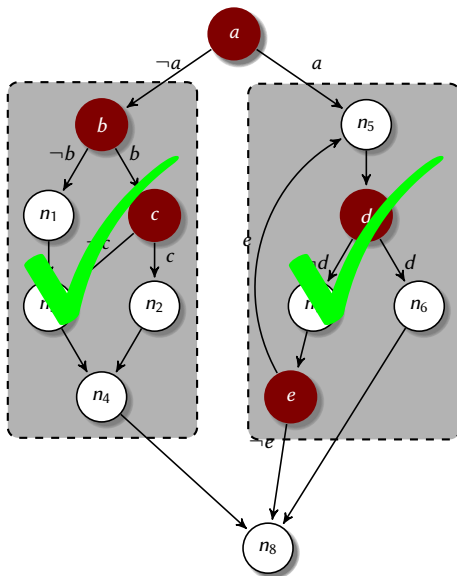
- Identify loop nodes and successor node
- Replace edges to the successor node by **break** statements
- Structure loop body  $B_{AST}$
- Initial AST: **while** (1) {  $B_{AST}$  }
- Infer loop type and condition

```
while (1)
  ...
  if ( $\neg e$ )
    break
```

```
do
  ...
  while (e)
```



# Running Example



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# Readability Enhancements

```
int f(int a1){
    int v2 = 0;
    while((a1 <= 1 && v2 <= 100)
        || (a1 > 1 && v2 <= 10)){
        printf("inside_loop");
        ++v2;
        --a1;
    }
    printf("loop_terminated");
    return v2;
}
```

DREAM

```
signed int f(signed int a1){
    signed int v2;
    v2 = 0;
    while ( a1 > 1 ){
        if ( v2 > 10 )
            goto LABEL_7;
    LABEL_6:
        printf("inside_loop");
        ++v2;
        --a1;
    }
    if ( v2 <= 100 )
        goto LABEL_6;
    LABEL_7:
        printf("loop_terminated");
        return v2;
}
```

Hex-Rays



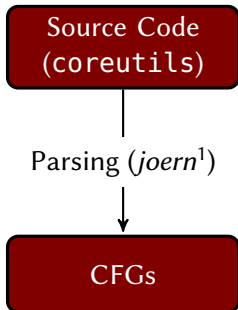
## Metrics

- Correctness
- Structuredness
- Compactness

Source Code  
(coreutils)

---

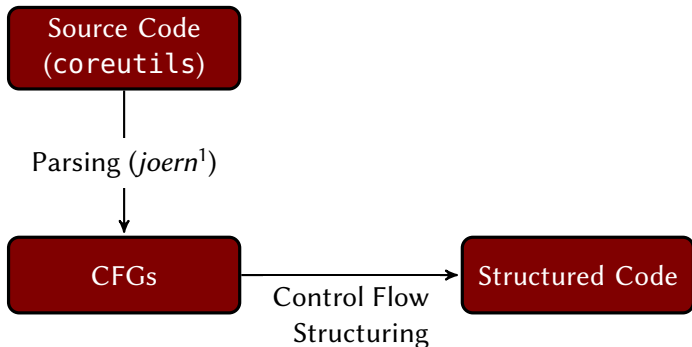
<sup>1</sup>[Yamaguchi et al. IEEE S&P 2014]



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# Correctness

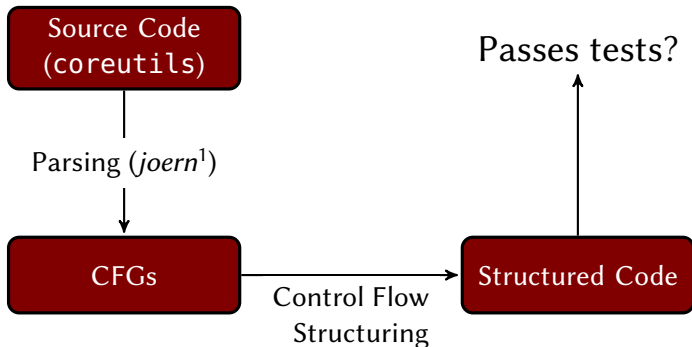
## Experiment Setup



<sup>1</sup>[Yamaguchi et al. IEEE S&P 2014]

# Correctness

## Experiment Setup



<sup>1</sup>[Yamaguchi et al. IEEE S&P 2014]

# Correctness

## Results

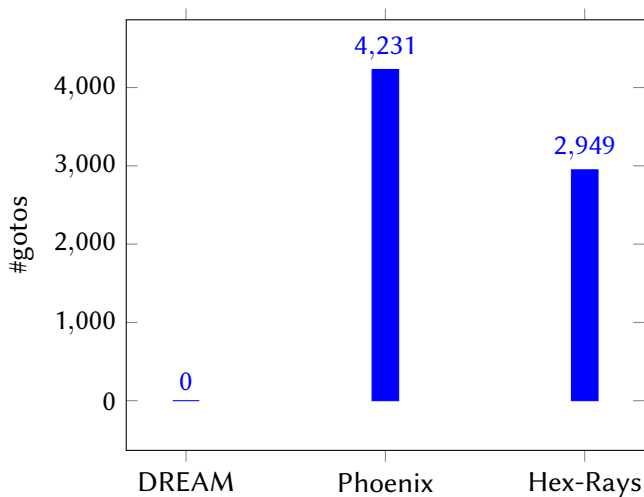
Considered Functions $F$	$ F $	Number of gotos
Functions after preprocessor	1,738	219
Functions correctly parsed by <i>joern</i> <sup>2</sup>	1,530	129
Functions passed tests after structuring	1,530	0

<sup>2</sup>Errors have been reported to *joern*'s authors and are fixed in the current release

# Structuredness and Compactness

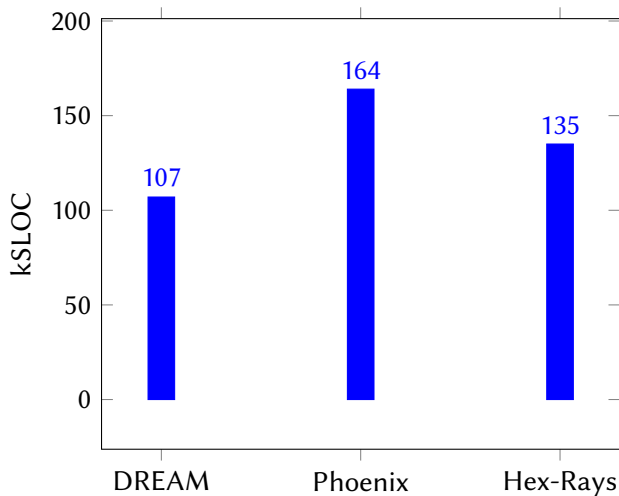
- Tested decompilers
  - DREAM
  - Phoenix (academic state of the art)
  - Hex-Rays (industry state of the art)
- Structuredness
  - Number of gotos
- Compactness
  - Total lines of code
  - Compact functions

# Structuredness

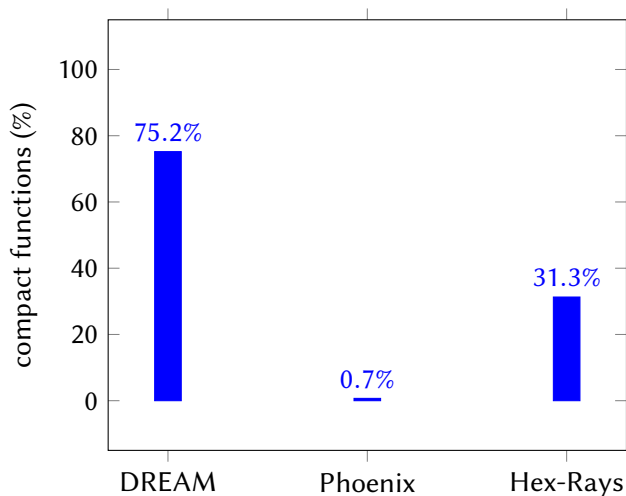




# Compactness



# Compactness



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- Novel control flow structuring algorithm
  - pattern-independent structuring
  - semantics-preserving transformations
- DREAM decompiler
  - goto-free decompiled code
  - compact code
  - good readability

Thank You!  
Questions?