



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

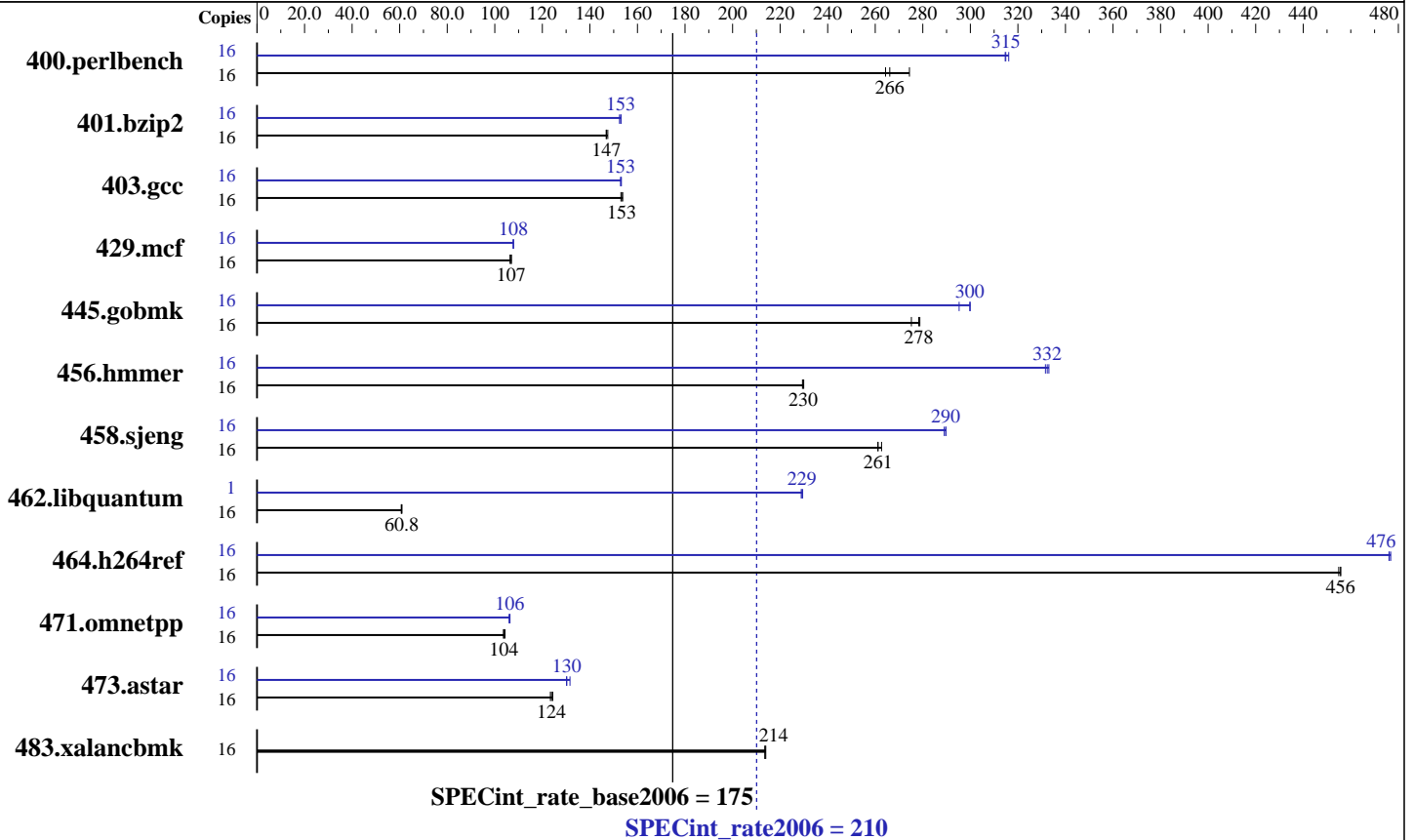
Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint[®]_rate2006 = 210

SPECint_rate_base2006 = 175

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2007
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X7350
 CPU Characteristics: 2.93 GHz, 2x4 MB L2 shared, 1066 MHz bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (16x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: I_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint_rate2006 = 210

SPECint_rate_base2006 = 175

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|------------|-------------|-------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 570 | 274 | 591 | 264 | 587 | 266 | 16 | 494 | 316 | 496 | 315 | 497 | 315 |
| 401.bzip2 | 16 | 1050 | 147 | 1052 | 147 | 1047 | 148 | 16 | 1013 | 152 | 1010 | 153 | 1008 | 153 |
| 403.gcc | 16 | 842 | 153 | 840 | 153 | 837 | 154 | 16 | 842 | 153 | 840 | 153 | 842 | 153 |
| 429.mcf | 16 | 1371 | 106 | 1364 | 107 | 1366 | 107 | 16 | 1354 | 108 | 1353 | 108 | 1353 | 108 |
| 445.gobmk | 16 | 602 | 279 | 603 | 278 | 610 | 275 | 16 | 560 | 300 | 568 | 295 | 560 | 300 |
| 456.hmmer | 16 | 651 | 229 | 650 | 230 | 649 | 230 | 16 | 450 | 332 | 448 | 333 | 449 | 332 |
| 458.sjeng | 16 | 737 | 263 | 741 | 261 | 741 | 261 | 16 | 670 | 289 | 668 | 290 | 668 | 290 |
| 462.libquantum | 16 | 5465 | 60.7 | 5451 | 60.8 | 5439 | 61.0 | 1 | 90.4 | 229 | 90.5 | 229 | 90.3 | 229 |
| 464.h264ref | 16 | 778 | 455 | 777 | 456 | 777 | 456 | 16 | 743 | 477 | 743 | 476 | 744 | 476 |
| 471.omnetpp | 16 | 965 | 104 | 963 | 104 | 959 | 104 | 16 | 943 | 106 | 941 | 106 | 940 | 106 |
| 473.astar | 16 | 910 | 123 | 903 | 124 | 907 | 124 | 16 | 863 | 130 | 862 | 130 | 853 | 132 |
| 483.xalancbmk | 16 | 516 | 214 | 517 | 214 | 517 | 214 | 16 | 516 | 214 | 517 | 214 | 517 | 214 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Bios settings:
Hardware Prefetcher: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2, 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint_rate2006 = 210

SPECint_rate_base2006 = 175

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint_rate2006 = 210

SPECint_rate_base2006 = 175

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint_rate2006 = 210

SPECint_rate_base2006 = 175

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:18:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 January 2008.