



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 31.6

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint_rate_base2006 = 29.8

CPU2006 license: 13

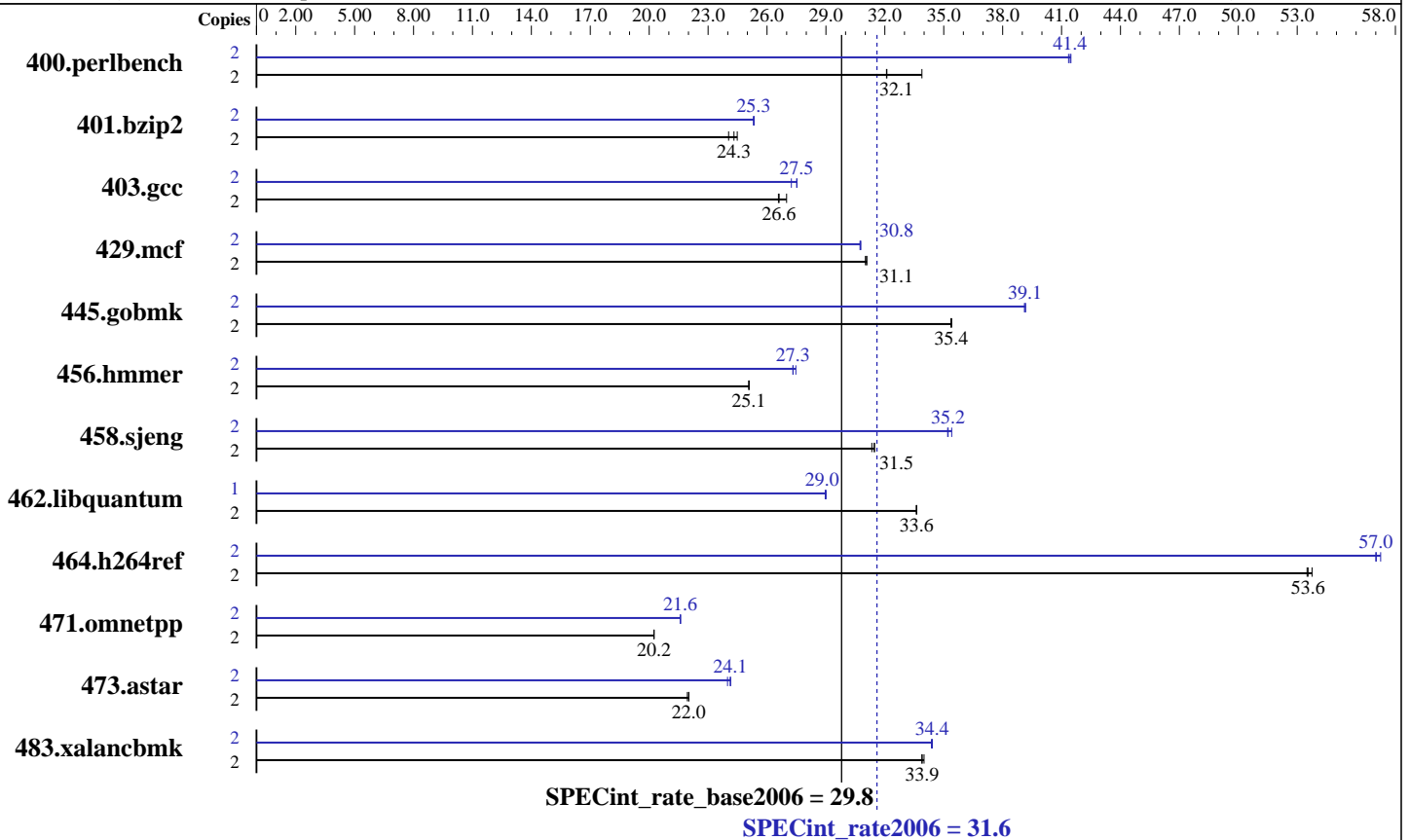
Test date: Dec-2007

Test sponsor: Intel Corporation

Hardware Availability: Jan-2008

Tested by: Intel Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Extreme X7900
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
 Disk Subsystem: Fujitsu 120GB GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate(32-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	577	33.9	609	32.1	609	32.1	2	472	41.4	471	41.5	472	41.4
401.bzip2	2	803	24.0	794	24.3	788	24.5	2	762	25.3	761	25.3	762	25.3
403.gcc	2	596	27.0	605	26.6	606	26.6	2	591	27.2	585	27.5	585	27.5
429.mcf	2	587	31.1	587	31.1	588	31.0	2	593	30.8	593	30.7	593	30.8
445.gobmk	2	593	35.4	593	35.4	593	35.4	2	536	39.1	536	39.1	536	39.2
456.hammer	2	744	25.1	744	25.1	744	25.1	2	683	27.3	679	27.5	683	27.3
458.sjeng	2	769	31.5	769	31.5	772	31.3	2	684	35.4	688	35.2	687	35.2
462.libquantum	2	1233	33.6	1233	33.6	1233	33.6	1	714	29.0	715	29.0	715	29.0
464.h264ref	2	827	53.5	823	53.8	826	53.6	2	773	57.2	776	57.0	776	57.0
471.omnetpp	2	617	20.2	618	20.2	617	20.2	2	579	21.6	579	21.6	579	21.6
473.astar	2	638	22.0	640	21.9	638	22.0	2	581	24.1	582	24.1	585	24.0
483.xalancbmk	2	407	33.9	406	34.0	407	33.9	2	401	34.4	401	34.4	401	34.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
 Binaries were built on Windows Vista32
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0
 The start command with the /affinity switch was used to bind processes to cores

Base Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-fast -Qvec-guard-write /F512000000

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:06:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.