



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3550 (Intel Xeon E5430)

## SPECint\_rate\_base2006 = 103

CPU2006 license: 11

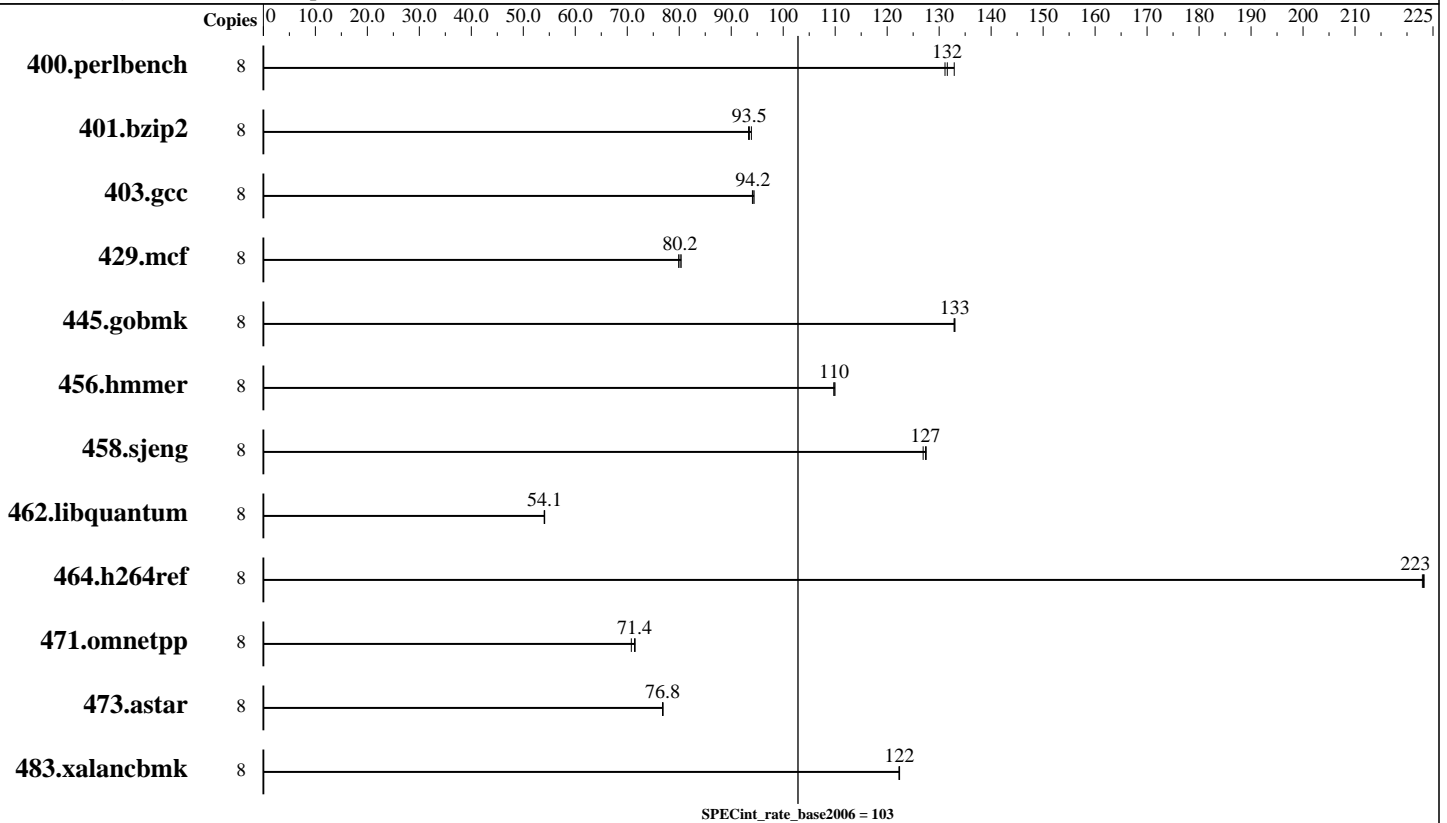
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	588	133	<u>594</u>	<u>132</u>	596	131							
401.bzip2	8	822	93.9	<u>826</u>	<u>93.5</u>	827	93.3							
403.gcc	8	<b>684</b>	<b>94.2</b>	682	94.4	685	94.1							
429.mcf	8	<b>910</b>	<b>80.2</b>	913	79.9	908	80.4							
445.gobmk	8	631	133	632	133	<b>631</b>	<b>133</b>							
456.hammer	8	680	110	<b>680</b>	<b>110</b>	679	110							
458.sjeng	8	763	127	759	128	<b>760</b>	<b>127</b>							
462.libquantum	8	3069	54.0	3063	54.1	<b>3065</b>	<b>54.1</b>							
464.h264ref	8	794	223	<b>793</b>	<b>223</b>	793	223							
471.omnetpp	8	699	71.5	<b>700</b>	<b>71.4</b>	706	70.8							
473.astar	8	731	76.8	730	76.9	<b>731</b>	<b>76.8</b>							
483.xalancbmk	8	451	122	451	122	<b>451</b>	<b>122</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode  
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
Powersaved dameon was disabled in OS  
taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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