



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3650 (Intel Xeon E5430)

SPECint_rate_base2006 = 103

CPU2006 license: 11

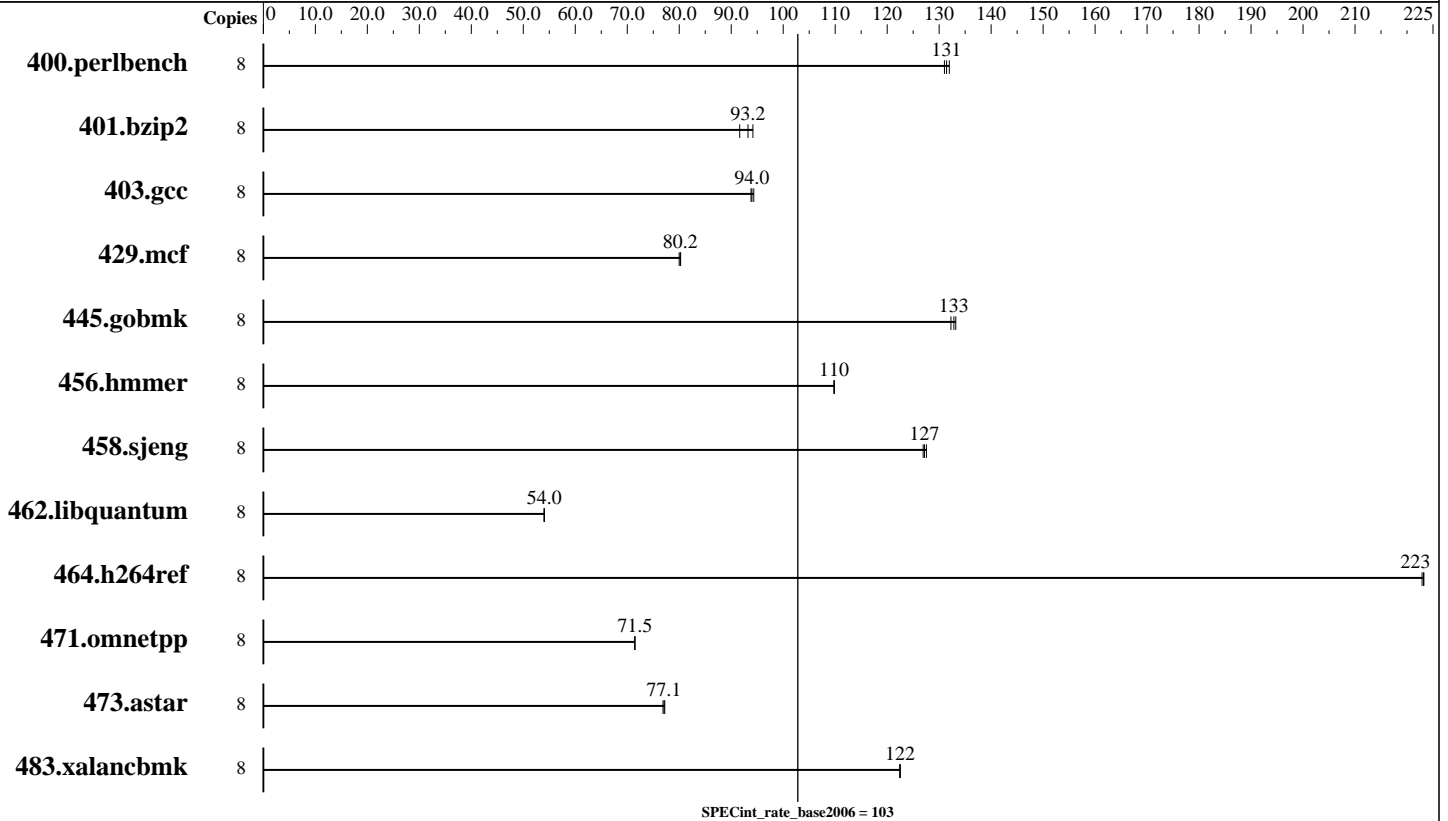
Test date: Dec-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>595</u>	<u>131</u>	592	132	597	131							
401.bzip2	8	820	94.2	843	91.6	<u>828</u>	<u>93.2</u>							
403.gcc	8	<u>685</u>	<u>94.0</u>	687	93.8	683	94.3							
429.mcf	8	909	80.3	<u>910</u>	<u>80.2</u>	912	80.0							
445.gobmk	8	630	133	634	132	<u>632</u>	<u>133</u>							
456.hammer	8	680	110	680	110	<u>680</u>	<u>110</u>							
458.sjeng	8	763	127	<u>761</u>	<u>127</u>	759	128							
462.libquantum	8	3069	54.0	3068	54.0	<u>3068</u>	<u>54.0</u>							
464.h264ref	8	<u>793</u>	<u>223</u>	794	223	793	223							
471.omnetpp	8	700	71.5	700	71.5	<u>700</u>	<u>71.5</u>							
473.astar	8	<u>729</u>	<u>77.1</u>	731	76.9	727	77.2							
483.xalancbmk	8	<u>451</u>	<u>122</u>	451	123	451	122							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.13.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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