



SPEC® CINT2006 Result

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NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5405)

SPECint®_rate2006 = 101

SPECint_rate_base2006 = 83.5

CPU2006 license: 9006

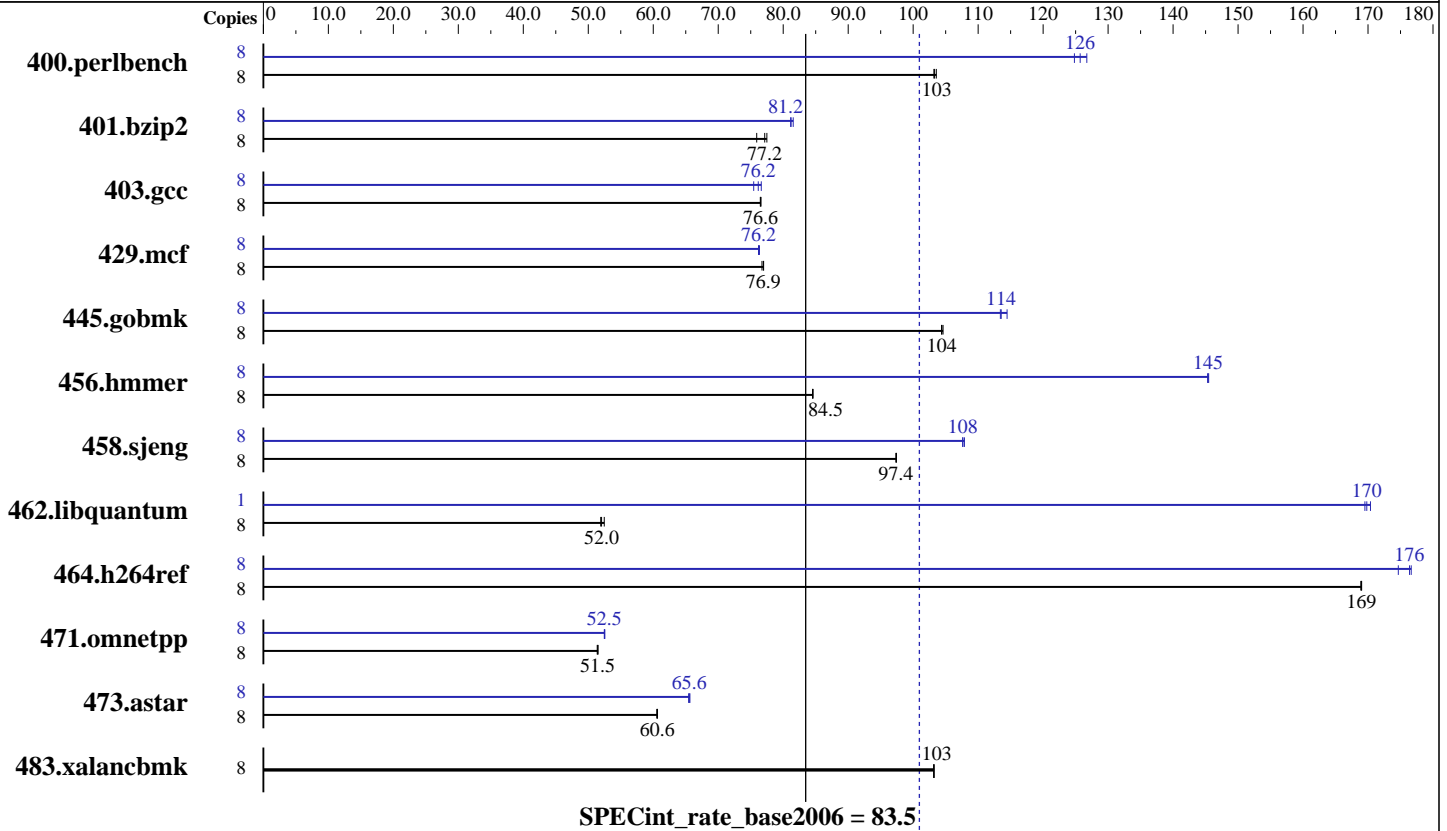
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 2.00 GHz, 2x6 MB L2 shared, 1333 MHz bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	757	103	<u>757</u>	<u>103</u>	755	104	8	626	125	<u>622</u>	<u>126</u>	617	127
401.bzip2	8	1017	75.9	<u>1001</u>	<u>77.2</u>	996	77.5	8	<u>951</u>	<u>81.2</u>	951	81.1	947	81.5
403.gcc	8	842	76.5	<u>841</u>	<u>76.6</u>	841	76.6	8	840	76.6	<u>846</u>	<u>76.2</u>	853	75.5
429.mcf	8	948	77.0	951	76.7	<u>948</u>	<u>76.9</u>	8	956	76.3	<u>957</u>	<u>76.2</u>	958	76.2
445.gobmk	8	802	105	<u>804</u>	<u>104</u>	804	104	8	<u>739</u>	<u>114</u>	740	113	733	114
456.hmmer	8	883	84.5	<u>883</u>	<u>84.5</u>	882	84.6	8	514	145	513	145	<u>513</u>	<u>145</u>
458.sjeng	8	<u>994</u>	<u>97.4</u>	994	97.4	993	97.4	8	899	108	<u>899</u>	<u>108</u>	897	108
462.libquantum	8	3160	52.5	3193	51.9	<u>3185</u>	<u>52.0</u>	1	122	170	122	170	<u>122</u>	<u>170</u>
464.h264ref	8	<u>1048</u>	<u>169</u>	1048	169	1047	169	8	<u>1004</u>	<u>176</u>	1002	177	1014	175
471.omnetpp	8	971	51.5	973	51.4	<u>972</u>	<u>51.5</u>	8	<u>952</u>	<u>52.5</u>	953	52.5	951	52.6
473.astar	8	926	60.7	928	60.5	<u>926</u>	<u>60.6</u>	8	856	65.6	858	65.4	<u>857</u>	<u>65.6</u>
483.xalanbmk	8	535	103	<u>535</u>	<u>103</u>	535	103	8	535	103	<u>535</u>	<u>103</u>	535	103

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default).

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-fast -inline-alloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include`

`456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:

`icpc`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.20090714.xml>



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For other inquiries, please contact webmaster@spec.org.

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