



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 27.6

ProLiant BL480c
(3.16 GHz, Intel Xeon X5460)

SPECint_base2006 = 24.0

CPU2006 license: 3

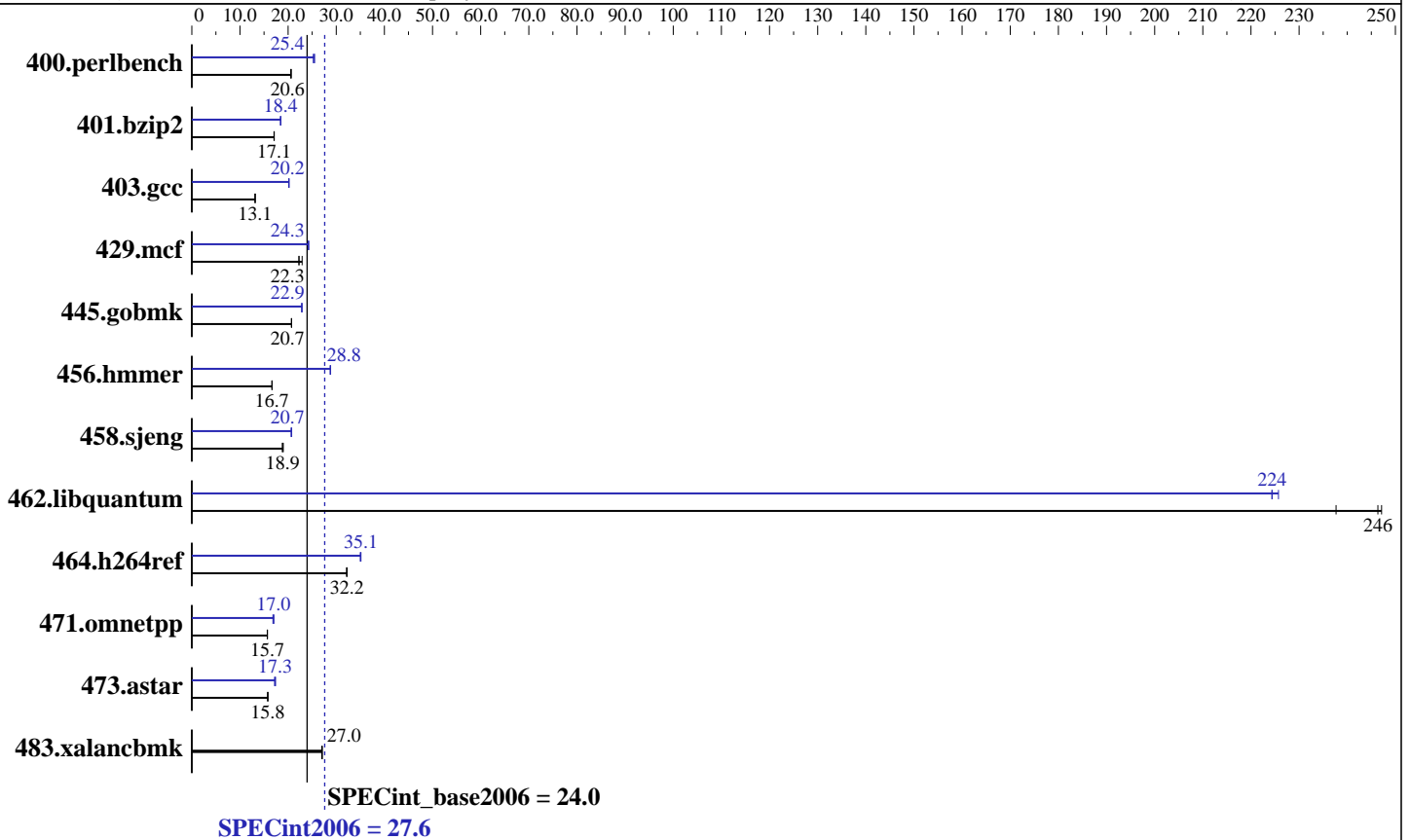
Test date: Jan-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 3.16 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	473	20.6	<u>475</u>	<u>20.6</u>	475	20.6	<u>384</u>	<u>25.4</u>	387	25.2	383	25.5
401.bzip2	565	17.1	<u>564</u>	<u>17.1</u>	564	17.1	522	18.5	524	18.4	<u>523</u>	<u>18.4</u>
403.gcc	613	13.1	<u>612</u>	<u>13.1</u>	612	13.2	400	20.1	399	20.2	<u>399</u>	<u>20.2</u>
429.mcf	398	22.9	<u>409</u>	<u>22.3</u>	410	22.2	376	24.3	<u>376</u>	<u>24.3</u>	378	24.1
445.gobmk	506	20.7	<u>507</u>	<u>20.7</u>	507	20.7	459	22.8	458	22.9	<u>459</u>	<u>22.9</u>
456.hammer	560	16.6	<u>560</u>	<u>16.7</u>	560	16.7	324	28.8	<u>324</u>	<u>28.8</u>	324	28.8
458.sjeng	643	18.8	637	19.0	<u>642</u>	<u>18.9</u>	<u>585</u>	<u>20.7</u>	583	20.7	587	20.6
462.libquantum	87.2	238	<u>84.1</u>	<u>246</u>	83.8	247	91.8	226	92.4	224	<u>92.3</u>	<u>224</u>
464.h264ref	688	32.2	687	32.2	<u>687</u>	<u>32.2</u>	<u>630</u>	<u>35.1</u>	630	35.1	633	35.0
471.omnetpp	<u>398</u>	<u>15.7</u>	399	15.7	398	15.7	367	17.0	<u>368</u>	<u>17.0</u>	371	16.8
473.astar	444	15.8	<u>445</u>	<u>15.8</u>	446	15.7	409	17.2	<u>406</u>	<u>17.3</u>	404	17.4
483.xalancbmk	255	27.1	<u>255</u>	<u>27.0</u>	256	27.0	255	27.1	<u>255</u>	<u>27.0</u>	256	27.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 100M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -opt-malloc-options=3 -parallel
-par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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