SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX300 S4, Intel Xeon X5460, 3.16 GHz

$SPECint^{®}\_rate2006 =$ 137

$SPECint\_rate\_base2006 =$ 111

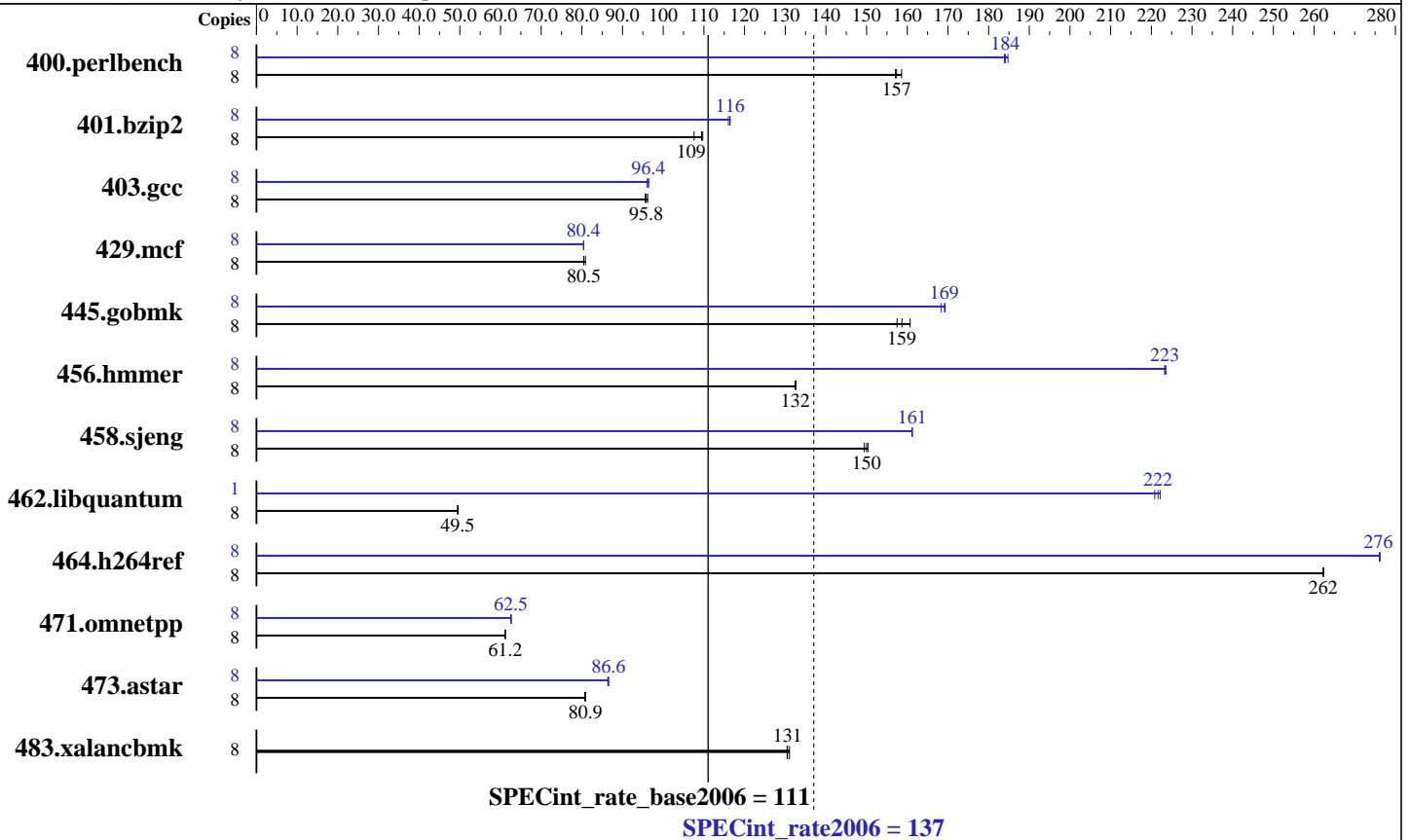**CPU2006 license:** 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007

	Copies		
400.perlbench	8	184	
	8	157	
401.bzip2	8	116	
	8	109	
403.gcc	8	96.4	
	8	95.8	
429.mcf	8	80.4	
	8	80.5	
445.gobmk	8	169	
	8	159	
456.hmmer	8	223	
	8	132	
458.sjeng	8	161	
	8	150	
462.libquantum	1	222	
	8	49.5	
464.h264ref	8	276	
	8	262	
471.omnetpp	8	62.5	
	8	61.2	
473.astar	8	86.6	
	8	80.9	
483.xalancbmk	8	131	

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Hardware

CPU Name: Intel Xeon X5460
CPU Characteristics: 1333 MHz system bus
CPU MHz: 3167
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: Yes
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	498	157	493	159	**497**	**157**	8	423	185	**424**	**184**	425	184
401.bzip2	8	718	108	**706**	**109**	704	110	8	663	116	665	116	**663**	**116**
403.gcc	8	**672**	**95.8**	670	96.2	674	95.6	8	668	96.4	671	96.0	**668**	**96.4**
429.mcf	8	902	80.9	907	80.5	**907**	**80.5**	8	**907**	**80.4**	907	80.4	907	80.4
445.gobmk	8	522	161	**529**	**159**	533	157	8	**496**	**169**	499	168	496	169
456.hmmer	8	564	132	**563**	**132**	563	133	8	334	223	334	224	**334**	**223**
458.sjeng	8	648	149	644	150	**645**	**150**	8	601	161	600	161	**601**	**161**
462.libquantum	8	3349	49.5	3350	49.5	**3349**	**49.5**	1	93.3	222	**93.5**	**222**	93.8	221
464.h264ref	8	675	262	675	262	**675**	**262**	8	641	276	**641**	**276**	641	276
471.omnetpp	8	818	61.2	817	61.2	**818**	**61.2**	8	**800**	**62.5**	798	62.7	800	62.5
473.astar	8	**694**	**80.9**	696	80.7	694	80.9	8	**649**	**86.6**	649	86.6	650	86.4
483.xalancbmk	8	423	130	**423**	**131**	421	131	8	423	130	**423**	**131**	421	131

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'OMP_NUM_THREADS' set to number of cores (default)
```

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
          -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
          -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
          -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
          -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:35:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 February 2008.