



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 82.1

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5260)

SPECint_rate_base2006 = 69.7

CPU2006 license: 3

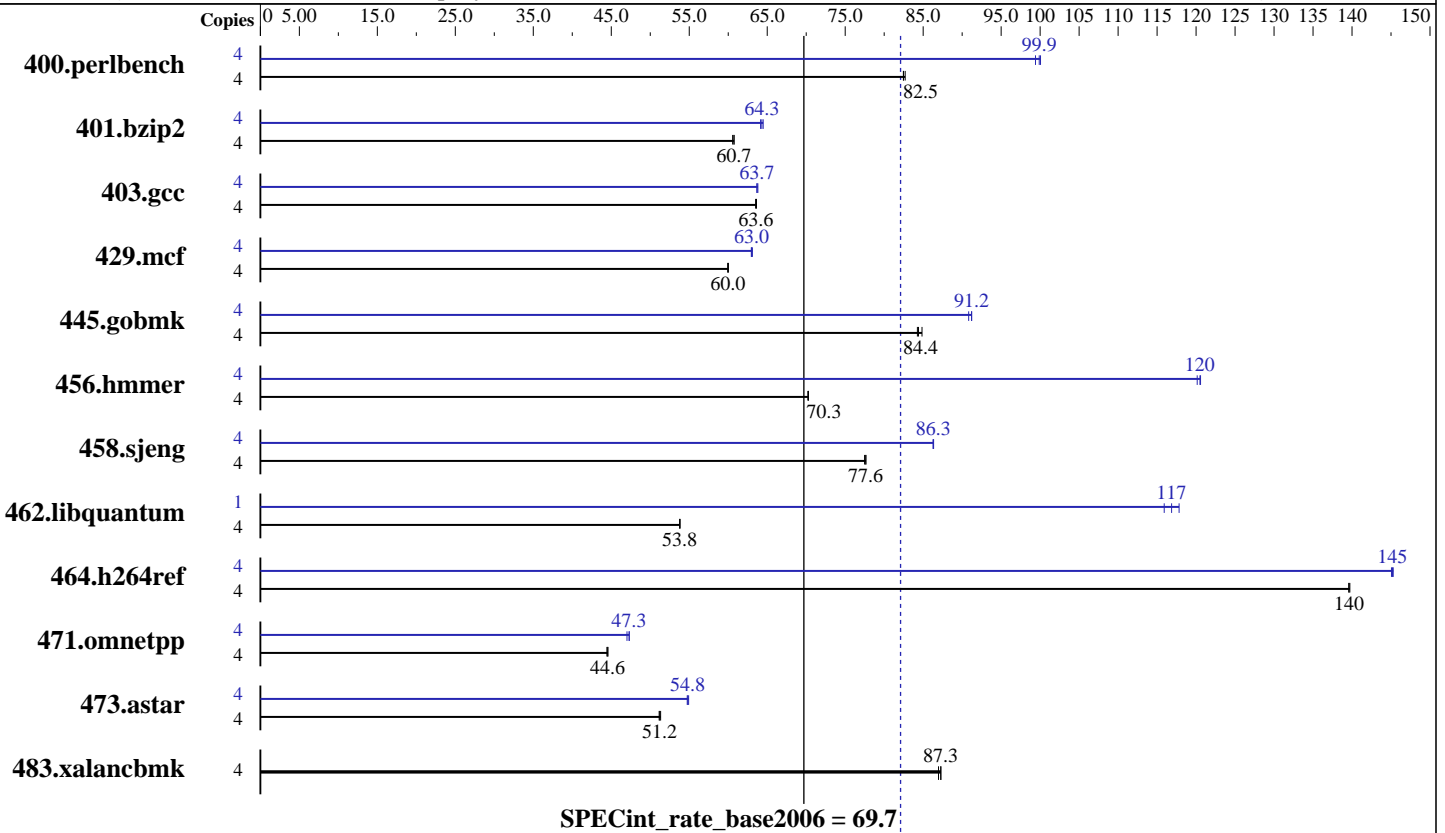
Test date: Jan-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|--------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 474 | 82.5 | <u>474</u> | <u>82.5</u> | 473 | 82.7 | 4 | <u>391</u> | <u>99.9</u> | 393 | 99.4 | 391 | 100 |
| 401.bzip2 | 4 | <u>636</u> | <u>60.7</u> | 637 | 60.6 | 635 | 60.7 | 4 | 599 | 64.5 | <u>600</u> | <u>64.3</u> | 602 | 64.1 |
| 403.gcc | 4 | 507 | 63.5 | <u>506</u> | <u>63.6</u> | 506 | 63.6 | 4 | 506 | 63.7 | <u>506</u> | <u>63.7</u> | 505 | 63.8 |
| 429.mcf | 4 | 608 | 60.0 | 609 | 59.9 | <u>608</u> | <u>60.0</u> | 4 | <u>579</u> | <u>63.0</u> | 578 | 63.1 | 579 | 63.0 |
| 445.gobmk | 4 | 498 | 84.3 | 495 | 84.9 | <u>497</u> | <u>84.4</u> | 4 | 460 | 91.2 | <u>460</u> | <u>91.2</u> | 462 | 90.8 |
| 456.hammer | 4 | 532 | 70.2 | <u>531</u> | <u>70.3</u> | 531 | 70.3 | 4 | 310 | 121 | <u>310</u> | <u>120</u> | 311 | 120 |
| 458.sjeng | 4 | <u>623</u> | <u>77.6</u> | 623 | 77.7 | 625 | 77.5 | 4 | <u>561</u> | <u>86.3</u> | 561 | 86.3 | 561 | 86.3 |
| 462.libquantum | 4 | 1540 | 53.8 | 1541 | 53.8 | <u>1541</u> | <u>53.8</u> | 1 | 179 | 116 | <u>177</u> | <u>117</u> | 176 | 118 |
| 464.h264ref | 4 | 634 | 140 | <u>634</u> | <u>140</u> | 634 | 140 | 4 | 609 | 145 | <u>610</u> | <u>145</u> | 610 | 145 |
| 471.omnetpp | 4 | <u>561</u> | <u>44.6</u> | 561 | 44.6 | 563 | 44.4 | 4 | <u>529</u> | <u>47.3</u> | 532 | 47.0 | 528 | 47.3 |
| 473.astar | 4 | <u>548</u> | <u>51.2</u> | 549 | 51.1 | 547 | 51.3 | 4 | 511 | 54.9 | 513 | 54.7 | <u>512</u> | <u>54.8</u> |
| 483.xalancbmk | 4 | 316 | 87.3 | 317 | 87.0 | <u>316</u> | <u>87.3</u> | 4 | 316 | 87.3 | 317 | 87.0 | <u>316</u> | <u>87.3</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/compiler/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/compiler/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/compiler/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.01.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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