



# SPEC<sup>®</sup> CINT2006 Result

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## Hewlett-Packard Company

SPECint<sup>®</sup>2006 = 27.3

ProLiant DL380 G5  
(3.33 GHz, Intel Xeon X5260)

SPECint\_base2006 = 22.6

CPU2006 license: 3

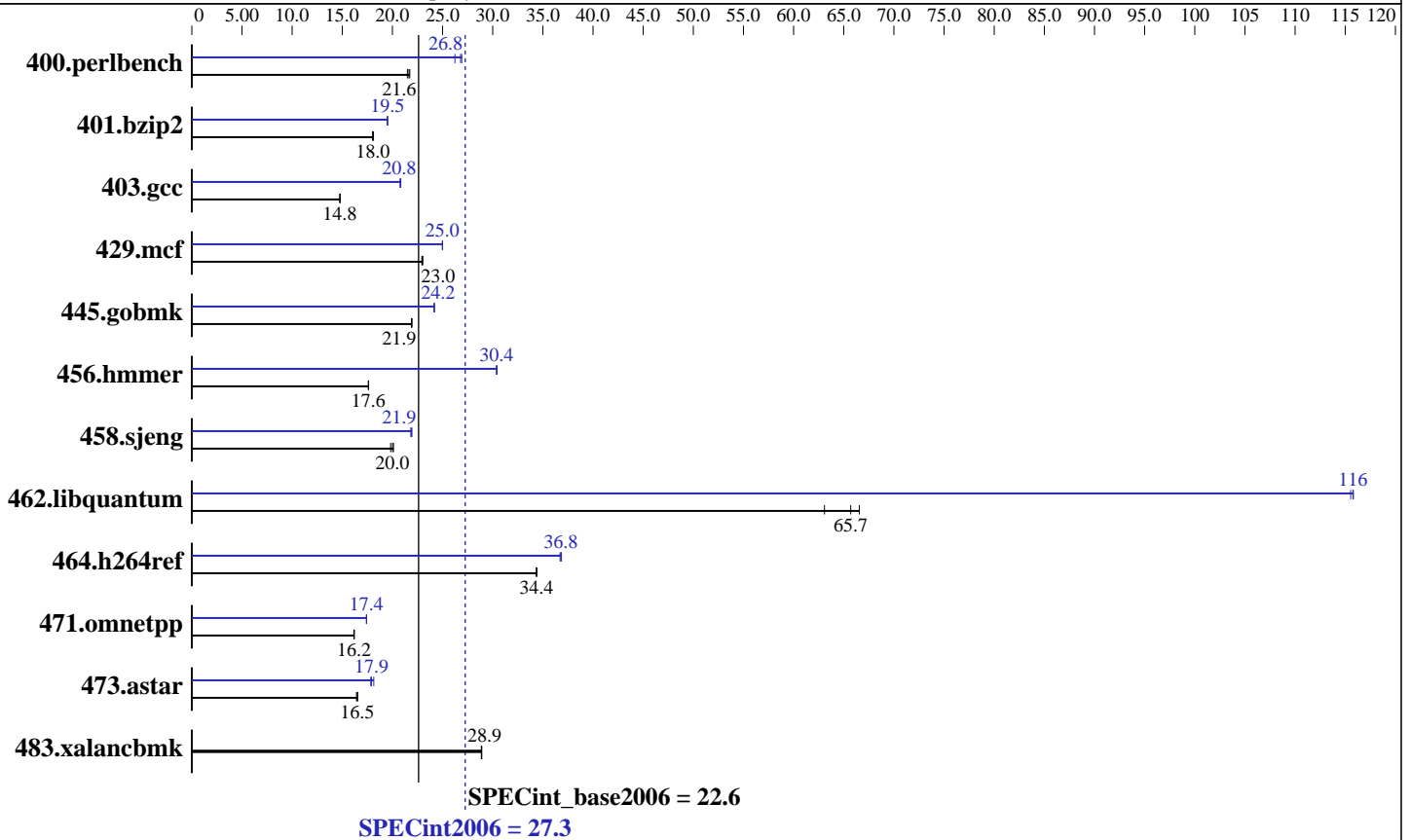
Test date: Jan-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 15 K SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multi-user run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>451</u></b>	<b><u>21.6</u></b>	455	21.5	450	21.7	<b><u>365</u></b>	<b><u>26.8</u></b>	372	26.2	363	26.9
401.bzip2	535	18.0	533	18.1	<b><u>535</u></b>	<b><u>18.0</u></b>	<b><u>495</u></b>	<b><u>19.5</u></b>	495	19.5	494	19.5
403.gcc	545	14.8	<b><u>545</u></b>	<b><u>14.8</u></b>	544	14.8	388	20.8	386	20.8	<b><u>387</u></b>	<b><u>20.8</u></b>
429.mcf	<b><u>397</u></b>	<b><u>23.0</u></b>	396	23.0	398	22.9	<b><u>365</u></b>	<b><u>25.0</u></b>	365	25.0	365	25.0
445.gobmk	<b><u>478</u></b>	<b><u>21.9</u></b>	478	21.9	479	21.9	434	24.1	434	24.2	<b><u>434</u></b>	<b><u>24.2</u></b>
456.hammer	529	17.6	530	17.6	<b><u>530</u></b>	<b><u>17.6</u></b>	<b><u>307</u></b>	<b><u>30.4</u></b>	307	30.4	307	30.4
458.sjeng	610	19.8	602	20.1	<b><u>605</u></b>	<b><u>20.0</u></b>	552	21.9	554	21.8	<b><u>552</u></b>	<b><u>21.9</u></b>
462.libquantum	328	63.1	311	66.6	<b><u>315</u></b>	<b><u>65.7</u></b>	179	116	<b><u>179</u></b>	<b><u>116</u></b>	179	116
464.h264ref	<b><u>644</u></b>	<b><u>34.4</u></b>	644	34.4	644	34.4	603	36.7	<b><u>601</u></b>	<b><u>36.8</u></b>	600	36.9
471.omnetpp	387	16.2	<b><u>387</u></b>	<b><u>16.2</u></b>	386	16.2	<b><u>359</u></b>	<b><u>17.4</u></b>	359	17.4	359	17.4
473.astar	<b><u>426</u></b>	<b><u>16.5</u></b>	425	16.5	428	16.4	<b><u>392</u></b>	<b><u>17.9</u></b>	393	17.8	387	18.1
483.xalancbmk	239	28.9	239	28.9	<b><u>239</u></b>	<b><u>28.9</u></b>	239	28.9	239	28.9	<b><u>239</u></b>	<b><u>28.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -opt-malloc-options=3 -parallel  
-par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbenc: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/cpu2006/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/cpu2006/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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