# SPEC® CINT2006 Result
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## Fujitsu Siemens Computers

PRIMERGY RX100 S5, Intel Core 2 Duo E4500, 2.20 GHz

$SPECint®\_rate2006 =$ 27.9

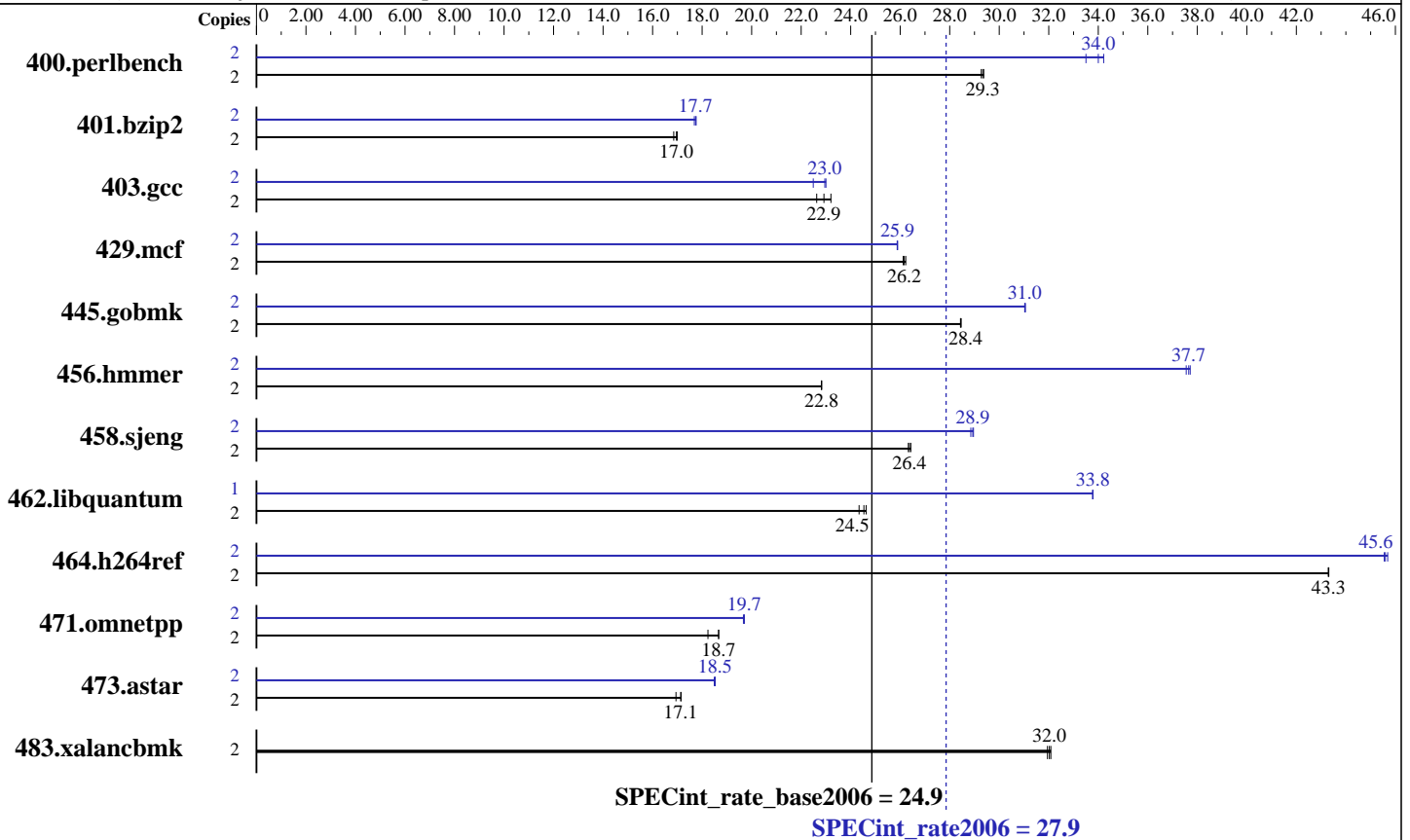$SPECint\_rate\_base2006 =$ 24.9

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** Jan-2008

**Hardware Availability:** Jan-2008

**Software Availability:** Nov-2007

| | Copies | |
|---|---|---|
| 400.perlbench | 2 | 34.0 |
| | 2 | 29.3 |
| 401.bzip2 | 2 | 17.7 |
| | 2 | 17.0 |
| 403.gcc | 2 | 23.0 |
| | 2 | 22.9 |
| 429.mcf | 2 | 25.9 |
| | 2 | 26.2 |
| 445.gobmk | 2 | 31.0 |
| | 2 | 28.4 |
| 456.hmmer | 2 | 37.7 |
| | 2 | 22.8 |
| 458.sjeng | 2 | 28.9 |
| | 2 | 26.4 |
| 462.libquantum | 1 | 33.8 |
| | 2 | 24.5 |
| 464.h264ref | 2 | 45.6 |
| | 2 | 43.3 |
| 471.omnetpp | 2 | 19.7 |
| | 2 | 18.7 |
| 473.astar | 2 | 18.5 |
| | 2 | 17.1 |
| 483.xalancbmk | 2 | 32.0 |

SPECint_rate_base2006 = 24.9

SPECint_rate2006 = 27.9

## Hardware

| | |
|---|---|
| CPU Name: | Intel Core 2 Duo E4500 |
| CPU Characteristics: | 800 MHz system bus |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 2 MB I+D on chip per chip |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 8 GB (4x2 GB PC2-6400E, 2 rank, CAS 6-6-6, with ECC) |
| Disk Subsystem: | Western Digital WD5000AAKS (SATA, 500GB, 7200rpm) |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp |
| Compiler: | Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725 |
| Auto Parallel: | Yes |
| File System: | ext2 |
| System State: | Multiuser, Runlevel 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17 |

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## Results Table

| Benchmark | Base Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Peak Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 2 | **666** | **29.3** | 667 | 29.3 | 665 | 29.4 | 2 | **575** | **34.0** | 571 | 34.2 | 583 | 33.5 |
| 401.bzip2 | 2 | 1135 | 17.0 | **1138** | **17.0** | 1145 | 16.9 | 2 | 1091 | 17.7 | **1088** | **17.7** | 1087 | 17.8 |
| 403.gcc | 2 | **702** | **22.9** | 694 | 23.2 | 712 | 22.6 | 2 | 700 | 23.0 | 716 | 22.5 | **701** | **23.0** |
| 429.mcf | 2 | 698 | 26.1 | **697** | **26.2** | 696 | 26.2 | 2 | **705** | **25.9** | 705 | 25.9 | 704 | 25.9 |
| 445.gobmk | 2 | 737 | 28.5 | 738 | 28.4 | **737** | **28.4** | 2 | 676 | 31.0 | 676 | 31.1 | **676** | **31.0** |
| 456.hmmer | 2 | 817 | 22.8 | **818** | **22.8** | 818 | 22.8 | 2 | 495 | 37.7 | 497 | 37.6 | **496** | **37.7** |
| 458.sjeng | 2 | 919 | 26.3 | 916 | 26.4 | **918** | **26.4** | 2 | 836 | 29.0 | 839 | 28.9 | **836** | **28.9** |
| 462.libquantum | 2 | **1689** | **24.5** | 1683 | 24.6 | 1703 | 24.3 | 1 | 613 | 33.8 | 613 | 33.8 | **613** | **33.8** |
| 464.h264ref | 2 | **1022** | **43.3** | 1022 | 43.3 | 1022 | 43.3 | 2 | 969 | 45.7 | 972 | 45.5 | **971** | **45.6** |
| 471.omnetpp | 2 | **670** | **18.7** | 685 | 18.2 | 669 | 18.7 | 2 | 634 | 19.7 | **634** | **19.7** | 636 | 19.7 |
| 473.astar | 2 | 829 | 16.9 | 819 | 17.2 | **819** | **17.1** | 2 | **758** | **18.5** | 758 | 18.5 | 759 | 18.5 |
| 483.xalancbmk | 2 | 430 | 32.1 | **431** | **32.0** | 432 | 31.9 | 2 | 430 | 32.1 | **431** | **32.0** | 432 | 31.9 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'OMP_NUM_THREADS' set to number of cores (default)
```

## General Notes

```
This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com
```

## Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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## Base Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX
     483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
     -fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
     -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
     -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
         403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
     icc
```

```
      401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
               -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
               -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

      456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
               -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
               -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
     icpc
```

## Peak Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
         401.bzip2: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LINUX
```

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# Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

# Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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