



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/120Bb-6  
(Intel Xeon X5260)

SPECint®2006 = 27.1

SPECint\_base2006 = 22.6

CPU2006 license: 9006

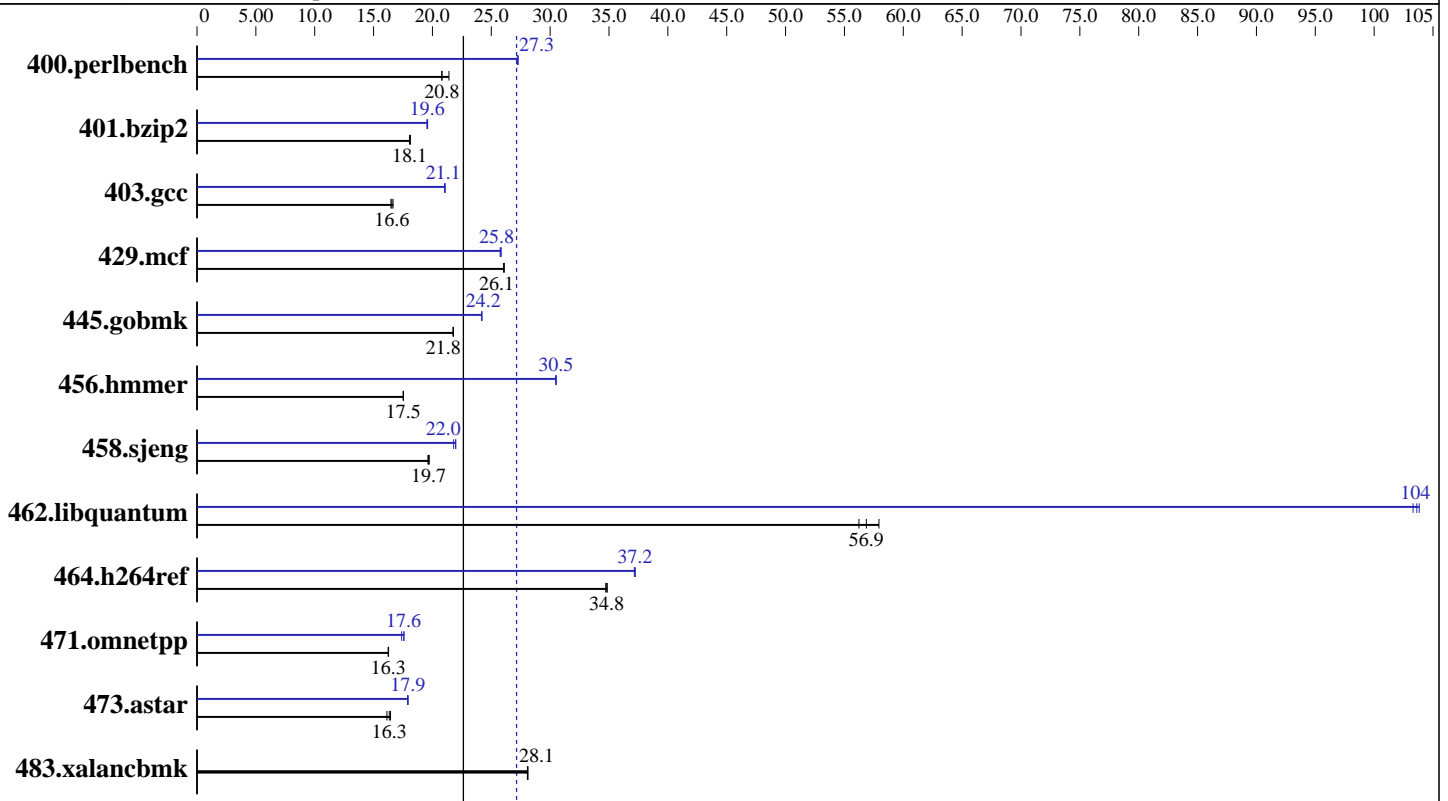
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 3.33 GHz, 6 MB L2, 1333 MHz bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>469</u></b>	<b><u>20.8</u></b>	470	20.8	456	21.4	358	27.3	359	27.2	<b><u>358</u></b>	<b><u>27.3</u></b>
401.bzip2	532	18.1	534	18.1	<b><u>534</u></b>	<b><u>18.1</u></b>	<b><u>493</u></b>	<b><u>19.6</u></b>	494	19.5	492	19.6
403.gcc	483	16.7	489	16.5	<b><u>486</u></b>	<b><u>16.6</u></b>	<b><u>382</u></b>	<b><u>21.1</u></b>	383	21.0	382	21.1
429.mcf	350	26.0	<b><u>350</u></b>	<b><u>26.1</u></b>	349	26.1	<b><u>353</u></b>	<b><u>25.8</u></b>	354	25.8	353	25.8
445.gobmk	<b><u>482</u></b>	<b><u>21.8</u></b>	482	21.8	482	21.8	434	24.2	<b><u>434</u></b>	<b><u>24.2</u></b>	434	24.2
456.hmmmer	<b><u>532</u></b>	<b><u>17.5</u></b>	533	17.5	532	17.5	306	30.5	306	30.5	<b><u>306</u></b>	<b><u>30.5</u></b>
458.sjeng	617	19.6	613	19.7	<b><u>615</u></b>	<b><u>19.7</u></b>	550	22.0	555	21.8	<b><u>551</u></b>	<b><u>22.0</u></b>
462.libquantum	358	57.9	<b><u>364</u></b>	<b><u>56.9</u></b>	368	56.2	<b><u>200</u></b>	<b><u>104</u></b>	201	103	200	104
464.h264ref	637	34.7	<b><u>636</u></b>	<b><u>34.8</u></b>	635	34.9	594	37.2	<b><u>595</u></b>	<b><u>37.2</u></b>	596	37.2
471.omnetpp	<b><u>384</u></b>	<b><u>16.3</u></b>	384	16.3	385	16.2	360	17.4	<b><u>356</u></b>	<b><u>17.6</u></b>	355	17.6
473.astar	427	16.4	<b><u>430</u></b>	<b><u>16.3</u></b>	435	16.1	393	17.9	392	17.9	<b><u>392</u></b>	<b><u>17.9</u></b>
483.xalancbmk	<b><u>246</u></b>	<b><u>28.1</u></b>	245	28.1	246	28.1	<b><u>246</u></b>	<b><u>28.1</u></b>	245	28.1	246	28.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,  
for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-fast -vec-guard-write -parallel -par-runtime-control`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap_8.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include`

`456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:

`icpc`

## Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.00.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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