



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 51.9

### CELSIUS R650, Intel Xeon E5205, 1.87 GHz

### SPECint\_rate\_base2006 = 44.7

CPU2006 license: 22

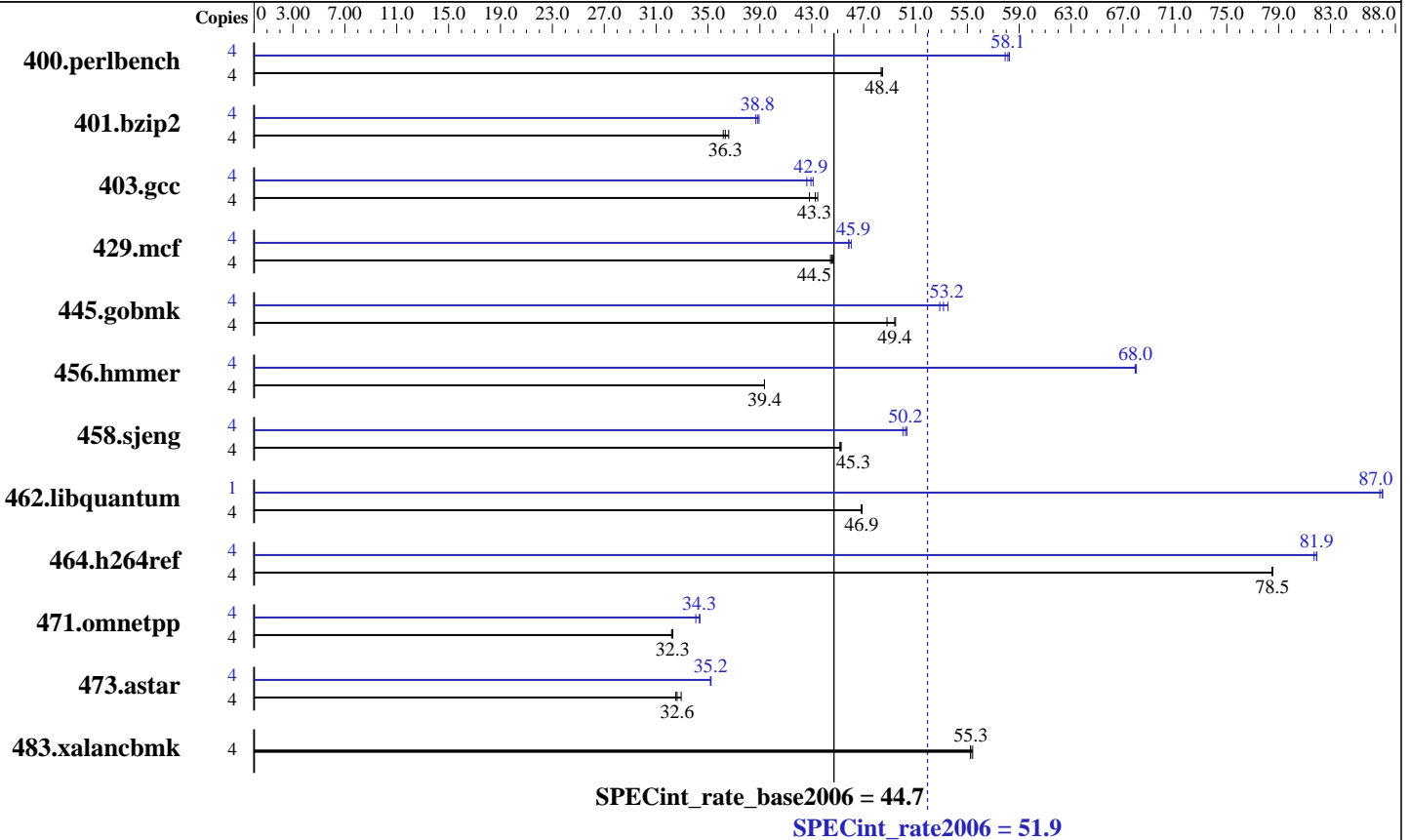
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5205  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User, Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86\_64



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>807</b>	<b>48.4</b>	807	48.4	808	48.3	4	<b>672</b>	<b>58.1</b>	671	58.2	675	57.9
401.bzip2	4	1067	36.2	1055	36.6	<b>1062</b>	<b>36.3</b>	4	<b>994</b>	<b>38.8</b>	998	38.7	991	38.9
403.gcc	4	741	43.5	<b>744</b>	<b>43.3</b>	752	42.8	4	<b>750</b>	<b>42.9</b>	755	42.6	747	43.1
429.mcf	4	<b>819</b>	<b>44.5</b>	817	44.6	821	44.4	4	<b>795</b>	<b>45.9</b>	792	46.0	796	45.8
445.gobmk	4	848	49.5	860	48.8	<b>849</b>	<b>49.4</b>	4	784	53.5	794	52.9	<b>789</b>	<b>53.2</b>
456.hmmmer	4	948	39.4	948	39.4	<b>948</b>	<b>39.4</b>	4	<b>549</b>	<b>68.0</b>	549	67.9	549	68.0
458.sjeng	4	<b>1070</b>	<b>45.3</b>	1072	45.2	1069	45.3	4	968	50.0	962	50.3	<b>964</b>	<b>50.2</b>
462.libquantum	4	1770	46.8	1768	46.9	<b>1769</b>	<b>46.9</b>	1	<b>238</b>	<b>87.0</b>	238	87.0	239	86.8
464.h264ref	4	1127	78.5	1128	78.5	<b>1127</b>	<b>78.5</b>	4	<b>1080</b>	<b>81.9</b>	1083	81.7	1080	81.9
471.omnetpp	4	<b>775</b>	<b>32.3</b>	776	32.2	774	32.3	4	<b>728</b>	<b>34.3</b>	734	34.1	727	34.4
473.astar	4	<b>861</b>	<b>32.6</b>	853	32.9	864	32.5	4	798	35.2	<b>797</b>	<b>35.2</b>	797	35.2
483.xalancbmk	4	500	55.2	498	55.4	<b>499</b>	<b>55.3</b>	4	500	55.2	498	55.4	<b>499</b>	<b>55.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores (default)

## Platform Notes

BIOS configuration:  
Enhanced Speedstep Technology = Disable  
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable  
SnoopFilter = Enable

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

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## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
 -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
 -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
 -opt-streaming-stores always -vec-guard-write  
 -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
 -Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.21.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.21.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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