



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint[®]_rate2006 = 74.7

PRIMERGY RX100 S5, Intel Xeon X3360, 2.83 GHz

SPECint_rate_base2006 = 64.0

CPU2006 license: 22

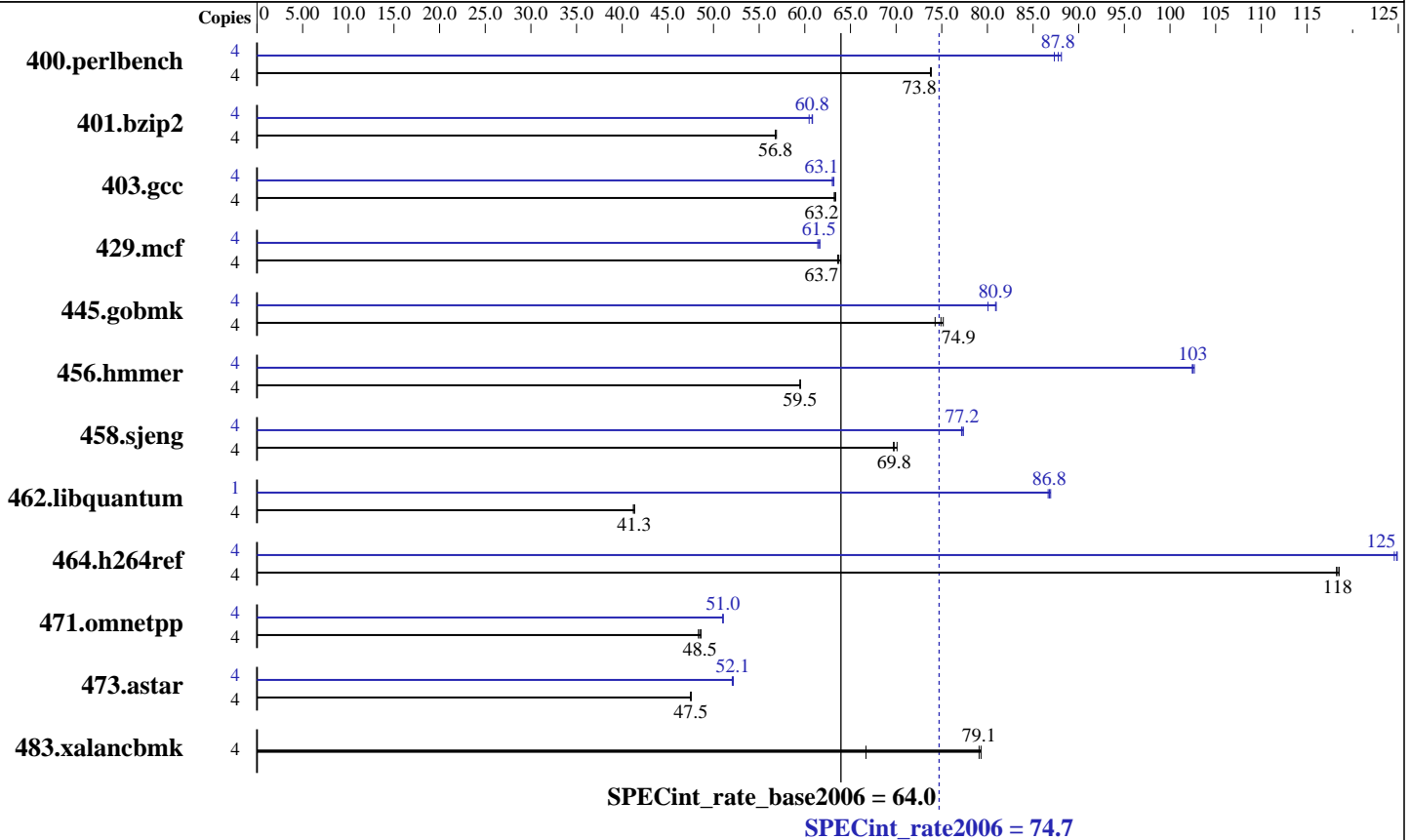
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3360
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CAS 6-6-6, with ECC)
 Disk Subsystem: Western Digital WD5000AAKS (SATA, 500GB, 7200rpm)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.7

PRIMERGY RX100 S5, Intel Xeon X3360, 2.83 GHz

SPECint_rate_base2006 = 64.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>529</u>	<u>73.8</u>	530	73.8	529	73.8	4	444	88.1	448	87.3	<u>445</u>	<u>87.8</u>
401.bzip2	4	678	56.9	<u>680</u>	<u>56.8</u>	680	56.8	4	634	60.8	638	60.5	<u>635</u>	<u>60.8</u>
403.gcc	4	<u>509</u>	<u>63.2</u>	508	63.4	509	63.2	4	510	63.2	<u>510</u>	<u>63.1</u>	511	63.0
429.mcf	4	571	63.9	<u>573</u>	<u>63.7</u>	574	63.6	4	<u>593</u>	<u>61.5</u>	594	61.4	592	61.7
445.gobmk	4	565	74.3	<u>560</u>	<u>74.9</u>	558	75.2	4	<u>519</u>	<u>80.9</u>	524	80.1	518	81.0
456.hmmmer	4	627	59.5	627	59.5	<u>627</u>	<u>59.5</u>	4	364	102	364	103	<u>364</u>	<u>103</u>
458.sjeng	4	<u>694</u>	<u>69.8</u>	690	70.1	694	69.7	4	626	77.4	627	77.2	<u>627</u>	<u>77.2</u>
462.libquantum	4	2011	41.2	2005	41.3	<u>2005</u>	<u>41.3</u>	1	<u>239</u>	<u>86.8</u>	238	86.9	239	86.6
464.h264ref	4	747	119	749	118	<u>748</u>	<u>118</u>	4	709	125	711	125	<u>709</u>	<u>125</u>
471.omnetpp	4	517	48.3	514	48.6	<u>515</u>	<u>48.5</u>	4	490	51.0	<u>490</u>	<u>51.0</u>	490	51.0
473.astar	4	<u>591</u>	<u>47.5</u>	591	47.5	591	47.5	4	538	52.2	<u>539</u>	<u>52.1</u>	539	52.1
483.xalancbmk	4	348	79.3	<u>349</u>	<u>79.1</u>	414	66.7	4	348	79.3	<u>349</u>	<u>79.1</u>	414	66.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'OMP_NUM_THREADS' set to number of cores (default)

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.7

PRIMERGY RX100 S5, Intel Xeon X3360, 2.83 GHz

SPECint_rate_base2006 = 64.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.7

PRIMERGY RX100 S5, Intel Xeon X3360, 2.83 GHz

SPECint_rate_base2006 = 64.0

CPU2006 license: 22

Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007

Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

483.xalanbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 74.7

PRIMERGY RX100 S5, Intel Xeon X3360, 2.83 GHz

SPECint_rate_base2006 = 64.0

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:42:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 March 2008.