



SPEC® CINT2006 Result

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Sun Microsystems Sun Fire X4150

SPECint®2006 = 27.1
SPECint_base2006 = 22.7

CPU2006 license: 6

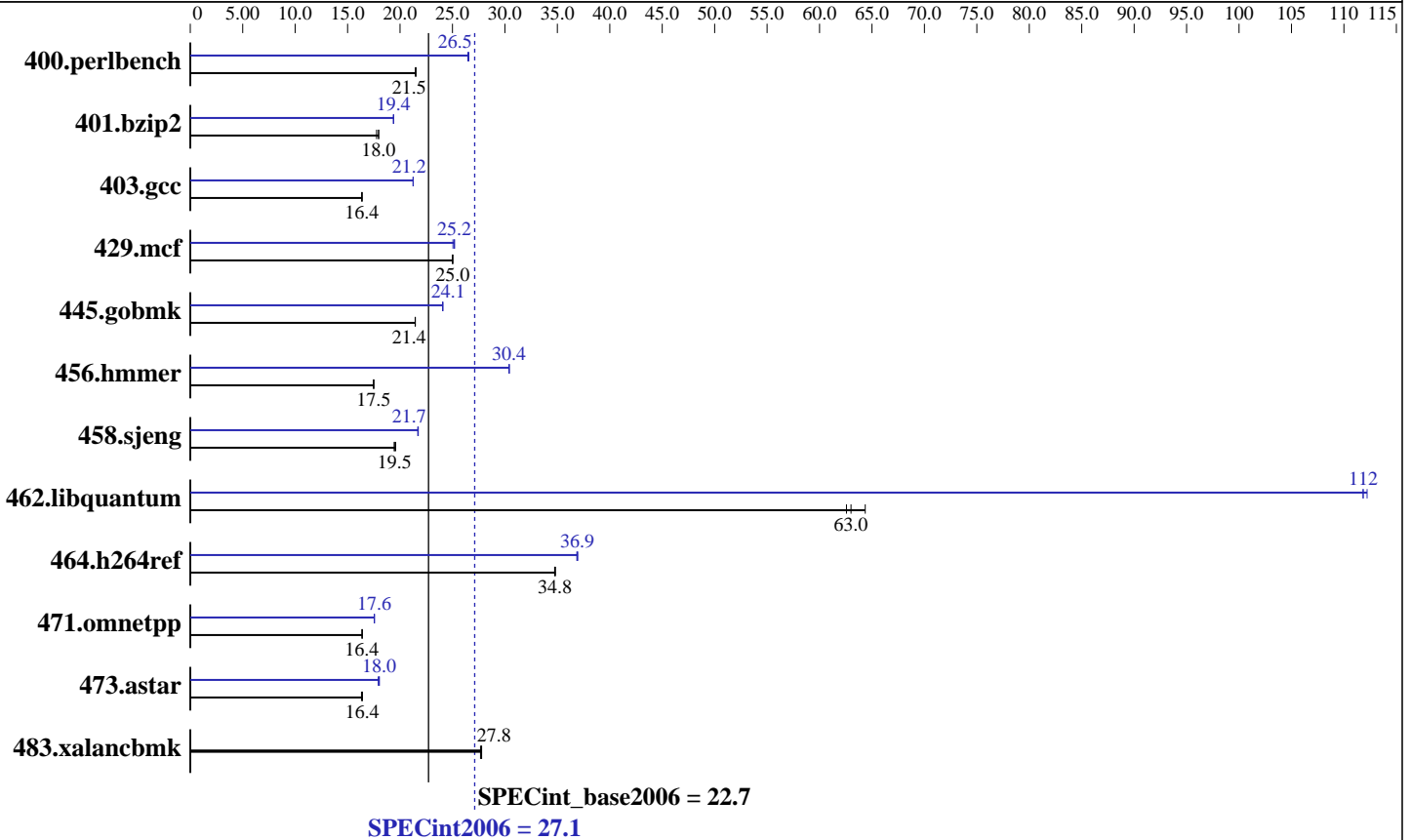
Test sponsor: Sun Microsystems

Tested by: Sun Microsystems

Test date: Dec-2007

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 (order by number of chips)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2GB DDR2 PC2-5300F 2rank CAS 5-5-5 with ECC)
 Disk Subsystem: SAS, 72 GB, 10K RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 SP1 for x86_64
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1
 Build 20070824
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	454	21.5	<u>454</u>	<u>21.5</u>	455	21.5	<u>368</u>	<u>26.5</u>	369	26.5	368	26.6
401.bzip2	543	17.8	<u>537</u>	<u>18.0</u>	537	18.0	<u>498</u>	<u>19.4</u>	498	19.4	498	19.4
403.gcc	493	16.3	491	16.4	<u>491</u>	<u>16.4</u>	379	21.3	379	21.2	<u>379</u>	<u>21.2</u>
429.mcf	<u>364</u>	<u>25.0</u>	365	25.0	364	25.0	364	25.0	<u>362</u>	<u>25.2</u>	362	25.2
445.gobmk	489	21.4	489	21.5	<u>489</u>	<u>21.4</u>	436	24.1	<u>436</u>	<u>24.1</u>	435	24.1
456.hmmmer	<u>533</u>	<u>17.5</u>	534	17.5	533	17.5	307	30.4	307	30.4	<u>307</u>	<u>30.4</u>
458.sjeng	<u>621</u>	<u>19.5</u>	618	19.6	622	19.5	558	21.7	<u>557</u>	<u>21.7</u>	557	21.7
462.libquantum	322	64.4	331	62.6	<u>329</u>	<u>63.0</u>	185	112	<u>185</u>	<u>112</u>	185	112
464.h264ref	637	34.7	<u>636</u>	<u>34.8</u>	636	34.8	<u>600</u>	<u>36.9</u>	599	36.9	600	36.9
471.omnetpp	382	16.4	<u>382</u>	<u>16.4</u>	381	16.4	<u>356</u>	<u>17.6</u>	356	17.6	356	17.5
473.astar	<u>428</u>	<u>16.4</u>	428	16.4	430	16.3	392	17.9	389	18.0	<u>390</u>	<u>18.0</u>
483.xalancbmk	248	27.8	<u>249</u>	<u>27.8</u>	249	27.7	248	27.8	<u>249</u>	<u>27.8</u>	249	27.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to 4
KMP_AFFINITY set to physical,0

Platform Notes

Default BIOS configurations were used.

General Notes

All benchmarks were compiled in 32-bit mode except 401.bzip2 and 456.hmmmer for peak were compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include
-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include
-fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.25.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.25.xml>

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For other inquiries, please contact webmaster@spec.org.

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