



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon 5148 LV)

SPECint®2006 = 19.2

SPECint_base2006 = 16.5

CPU2006 license: 9006

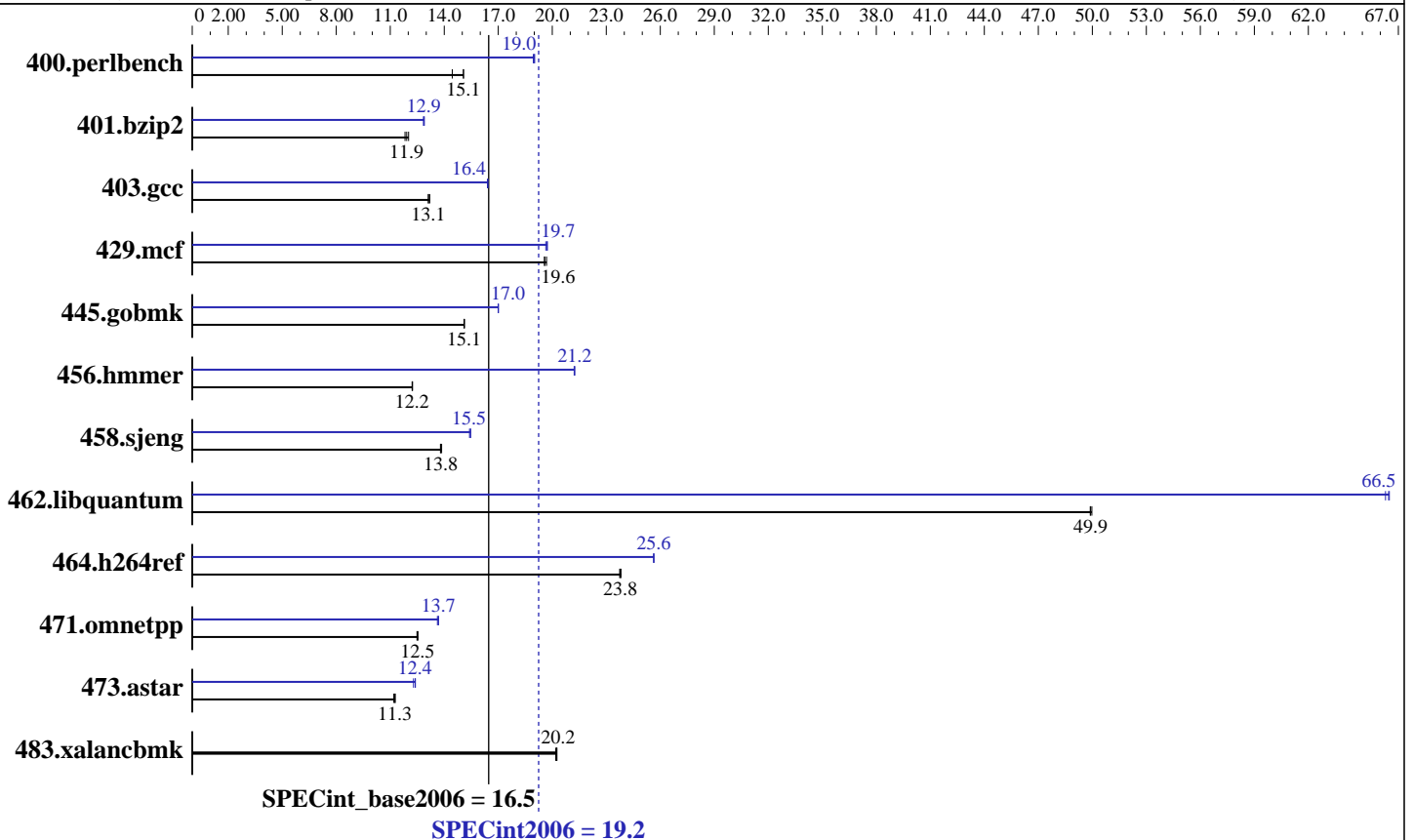
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: May-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5148 LV
 CPU Characteristics: 2.33 GHz, 4 MB L2, 1333 MHz bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: I_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon 5148 LV)

SPECint2006 = 19.2

SPECint_base2006 = 16.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: May-2007

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	648	15.1	676	14.4	<u>649</u>	<u>15.1</u>	516	18.9	514	19.0	<u>514</u>	<u>19.0</u>
401.bzip2	803	12.0	816	11.8	<u>810</u>	<u>11.9</u>	748	12.9	751	12.8	<u>750</u>	<u>12.9</u>
403.gcc	614	13.1	610	13.2	<u>613</u>	<u>13.1</u>	491	16.4	490	16.4	<u>490</u>	<u>16.4</u>
429.mcf	463	19.7	466	19.6	<u>466</u>	<u>19.6</u>	<u>463</u>	<u>19.7</u>	462	19.7	464	19.6
445.gobmk	694	15.1	<u>694</u>	<u>15.1</u>	694	15.1	<u>617</u>	<u>17.0</u>	617	17.0	617	17.0
456.hmmmer	764	12.2	<u>763</u>	<u>12.2</u>	762	12.2	440	21.2	439	21.2	<u>439</u>	<u>21.2</u>
458.sjeng	878	13.8	<u>876</u>	<u>13.8</u>	873	13.9	<u>783</u>	<u>15.5</u>	786	15.4	783	15.5
462.libquantum	415	50.0	415	49.9	<u>415</u>	<u>49.9</u>	312	66.5	313	66.3	<u>312</u>	<u>66.5</u>
464.h264ref	<u>931</u>	<u>23.8</u>	929	23.8	932	23.7	<u>863</u>	<u>25.6</u>	863	25.6	863	25.6
471.omnetpp	<u>499</u>	<u>12.5</u>	499	12.5	501	12.5	<u>457</u>	<u>13.7</u>	457	13.7	459	13.6
473.astar	622	11.3	627	11.2	<u>624</u>	<u>11.3</u>	566	12.4	<u>567</u>	<u>12.4</u>	571	12.3
483.xalancbmk	341	20.3	<u>341</u>	<u>20.2</u>	342	20.2	341	20.3	<u>341</u>	<u>20.2</u>	342	20.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,
for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon 5148 LV)

SPECint2006 = 19.2

SPECint_base2006 = 16.5

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: May-2007
Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon 5148 LV)

SPECint2006 = 19.2

SPECint_base2006 = 16.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: May-2007

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon 5148 LV)

SPECint2006 = 19.2

SPECint_base2006 = 16.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: May-2007

Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:34:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 March 2008.