



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = Not Run

Servidor Itaotec LX211 (Intel Xeon E5440)

SPECint_rate_base2006 = 98.3

CPU2006 license: 9001

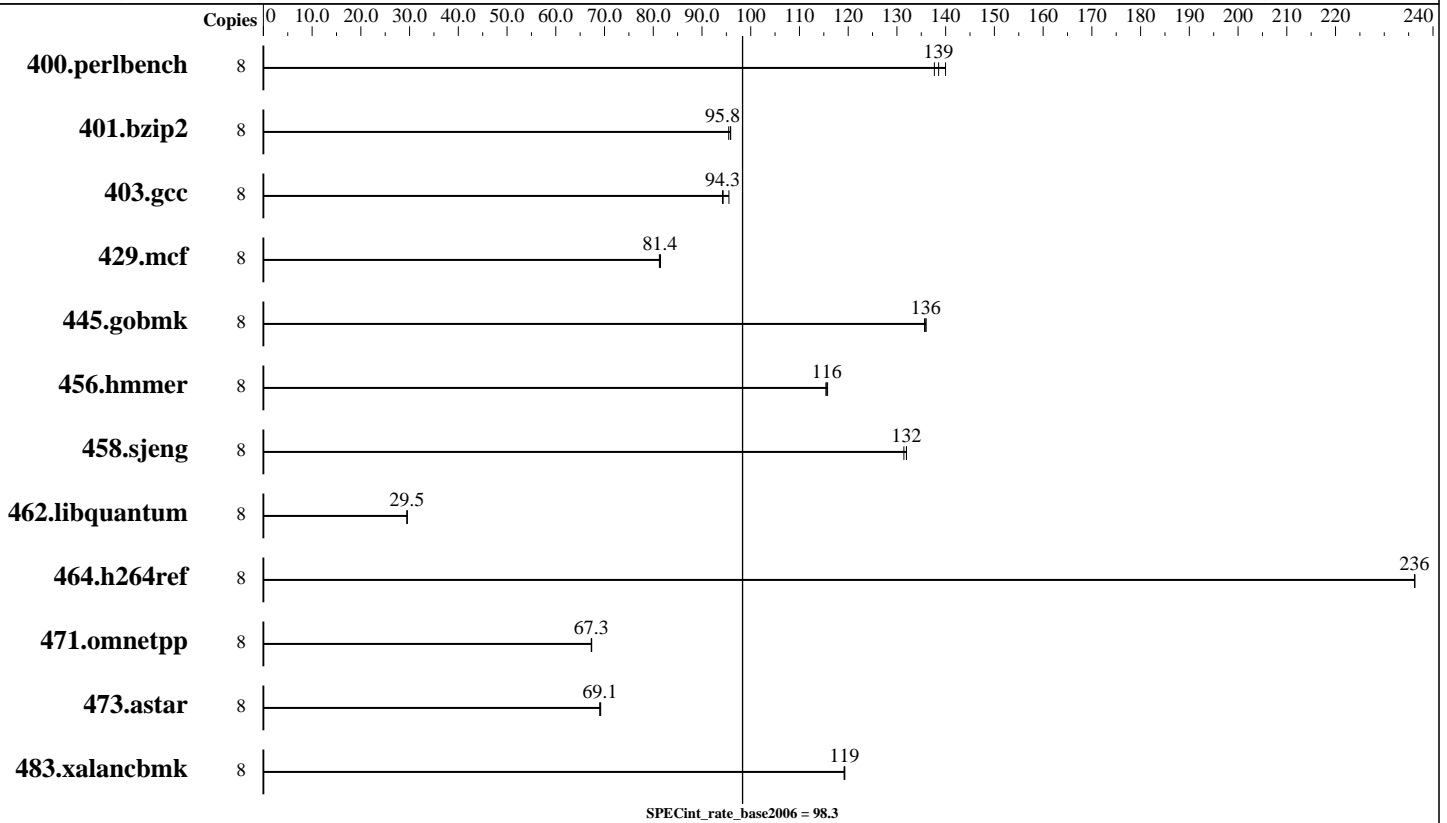
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Feb-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics:
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (6 * 2GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1 x SATA, 200GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.50.0.15
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	558	140	564	139	568	138							
401.bzip2	8	806	95.8	805	95.9	809	95.5							
403.gcc	8	674	95.5	684	94.2	683	94.3							
429.mcf	8	895	81.5	896	81.4	898	81.3							
445.gobmk	8	617	136	617	136	619	136							
456.hammer	8	647	115	646	116	645	116							
458.sjeng	8	734	132	737	131	734	132							
462.libquantum	8	5620	29.5	5623	29.5	5621	29.5							
464.h264ref	8	750	236	749	236	749	236							
471.omnetpp	8	743	67.3	742	67.4	743	67.3							
473.astar	8	811	69.2	813	69.1	813	69.0							
483.xalancbmk	8	463	119	463	119	463	119							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetcher Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs -L/CPU2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic10-ia32-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic10-ia32-intel64-linux-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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