SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant BL460c
(2.83 GHz, Intel Xeon E5440)

SPECint®_rate2006 = 129
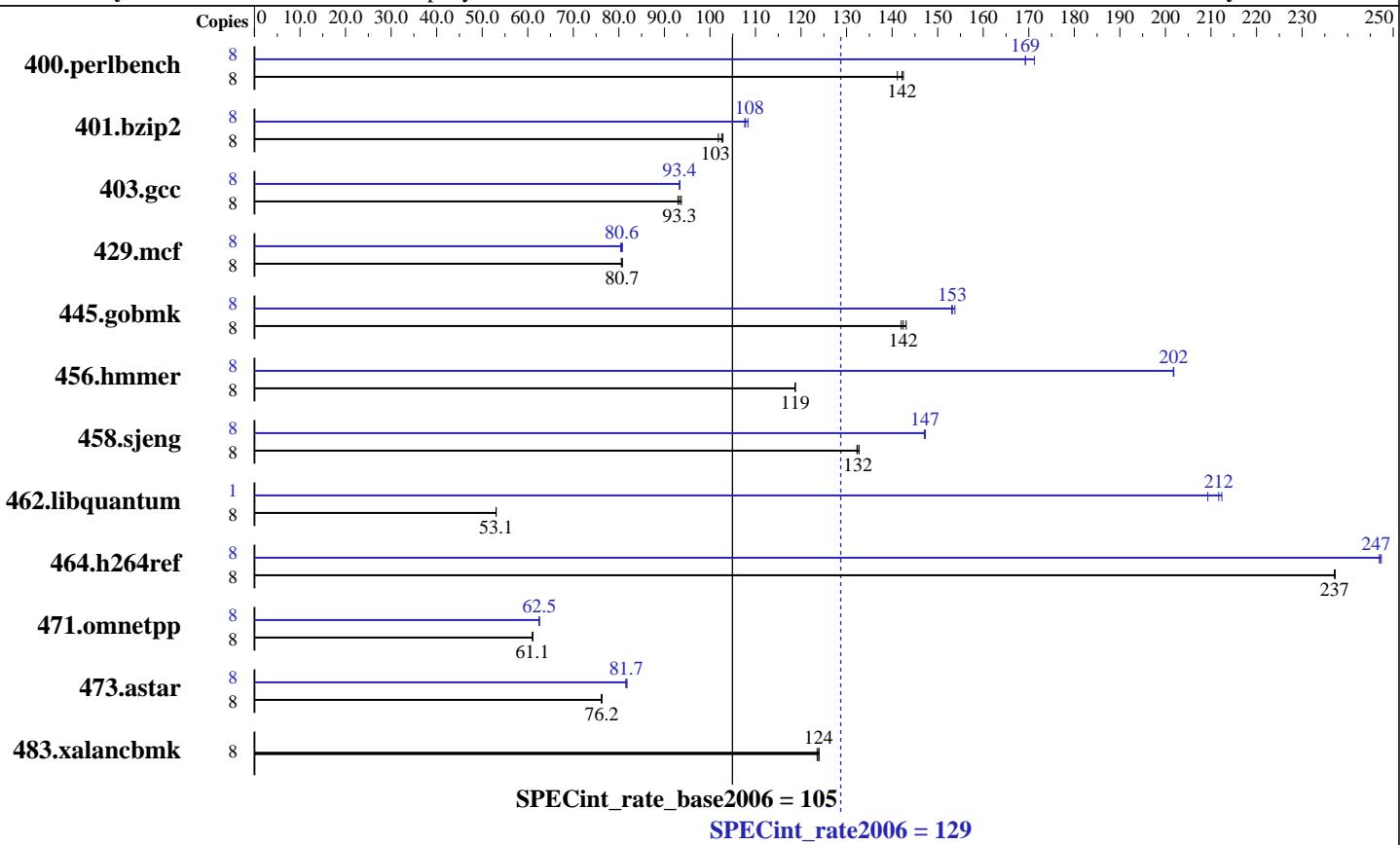
SPECint_rate_base2006 = 105

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Benchmark	Copies	Result
400.perlbench	8	169
	8	142
401.bzip2	8	108
	8	103
403.gcc	8	93.4
	8	93.3
429.mcf	8	80.6
	8	80.7
445.gobmk	8	153
	8	142
456.hmmer	8	202
	8	119
458.sjeng	8	147
	8	132
462.libquantum	1	212
	8	53.1
464.h264ref	8	247
	8	237
471.omnetpp	8	62.5
	8	61.1
473.astar	8	81.7
	8	76.2
483.xalancbmk	8	124

SPECint_rate_base2006 = 105

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Hardware

CPU Name: Intel Xeon E5440
CPU Characteristics: 2.83 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 2833
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	549	142	554	141	**550**	**142**	8	462	169	**462**	**169**	456	171
401.bzip2	8	758	102	**753**	**103**	751	103	8	717	108	**716**	**108**	713	108
403.gcc	8	693	93.0	687	93.7	**690**	**93.3**	8	690	93.4	690	93.3	**690**	**93.4**
429.mcf	8	906	80.5	**904**	**80.7**	903	80.8	8	903	80.8	907	80.4	**905**	**80.6**
445.gobmk	8	**589**	**142**	587	143	591	142	8	**548**	**153**	548	153	546	154
456.hmmer	8	629	119	629	119	**629**	**119**	8	**370**	**202**	370	202	370	202
458.sjeng	8	732	132	**731**	**132**	729	133	8	658	147	**658**	**147**	657	147
462.libquantum	8	3126	53.0	3124	53.1	**3124**	**53.1**	1	99.0	209	97.6	212	**97.9**	**212**
464.h264ref	8	746	237	**747**	**237**	747	237	8	716	247	717	247	**717**	**247**
471.omnetpp	8	819	61.1	**819**	**61.1**	819	61.0	8	**800**	**62.5**	799	62.6	800	62.5
473.astar	8	**737**	**76.2**	736	76.3	737	76.2	8	**688**	**81.7**	689	81.5	687	81.8
483.xalancbmk	8	447	124	**446**	**124**	445	124	8	447	124	**446**	**124**	445	124

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
   Power Regulator set to Static High Performance Mode
   Adjacent Sector Prefetch Disabled
   Hardware Prefetcher Enabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:22:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 March 2008.